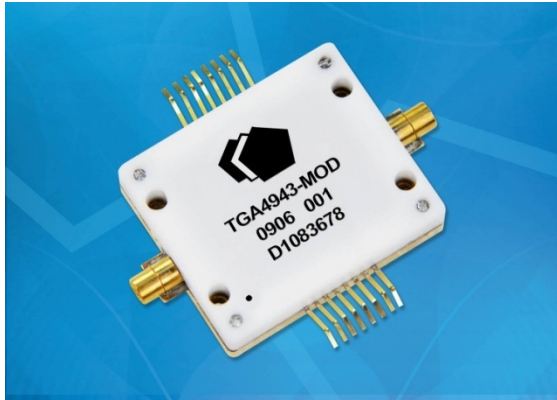


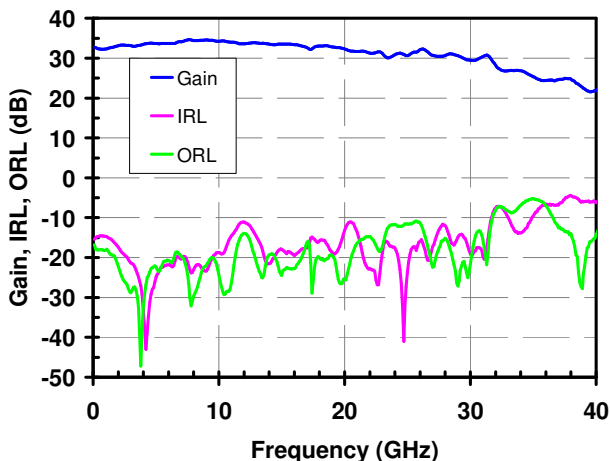
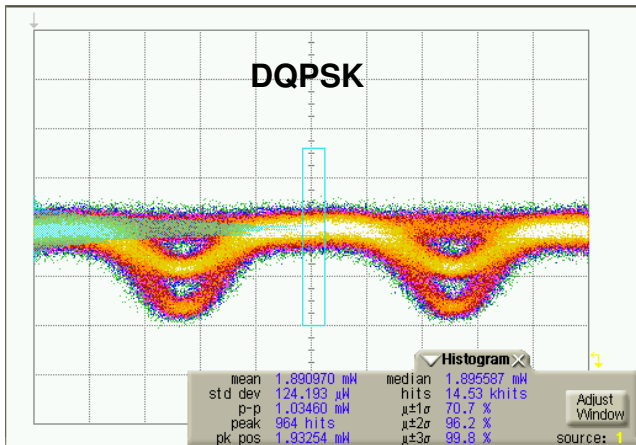
**40 & 100 Gb/s 8Vpp Optical Modulator Driver Module**



**Measured Performance**

Bias conditions:  $V_{d1,2,3} = 7\text{ V}$ ,  $I_{dq} = 310\text{ mA}$ ,  
 $V_{c1,2,3} = 0.7, 0.7, 0.8\text{ V}$  Typical

22 Gb/s, 0.48 Vpp Input,  $V_{out} = 7.2\text{ Vpp}$ .



**Key Features**

- 40 & 100 Gb/s Performance
- Single Ended Input and Output
- Adjustable Output Amplitude, 3 Vpp – 9 Vpp
- Low Additive RMS Jitter, 500 fsec
- High Output Drive, 8Vpp with 0.4 Vpp Input
- Gain, 32 dB at 20 GHz
- Low DC Power Dissipation, 1.7 W for  $V_{out} = 8\text{ Vpp}$  at  $V_d=6\text{ V}$
- Rise and Fall Times <12 psec
- Hot-Pluggable
- Module Size: 28.5 x 24 x 6.48 mm

**Primary Applications**

- 40 GB/s Optical Market: DQPSK (2x)
- 40 GB/s Optical Market: DPSK (1x)
- 100 GB/s Optical Market: DP-QPSK (4x)

**Product Description**

The TriQuint TGA4943-MOD is a three stage optical modulator driver module designed to operate at frequencies that target the 40 and 100 Gb/s optical market.

The TGA4943-MOD consists of three high performance wideband amplifiers assembled in a module that includes all necessary components for operation. A single TGA4943-MOD placed between the MUX and Optical Modulator provides OEMs with a modulator driver solution.

The TGA4943-MOD provides Metro and Long Haul designers with system critical features such as: low power dissipation, low rail ripple, high voltage drive capability (3 Vpp amplitude adjustable up to 9 Vpp), low output jitter, and low input drive sensitivity (0.4 Vpp – 1 Vpp at  $V_{out} = 8\text{ Vpp}$ ).

The TGA4943-MOD finish is lead-free. RoHS compliant. TGA4943-MOD parts are available upon request for evaluation purposes.

**Table I**  
**Absolute Maximum Ratings 1/**

Symbol	Parameter	Value	Notes
Vd1, Vd2, Vd3	Drain Voltage	9 V	<u>2/</u> <u>3/</u> <u>4/</u> <u>5/</u>
Vd1-Vg1, Vd2-Vg2, Vd3-Vg3	Drain to Gate Voltage	10 V, 13 V, 13 V	<u>2/</u> <u>3/</u> <u>4/</u> <u>5/</u>
Vg1, Vg2, Vg3	Gate Voltage Range	-4 to 0 V	
Vc1, Vc2, Vc3	Control Voltage Range	(Vd-7) to +5.5 V	<u>3/</u> <u>4/</u> <u>5/</u>
Id1, Id2	Drain Current	80, 135 mA	<u>2/</u>
Id3	Drain Current	270 mA	<u>2/</u>
Ig1, Ig2, Ig3	Gate Current Range	-15 to 21 mA	<u>5/</u>
Ic1, Ic2, Ic3	Control Current Range	-15 to 21 mA	<u>5/</u>
Vin_pp	Peak-Peak Input Voltage	7 V	
Pin	Input Continuous Wave Power	24 dBm	<u>2/</u>
Pd	Max Power Dissipation	3.05	<u>6/</u>
Tch	Maximum Channel Temperature	200 °C	

- 1/ These ratings represent the maximum operable values for this device. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device and / or affect device lifetime. These are stress ratings only, and functional operation of the device at these conditions is not implied.
- 2/ Combinations of supply voltage, supply current, input power, and output power shall not exceed the maximum power dissipation listed in Table IV.
- 3/ Assure  $Vd1 - (Vc1 - Vg1) \leq 6.5 \text{ V}$ ,  
Assure  $Vd2 - (Vc2 - 0.34 * Vg2) \leq 6.5 \text{ V}$ ,  
Assure  $Vd3 - (Vc3 - 0.34 * Vg3) \leq 6.5 \text{ V}$ ,
- 4/ Assure  $(Vd1 - Id1 - 50) - Vc1 \geq -0.5$ ,  
Assure  $(Vd2 - Id2 - 50) - Vc2 \geq -0.5$ ,  
Assure  $Vd3 - Vc3 \geq -0.5$ .
- 5/ HOT PLUGGABLE: Comply with voltage and currents outlined in Max Ratings Table.
- 6/ Maximum Power Dissipation is based upon Baseplate temperature of 80 °C and Channel Temperature of 200 °C, using the thermal resistance published in Table IV.

**Table II**  
**Recommended Operating Conditions**  
 21.5 Gbps Operation,  $V_{in} = 500$  mVpp

Output Voltage (Vpp)	Drain Voltage: Vd1, Vd2, Vd3 (V)	Quiescent Drain Current: Idq1, Idq2, Idq3 (mA)	Under RF Drive Drain Current: Id1, Id2, Id3 (mA)	Control Voltage: Vc1, Vc2, Vc3 (V)	Gate Voltage: Vg1, Vg2, Vg3 (V)*	Pdiss (W)
3.6	5	40, 30, 84	44, 72, 91	0.6, 0.6, -0.23	-0.56, -1.89, -1.34	0.96
4	5	40, 30, 100	44, 72, 101	0.6, 0.6, -0.01	-0.56, -1.89, -1.25	1.00
5	5	40, 30, 133	44, 72, 125	0.6, 0.6, 0.23	-0.56, -1.89, -1.09	1.09
6	5	40, 30, 160	44, 72, 149	0.6, 0.6, 0.42	-0.56, -1.89, -0.97	1.18
4	6	51, 51, 63	53, 90, 101	0.6, 0.6, -0.06	-0.51, -1.68, -1.53	1.32
5	6	51, 51, 100	53, 90, 125	0.6, 0.6, 0.14	-0.51, -1.68, -1.30	1.42
6	6	51, 51, 135	53, 90, 149	0.6, 0.6, 0.36	-0.51, -1.68, -1.11	1.51
7	6	51, 51, 167	53, 90, 175	0.6, 0.6, 0.59	-0.51, -1.68, -0.95	1.59
8	6	51, 51, 201	53, 90, 201	0.6, 0.6, 0.86	-0.51, -1.68, -0.78	1.66
6	7	62, 50, 98	64, 101, 150	0.6, 0.6, 0.24	-0.47, -1.74, -1.34	2.03
7	7	62, 50, 144	64, 101, 161	0.6, 0.6, 0.45	-0.47, -1.74, -1.09	2.16
8	7	62, 50, 179	64, 101, 202	0.6, 0.6, 0.66	-0.47, -1.74, -0.92	2.28
9	7	62, 50, 223	64, 101, 227	0.6, 0.6, 0.91	-0.47, -1.74, -0.69	2.4

\* Gate and control voltages should be adjusted to reach target drain currents and optimal eye performance and may vary from above voltages.

**Table III**  
**RF Characterization Table**

**Bias: Vd = 7 V, Idq=310 mA, Vc1, Vc2 = +0.7 V, Vc3 = 0.8 V, typical**

**Small Signal Characteristics**

<b>SYMBOL</b>	<b>PARAMETER</b>	<b>TEST CONDITIONS</b>	<b>MIN</b>	<b>NOMINAL</b>	<b>MAX</b>	<b>UNITS</b>
Gain	Small Signal Gain	f = 0.1 - 18 GHz	31	32	-	dB
		f = 18 - 26 GHz	28	30	-	
		f = 26.1 - 31 GHz	25	29	-	
		f = 31.1 - 36 GHz	21	28	-	
		f = 36.1 - 40 GHz	18	20	-	
IRL	Input Return Loss	f = 0.1 - 18 GHz	10	13	-	dB
		f = 18.1 - 30 GHz	8	11	-	
		f = 30.1 - 34 GHz	4	6	-	
		f = 34.1 - 40 GHz	3	4	-	
ORL	Output Return Loss	f = 0.1 - 18 GHz	10	15	-	dB
		f = 18.1 - 22 GHz	8	15	-	
		f = 22.1 - 30 GHz	6.5	8	-	
		f = 30.1 - 40 GHz	3	6	-	
BW	3 dB Bandwidth	f(x) - f(1 GHz) = 3 dB x = freq of 3 dB BW	-	27	-	GHz

**Table III - Cont.**  
**RF Characterization Table**

Bias: Vd = 7 V, Idq = 300 mA, Vc1, Vc2 = 0.6 V, Vc3 = 0.8 V, typical

**Electrical Eye Test Conditions: Vin = 0.5 Vpp, PRBS 2<sup>31</sup>-1, 21.5 Gbps, Crossing = 50% +/- 2%**

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	NOMINAL	MAX	UNITS
Vout_Max	Maximum Eye Amplitude	500 mVpp Input	7.8	10	-	Vpp
Tr	Risetime	8 Vpp Output		11	14	psec
Tf	Falltime	8 Vpp Output		11	14	psec
SNR	Signal to Noise Ratio	8 Vpp Output	14.5	20		dB
Eye Opening	<u>1/</u>	8 Vpp Output		85		%
Jrms_Add	Additive Jitter <u>2/</u>	8 Vpp Output		500	1100	fsec
Jpp	P-P Jitter	8 Vpp Output		6	8.5	psec
Id	Total Drain Current	8 Vpp Output	-	350	375	mA

1/ Eye Opening defined as: [ Eye Height (Vpp)/Eye Amplitude (Vpp)] \*100

2/ Additive Jitter defined as: Sqrt [(Total RMS Jitter)<sup>2</sup> – (Input RMS Jitter)<sup>2</sup>]

**Table IV**  
**Power Dissipation and Thermal Properties**

Parameter	Test Conditions	Value	Notes
Thermal Resistance, $\theta_{jc}$	Vd = 7 V Idq_tot = 300 mA; Idq3=190 mA Pd_stage3 = 1.33 W Tbaseplate = 70 °C	$\theta_{jc} = 55.8$ (°C/W) Tchannel = 144.2 °C Tm = 1.9 E+6 Hrs	1/, 2/
Thermal Resistance, $\theta_{jc}$ Under RF Drive	Vout = 8 Vpp, Vd = 7V Id3 = 190 mA Pd_stage3 = 1.17 W Tbaseplate = 70 °C	$\theta_{jc} = 55.8$ (°C/W) Tchannel = 135.3 °C Tm = 5.5 E+6 Hrs	
Storage Temperature		-65 to 150 °C	
Case Operating Range		-65 to 150 °C	3/

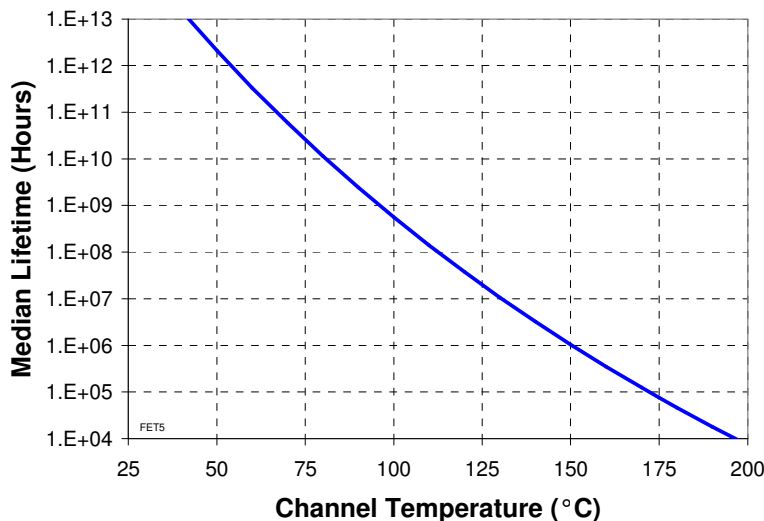
1/ Channel operating temperature will directly affect the device median lifetime (Tm). For maximum life, it is recommended that channel temperatures be maintained at the lowest possible levels.

2/  $\theta_{jc}$  is modeled based upon hottest stage (stage 3) because there is negligible thermal coupling between stages. It is the thermal resistance of the part calculated from the junction to the bottom of the TGA4943-MOD package.

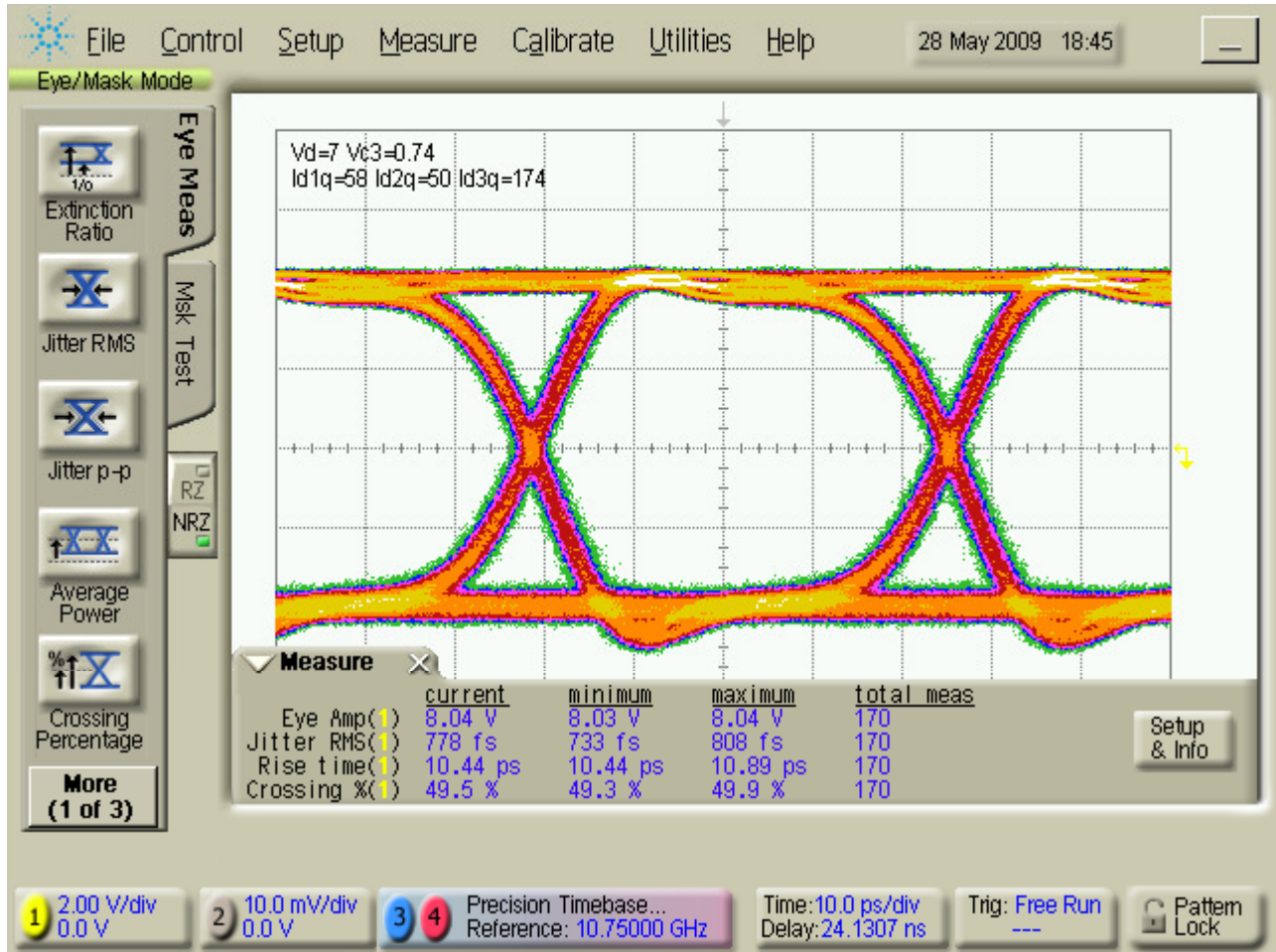
To calculate the temperature rise, calculate the dissipated power in the third stage ( $P_{diss} = I_{dq3} \cdot V_{d3}$ ) and multiply by  $\theta_{jc}$ . For example: Tbase = 70 °C, Idq3 = 190 mA, Vd3 = 7V.  $P_{diss\_stage3} = 1.33$  W.  $\theta_{jc} = 55.8$  °C/W. Temperature rise =  $\theta_{jc} \cdot P_{diss} = 74.2$  C; Tchannel = Tbase + Temp rise = 142.2 °C

3/ Case operating range values are for reference only. Channel temperature must not exceed maximum ratings value and must be at value acceptable to customer lifetime requirements.

### Median Lifetime (Tm) vs. Channel Temperature

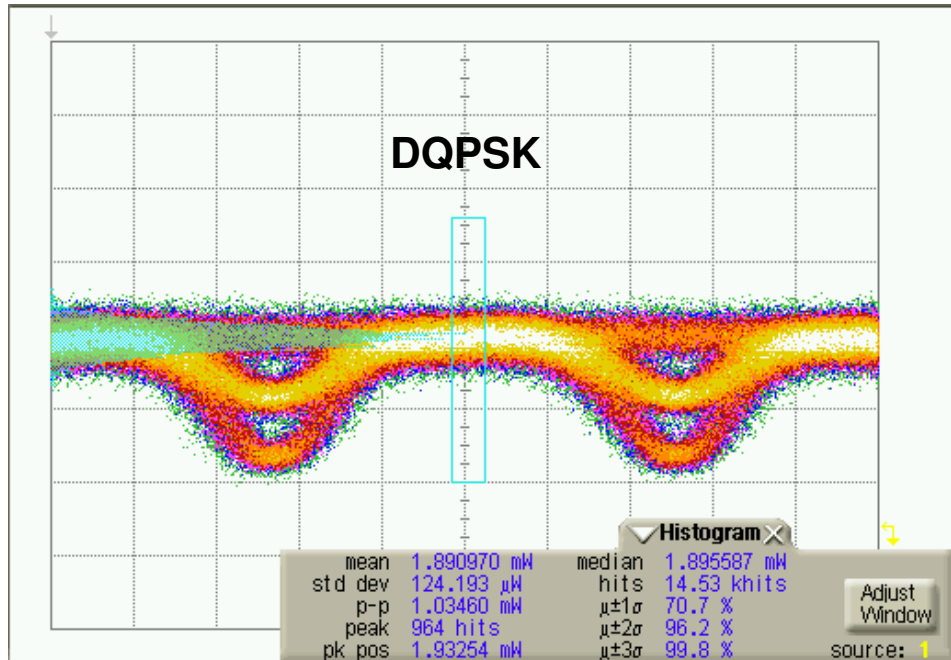
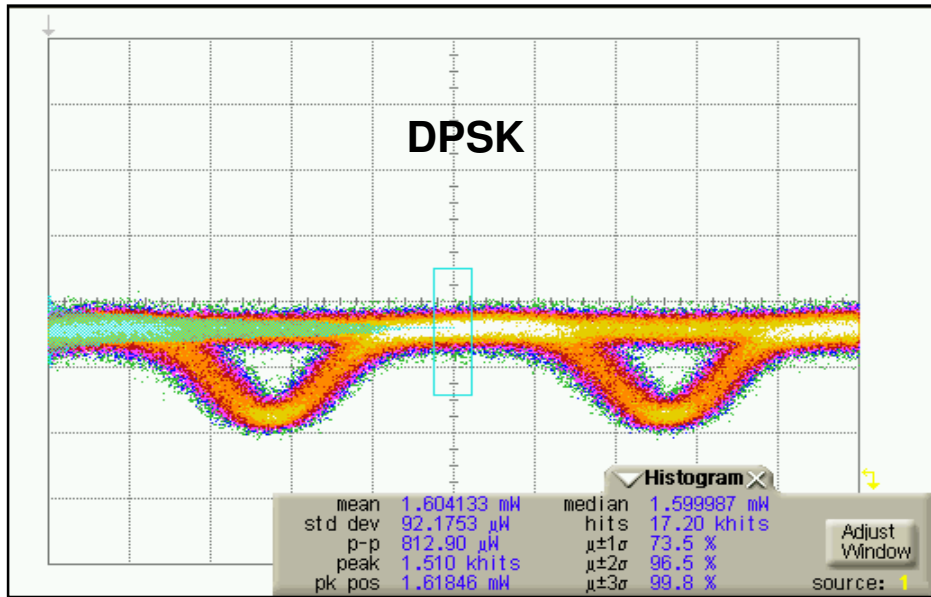


**Vd = 7 V, Vin = 0.5 Vpp, Vout = 8 Vpp, 21.5 Gb/s, PRBS 2<sup>31</sup> -1**





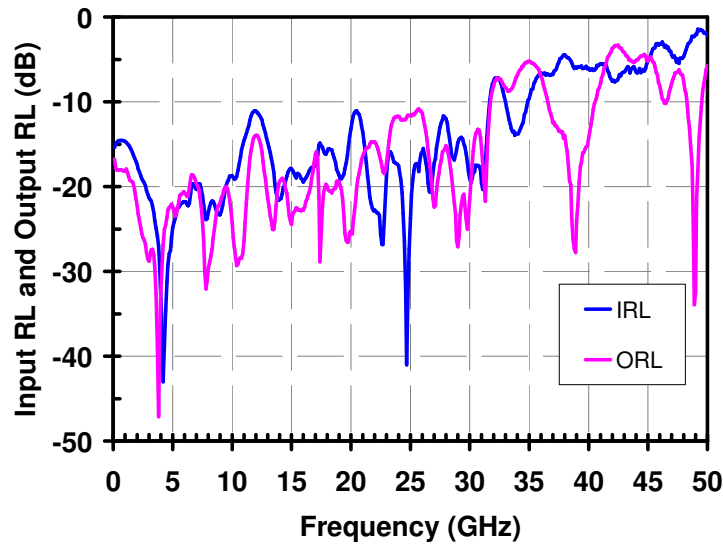
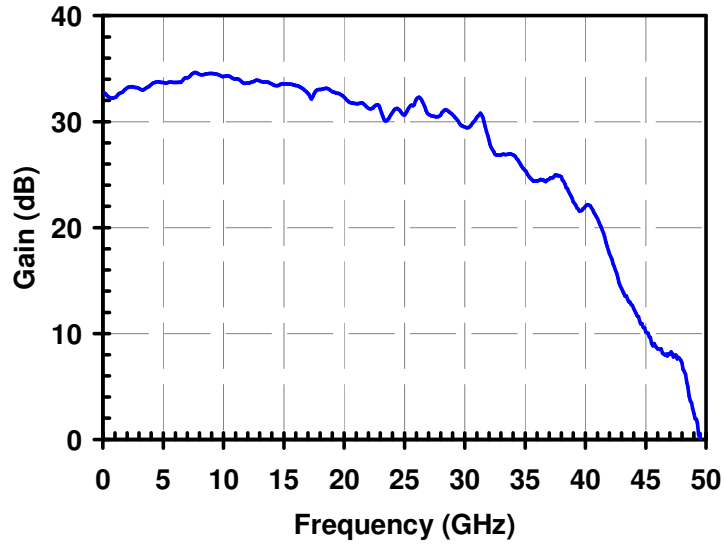
**Measured Data:  $V_d = 7\text{ V}$ ,  $V_{c1, 2, 3} = 0.7, 0.7, 0.8\text{ V}$ ,  
 $I_{dq1, 2, 3} = 40, 60, 210\text{ mA}$ ,  
 $V_{in} = 0.48\text{ Vpp}$ ,  $V_{out} = 8\text{ Vpp}$ ,  $22\text{ Gb/s}$**





**Measured Data**

**Bias Conditions:  $V_d = 7\text{ V}$ ,  $I_{dq} = 293\text{ mA}$ ,  
 $V_{g1,2,3} = -0.5, -1.7, -0.9\text{ V}$ ,  $V_{c1,2,3} = 0.7, 0.7, 0.7\text{ V}$  Typical**



**Ordering Information**

Part	Package Style
TGA4943-MOD	Module (RoHS)