

P-CHANNEL ENHANCEMENT MODE POWER MOSFET

MTP2603N6

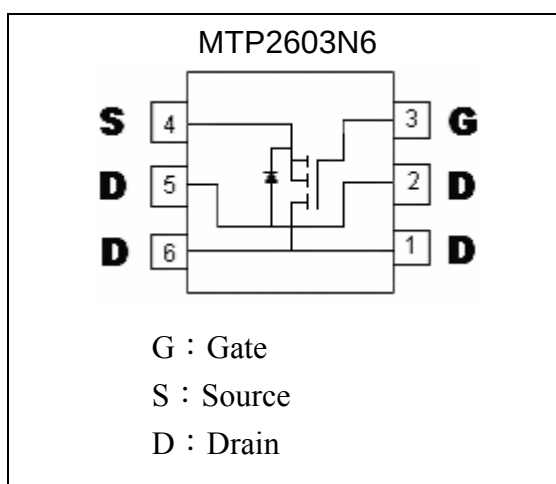
Description

The MTP2603N6 is a P-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The SOT-26 package is universally preferred for all commercial-industrial surface mount applications.

Features

- Simple drive requirement
- Low on-resistance
- Small package outline
- Pb-free package

Equivalent Circuit



Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	-20	V
Gate-Source Voltage	V _{GS}	±12	V
Continuous Drain Current @T _A =25 °C (Note 1)	I _D	-5	A
Continuous Drain Current @T _A =70 °C (Note 1)	I _D	-4	A
Pulsed Drain Current (Note 2, 3)	I _{DM}	-20	A
Total Power Dissipation @ T _A =25 °C Linear Derating Factor	P _d	2	W
		0.016	W / °C
Operating Junction Temperature and Storage Temperature Range	T _j , T _{stg}	-55~+150	°C
Thermal Resistance, Junction-to-Ambient (Note 1)	R _{th,ja}	62.5	°C/W

Note : 1.Surface mounted on 1 in² copper pad of FR-4 board. 156°C/W when mounted on minimum copper pad.

2.Pulse width limited by maximum junction temperature.

3.Pulse Width ≤300μs, Duty Cycle≤2%

Electrical Characteristics (Ta=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV_{DSS}	-20	-	-	V	$V_{GS}=0, I_D=-250\mu A$
$\Delta BV_{DSS}/\Delta T_j$	-	-0.1	-	V/°C	Reference to 25°C, $I_D=-1mA$
$V_{GS(th)}$	-0.5	-	-1.2	V	$V_{DS}=V_{GS}, I_D=-250\mu A$
I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 12V, V_{DS}=0$
I_{DSS}	-	-	-1	μA	$V_{DS}=-20V, V_{GS}=0, T_j=25^\circ C$
I_{DSS}	-	-	-10	μA	$V_{DS}=-16V, V_{GS}=0, T_j=55^\circ C$
$*R_{DS(ON)}$	-	-	53	mΩ	$I_D=-4.5A, V_{GS}=-10V$
	-	-	65		$I_D=-4.2A, V_{GS}=-4.5V$
	-	-	120		$I_D=-2.0A, V_{GS}=-2.5V$
	-	-	250		$I_D=-1.0A, V_{GS}=-1.8V$
$*G_{FS}$	-	9	-	S	$V_{DS}=-5V, I_D=-2.8A$
C_{iss}	-	740	1200	pF	$V_{DS}=-15V, V_{GS}=0, f=1MHz$
C_{oss}	-	167	-		
C_{rss}	-	126	-		
$t_{d(ON)}$	-	5.9	-	ns	$V_{DS}=-15V, I_D=-4.2A,$ $V_{GS}=-10V, R_G=6\Omega, R_D=3.6\Omega$
t_r	-	3.6	-		
$t_{d(OFF)}$	-	32.4	-		
t_f	-	2.6	-		
Q_g	-	10.6	16	nC	$V_{DS}=-16V, I_D=-4.2A,$ $V_{GS}=-4.5V,$
Q_{gs}	-	2.32	-		
Q_{gd}	-	3.68	-		

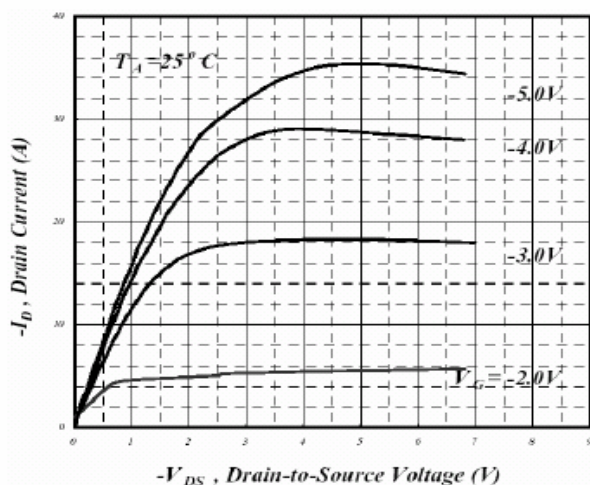
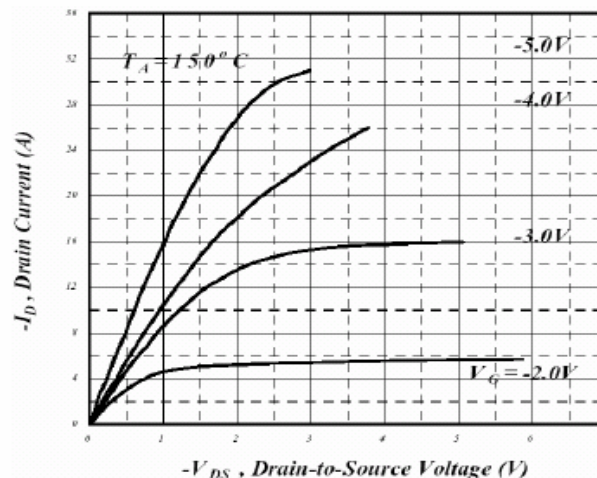
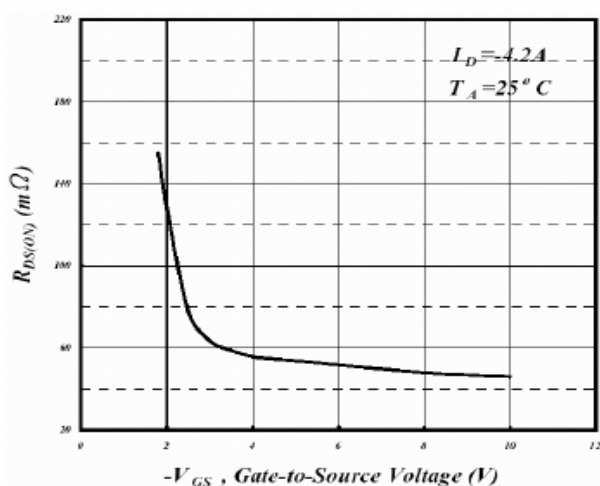
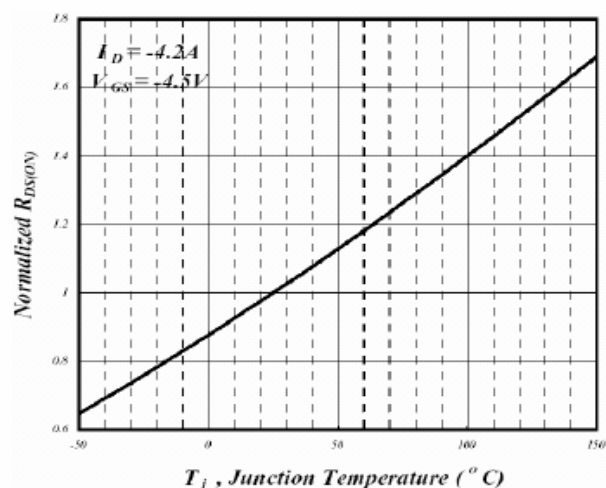
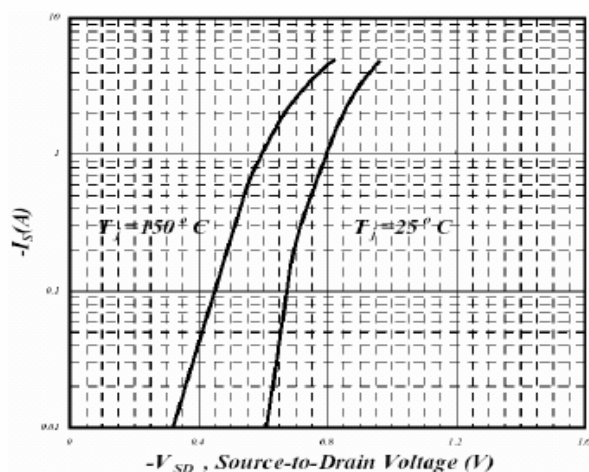
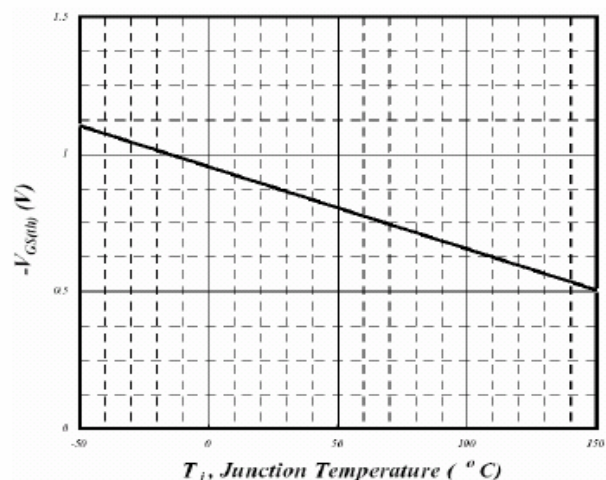
*Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

Source Drain Diode

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
$*V_{SD}$	-	-	-1.2	V	$I_S=-1.2A, V_{GS}=0V$
$*T_{rr}$	-	27.7	-	ns	$I_S=-4.2A, V_{GS}=0V, dI/dt=100A/\mu s$
Q_{rr}	-	22	-	nC	

*Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

Characteristic Curves



Characteristic Curves(Cont.)

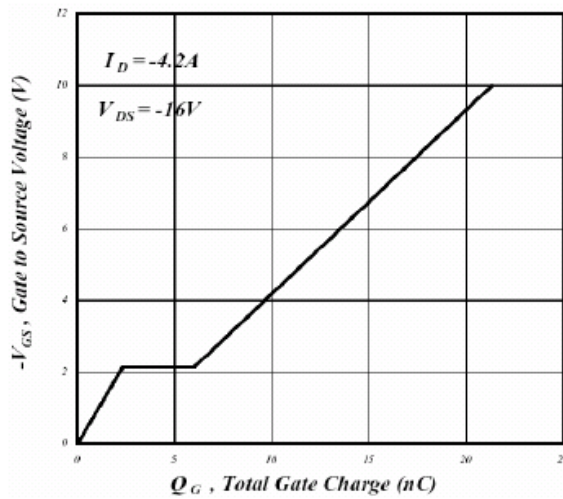


Fig 7. Gate Charge Characteristics

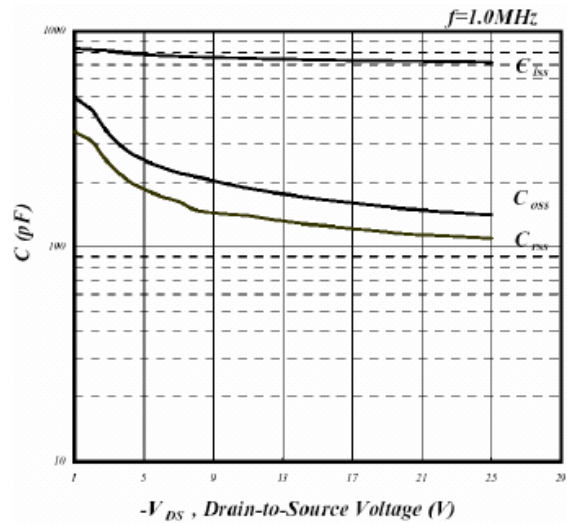


Fig 8. Typical Capacitance Characteristics

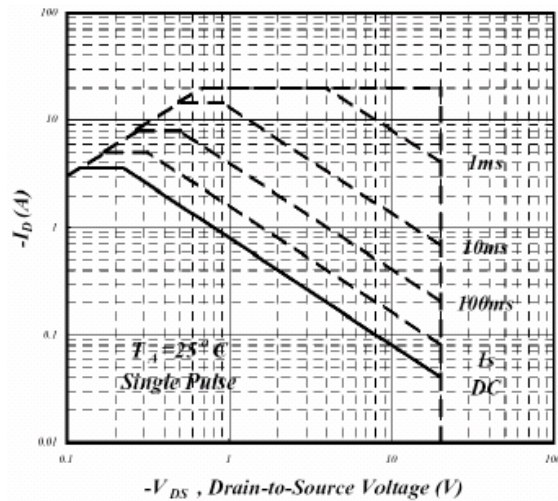


Fig 9. Maximum Safe Operating Area

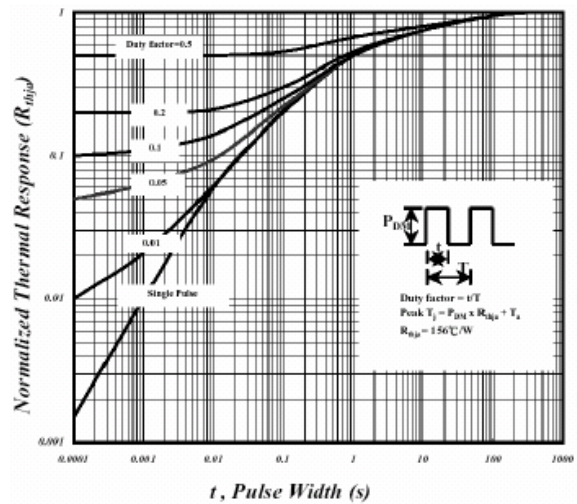


Fig 10. Effective Transient Thermal Impedance

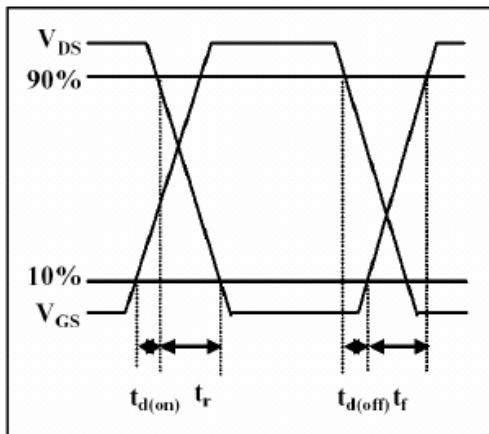


Fig 11. Switching Time Waveform

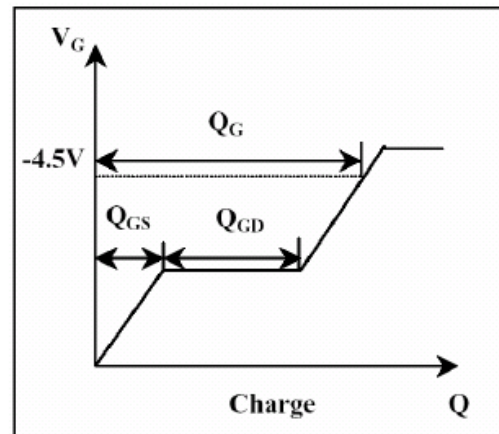
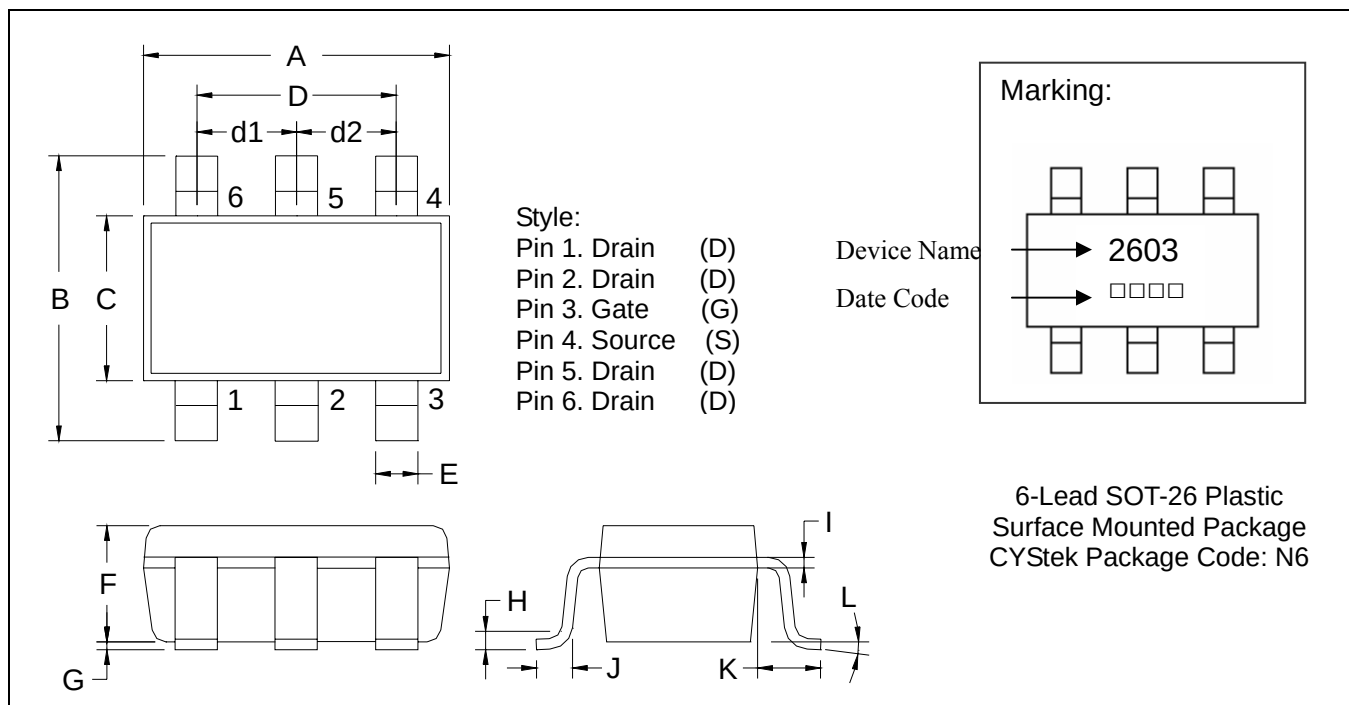


Fig 12. Gate Charge Waveform

SOT-26 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1063	0.1220	2.70	3.10	F	0.0472 REF		1.20	REF
B	0.1024	0.1181	2.60	3.00	G	0	0.0039	0	0.10
C	0.0551	0.0709	1.40	1.80	H	-	0.0079	-	0.20
D	0.0748 REF		1.90 REF		I	0.0047 REF		0.12 REF	
d1	0.0374 REF		0.95 REF		J	0.0146 REF		0.37 REF	
d2	0.0374 REF		0.95 REF		K	0.0236 REF		0.60 REF	
E	0.0118	0.0217	0.30	0.55	L	0°	10°	0°	10°

Notes : 1.Controlling dimension : millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.