

SMDA30 SERIES



30W Desktop for Medical Equipment

- Wide Input Voltage 90 to 264 VAC, 47 to 63Hz
- IEC-320-C14 input inlet
- Output Voltage Available From 3VDC Thru 40VDC
- Single to Triple Output
- Input Surge Current, Over Voltage, and Over Load Protection.
- CEC Level V, Energy Star 2.0, and RoHS Compliance
- Class I Insulation

3 Year Warranty

Approvals:

Single Output

Model Number	Output Voltage	Max. Output Current	Total Regulation	Maximum Output Power
SMDA30-S01*	3 ~ 5 VDC	6.66 ~ 4.00 A	5%	20W
SMDA30-S02*	5 ~ 6 VDC	5.00 ~ 4.16 A	5%	25W
SMDA30-S03*	6 ~ 8 VDC	4.16 ~ 3.12 A	5%	25W
SMDA30-S04*	8 ~ 11 VDC	3.75 ~ 2.72 A	4%	30W
SMDA30-S05*	11 ~ 13 VDC	2.72 ~ 2.30 A	3%	30W
SMDA30-S06*	13 ~ 16 VDC	2.30 ~ 1.87 A	3%	30W
SMDA30-S07	16 ~ 21 VDC	1.87 ~ 1.42 A	3%	30W
SMDA30-S08	21 ~ 27 VDC	1.42 ~ 1.11 A	2%	30W
SMDA30-S09	27 ~ 33 VDC	1.11 ~ 0.90 A	2%	30W
SMDA30-S10	33 ~ 40 VDC	0.90 ~ 0.75 A	2%	30W

Multi Output

Model Number	Output 1				Output 2				Output 3				Maximum Output Power
	Vonom	Iomin	Iomax	Regmax	Vonom	Iomin	Iomax	Regmax	Vonom	Iomin	Iomax	Regmax	
SMDA30-D00*	+3.3V	0.3A	3A	7%	+12V	0.13A	1.3A	5%					25W
SMDA30-D01*	+5V	0.3A	3A	5%	+12V	0.13A	1.3A	5%					30W
SMDA30-D02*	+5V	0.3A	3A	5%	+15V	0.1A	1.0A	5%					30W
SMDA30-D03	+5V	0.3A	3A	5%	+24V	0.07A	0.7A	5%					30W
SMDA30-D04*	+3.3V	0.3A	3A	7%	+5V	0.16A	1.6A	5%					17.9W
SMDA30-D09*	+12V	0.2A	2A	5%					-12V	0A	0.5A	10%	30W
SMDA30-D10*	+15V	0.15A	1.5A	5%					-15V	0A	0.5A	10%	30W
SMDA30-D15	+5V	0.25A	2.5A	5%					-24V	0A	1.0A	10%	30W
SMDA30-T01*	+5V	0.25A	2.5A	5%	+12V	0.13A	1.3A	5%	-5V	0A	0.5A	10%	25W
SMDA30-T02*	+5V	0.25A	2.5A	5%	+12V	0.1A	1.0A	5%	-12V	0A	0.5A	10%	30W
SMDA30-T03*	+5V	0.25A	2.5A	5%	+15V	0.1A	1.0A	5%	-15V	0A	0.5A	10%	30W
SMDA30-T04	+5V	0.3A	3.0A	5%	+24V	0.1A	1.0A	5%	-24V	0A	0.5A	10%	30W
SMDA30-T05	+5V	0.3A	3.0A	5%	+24V	0.1A	1.0A	5%	-12V	0A	0.5A	10%	30W
SMDA30-T06*	+3.3V	0.25A	2.5A	7%	+12V	0.11A	1.1A	5%	-5V	0A	0.5A	10%	25W

Mark " * " means " PSE approval "

The model number of S03~S11 had been approved by CEC level V.

Total Regulation is conditioned by below configuration

(S03~S04: AWG16/4FT output cable)

(S05~S08: AWG88/6FT output cable)

(S09~S10: AWG20/6FT output cable)

Electrical Characteristics

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Input Voltage	Operating Voltage	90		264	VAC
Input Frequency		47		63	Hz
Output Power Range	Vin=90 to 264 VAC	0		30	W
Input Current (Low Line)	Io=Full load, Vin= 115 VAC			0.9	A
Input Current (High Line)	Io=Full load, Vin= 230 VAC			0.34	A
Low Line Inrush Current	Io=Full load, 25°C, Cool start, Vin=115VAC		12	15	A
High Line Inrush Current	Io=Full load, 25°C, Cool start, Vin=230VAC		28	38	A
Efficiency	Io=Full Load, Vin=230VAC	70	83	85	%
Line Regulation	Io=Full Load		0.5	1	%
Load Regulation	Vin=230VAC		3	10	%
Over Voltage Protection		112		132	%
Over Current Protection		110		150	%
Transient Response	Io=Full Load to Half Load, Vin=100VAC			4	mS
Hold-Up Time	Io=Full Load, Vin=110VAC	12			mS
Start Up Time	Io=Full Load, Vin=100VAC	0.3	1.5	2	S
* Ripple & Noise (Peak to Peak)	Full Load, Vin=90VAC		0.5	1	%
Safety Ground Leakage Current	Io= Full Load, Vin=240VAC			0.1	mA
Temperature Coefficient	All output	-0.04		0.04	%/°C
No-Load Power Consumption	No load, vin=240VAC	0.3		0.5	W

* **Note:** The Ripple & Noise which is under 3.3VDC at 2% max.

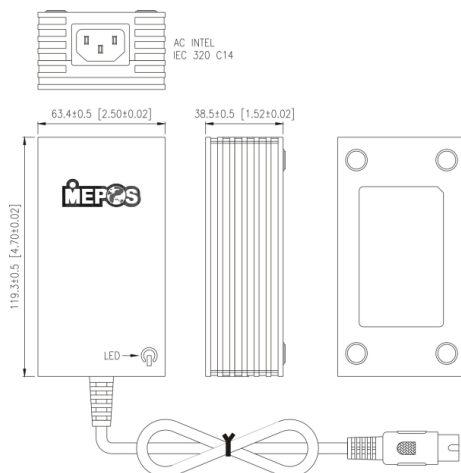
Conditions

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Operating Temperature		0	50	70	°C
Storage Temperature		-40		85	°C
Relative Humidity		5		95	%
Operating Temperature at 25°C, Calculated per MIL-HDBK-217F		0.1M			Hrs
De-rate linearly from 100% load at 50°C to 50% load at 70°C					

Approvals and Compliance

Parameter	Test Conditions	Min.	Unit
Dielectric Withstanding Voltage for Primary to secondary	Primary to secondary	5656	VDC
Dielectric Withstanding Voltage for Primary to Ground	Primary to ground	2828	VDC
Isolation Resistance	Test Voltage = 500VDC	50	MΩ
EMI requirements for CISPR-11	Vin=220VAC	B	CLASS
EMI requirements for FCC PART-18	Vin=110VAC	B	CLASS

Mechanical and PIN out



1. Dimensions are shown in inches or mm.
2. Weight: 400-460gs approx.
3. Optional output connector