MAX17530 5V Output Evaluation Kit (TDFN)

Evaluates: MAX17530 (TDFN) in 5V Output Voltage Applications

General Description

The MAX17530 5V evaluation kit (EV kit) (TDFN) is a fully assembled and tested circuit board that demonstrates the performance of this 42V, 25mA ultra-small, high-efficiency, synchronous step-down DC-DC converter in a 10-pin TDFN package. The EV kit operates over a wide input voltage range of 6V to 42V and provides up to 25mA load current at 5V output. It draws only 26µA supply current under no-load conditions (EN/UVLO connected to V_{IN}). The EV kit is programmed to switch at a frequency of 220kHz. The device is simple to use and easily configurable with minimal external components. It features cycle-by-cycle peak current-limit protection, undervoltage lockout, and thermal shutdown.

The EV kit comes installed with the MAX17530ATB+ in a 10-pin (3mm x 2mm) lead(Pb)-free/RoHS-compliant TDFN package.

Features

- 6V to 42V Input Voltage Range
- 5V Output, 25mA Continuous Current
- 26µA No-Load Supply Current
- EN/UVLO for On/Off Control and Programmable Input Undervoltage Lockout
- Programmable Switching Frequency
- Internal or Programmable Soft-Start
- PFM or Forced-PWM Mode of Operation
- Open-Drain $\overline{\text{RESET}}$ Output
- Peak Current-Limit Protection
- Thermal Shutdown
- Proven PCB Layout
- Fully Assembled and Tested

Quick Start

Recommended Equipment

- MAX17530 5V EV kit (TDFN)
- 42V adjustable, 0.5A DC power supply
- Electronic load up to 25mA
- Voltmeter

Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation. **Caution: Do not turn on the power supply until all connections are completed.**

1) Verify that shunts are installed on jumpers JU1, JU2 (EN/UVLO).
2) Verify that jumper JU3 (MODE-PFM operation) is open.
3) Set the electronic load to constant-current mode, 25mA, and disable the electronic load.
4) Connect the electronic load's positive terminal to the VOUT PCB pad. Connect the negative terminal to the GND PCB pad.
5) Connect the voltmeter across the VOUT and GND PCB pads.
6) Set the power-supply output to 24V. Disable the power supply.
7) Connect the power-supply output to the VIN PCB pad. Connect the supply ground to the GND PCB pad.
8) Turn on the power supply.
9) Enable the electronic load and verify that output voltage is 5V with respect to GND.
10) Vary the input voltage from 6V to 42V.
11) Vary the load current from 1mA to 25mA and verify that output voltage is 5V with respect to GND.

Note: While performing an output short-circuit test, it is possible for the ceramic output capacitor to oscillate with the wiring inductance between the capacitor and short-circuited load, and thereby cause the absolute maximum rating of the V_{OUT} pin (-0.3V) to be exceeded. The resistor (R7) and the capacitor (C5) are included on this EV kit PCB to protect against unintentional violation of the above mentioned rating. In the actual system design, parasitic board or wiring inductance should be minimized and the output-voltage waveform under short-circuit operation should be verified to ensure that the absolute maximum rating of the V_{OUT} pin is not exceeded.

Ordering Information ***appears at end of data sheet.***

19-7697; Rev 0; 6/15

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Detailed Description

The MAX17530 5V EV kit (TDFN) is a fully assembled and tested circuit board that demonstrates the performance of the 42V, 25mA ultra-small, high-efficiency, synchronous step-down DC-DC converter in a 10-pin TDFN package. The EV kit operates over a wide input voltage range of 6V to 42V and provides up to 25mA load current at 5V output. It draws only 26µA supply current under no-load conditions (EN/UVLO connected to VIN). The EV kit is programmed to switch at a frequency of 220kHz. The device is simple to use and easily configurable with minimal external components. It features cycle-by-cycle peak current-limit protection, undervoltage lockout, and thermal shutdown.

The EV kit includes an EN/UVLO PCB pad and jumpers JU1 and JU2 to enable control of the converter output. The MODE PCB pad and JU3 are provided for selecting the mode of operation of the converter. A $\overline{\text{RESET}}$ PCB pad is available for monitoring the $\overline{\text{RESET}}$ output. The RT/SYNC PCB pad can be used to synchronize the EV kit switching frequency to an external clock frequency.

Enable Control (JU1, JU2)

The EN/UVLO pin on the EV kit serves as an on/off control while also allowing the user to program the input undervoltage-lockout (UVLO) threshold. JU1 and JU2 configure the EV kit's output for turn-on/turn-off control. See Table 1 for proper JU1, JU2 jumper configurations.

Additionally, resistors R1 and R2 are included to set the UVLO to a desired turn-on voltage. Refer to the *Setting the Input Undervoltage-Lockout Level* section in the MAX17530 IC data sheet for additional information on setting the UVLO threshold voltage.

$\overline{\text{RESET}}$ Output

The EV kit provides a PCB pad to monitor the status of the $\overline{\text{RESET}}$ output. $\overline{\text{RESET}}$ goes high when the output voltage rises above 95% (typ) of its nominal regulated output voltage. $\overline{\text{RESET}}$ goes low when output voltage falls below 92% (typ) of its nominal regulated voltage.

PFM or Forced-PWM Mode (MODE)

The EV kit includes JU3 to select the mode of operation of the converter. Install a shunt across JU3 before powering up the EV kit to enable the forced-PWM operation. Keep JU3 open to enable the light-load PFM operation. See Table 2 for proper JU3 settings.

Soft-Start

The EV kit offers a fixed 5ms soft-start time. Connect capacitor C4 to adjust the soft-start time (t_{SS}). The minimum soft-start time is related to the output capacitance (C_{OUT}) and the output voltage (V_{OUT}) by the following equation.

$$t_{SS} > 0.05 \times C_{OUT} \times V_{OUT}$$

where t_{SS} is in milliseconds and C_{OUT} is in µF.

Use the following equation to determine the soft-start capacitance value (C_{SS}).

$$C_{SS} = 6.25 \times t_{SS}$$

where t_{SS} is in milliseconds and C_{SS} is in nanofarads.

External Synchronization (RT/SYNC)

The EV kit provides a PCB pad to synchronize the EV kit switching frequency to an external clock frequency. Refer to the *External Synchronization* section in the MAX17530 IC data sheet for additional information on configuring the external clock.

Table 1. Enable Control (EN/UVLO) (JU1, JU2)

SHUNT POSITION		EN/UVLO PIN	VOUT OUTPUT
JU1	JU2		
1-2	Open	Connected to VIN	Enabled
Open	1-2	Connected to GND	Disabled
1-2*	1-2	Connected to midpoint of R1, R2 resistor-divider	Enabled at $V_{IN} \geq 6V$

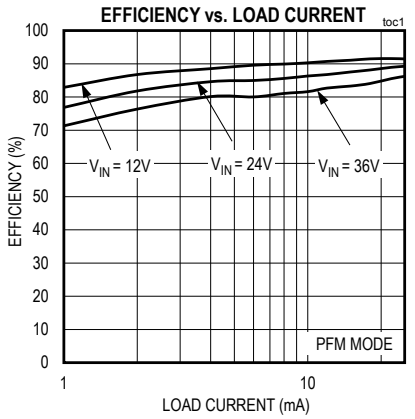
**Default position.*

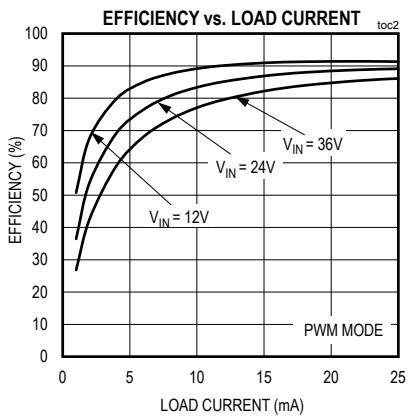
Table 2. MODE Control (JU3)

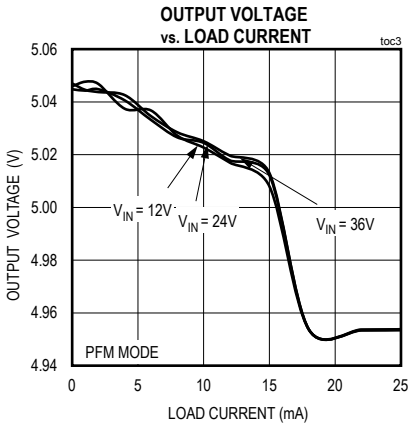
SHUNT POSITION	MODE PIN	MODE OF OPERATION
1-2	Connected to GND	Forced PWM
Open*	Unconnected	PFM

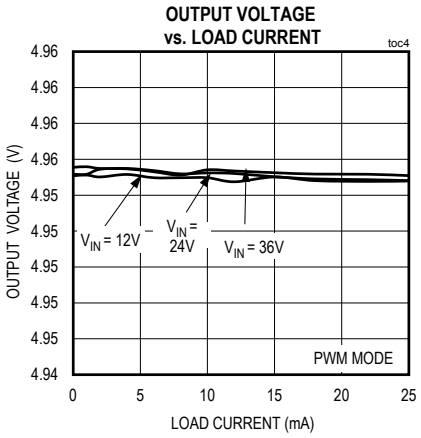
**Default position.*

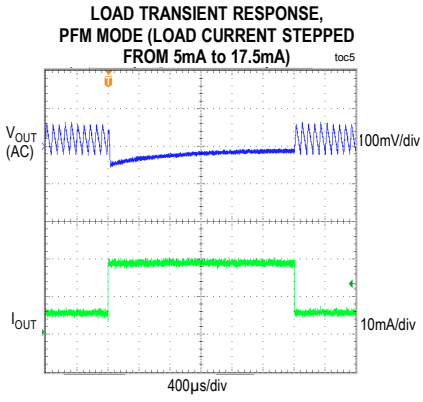
EV Kit Performance Report

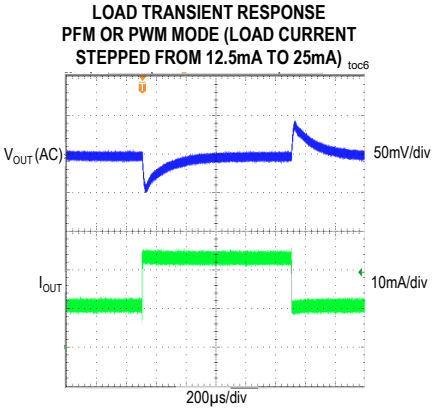


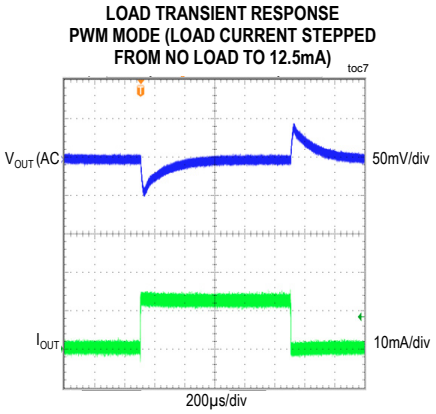


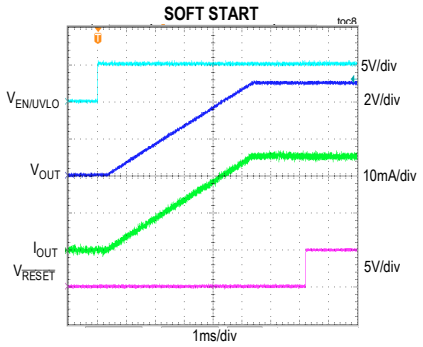


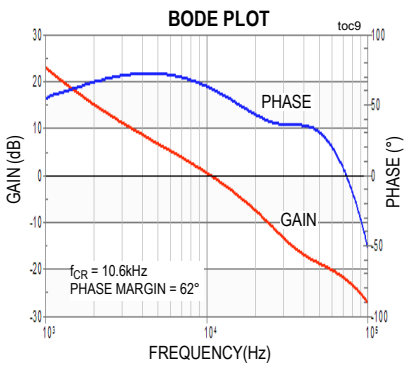












Component Suppliers

SUPPLIER	WEBSITE
Coilcraft, Inc.	www.coilcraft.com
Murata Americas	www.murata.com
Panasonic Corp.	www.panasonic.com

Note: *Indicate that you are using the MAX17530ATB when contacting these component suppliers.*

Component List and Schematic

Refer to the following files attached to this data sheet for component information and schematic:

- **MAX17530EV TDFN BOM**
- **MAX17530EV TDFN Schematic**

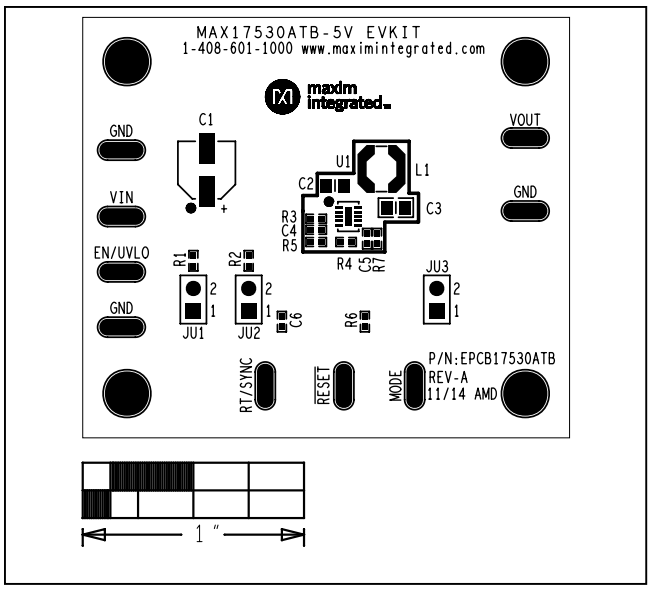


Figure 1. MAX17530 5V EV Kit (TDFN) Component Placement Guide—Component Side

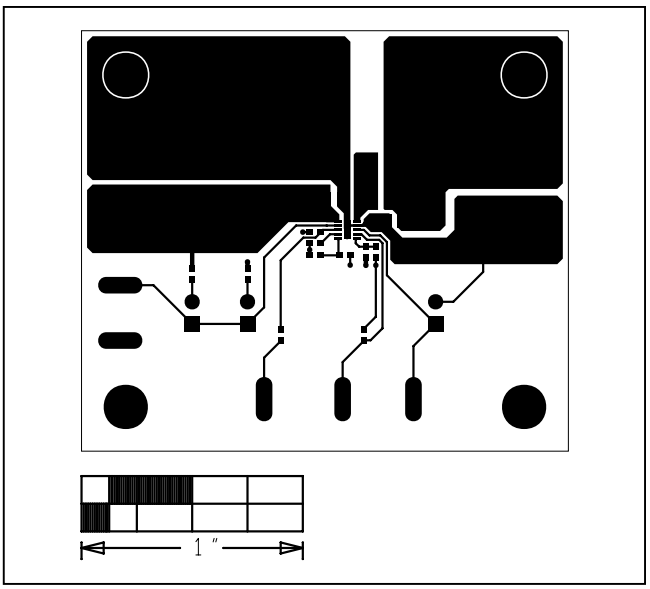


Figure 2. MAX17530 5V EV Kit (TDFN) PCB Layout—Component Side

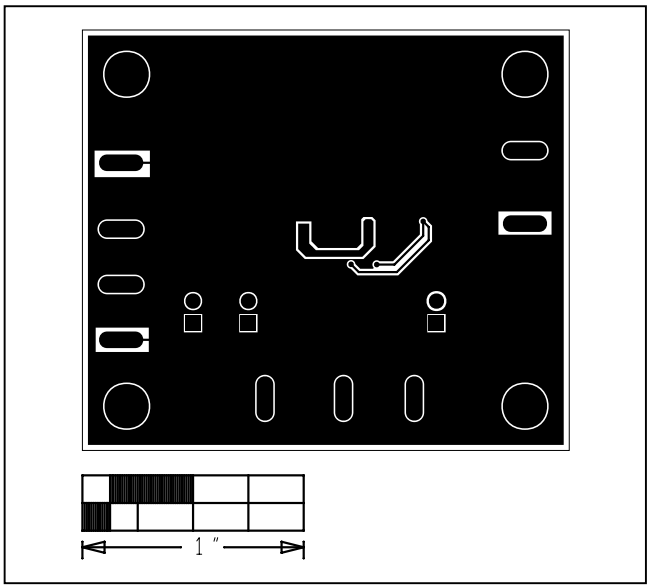


Figure 3. MAX17530 5V EV Kit (TDFN) PCB Layout—Solder Side

Ordering Information

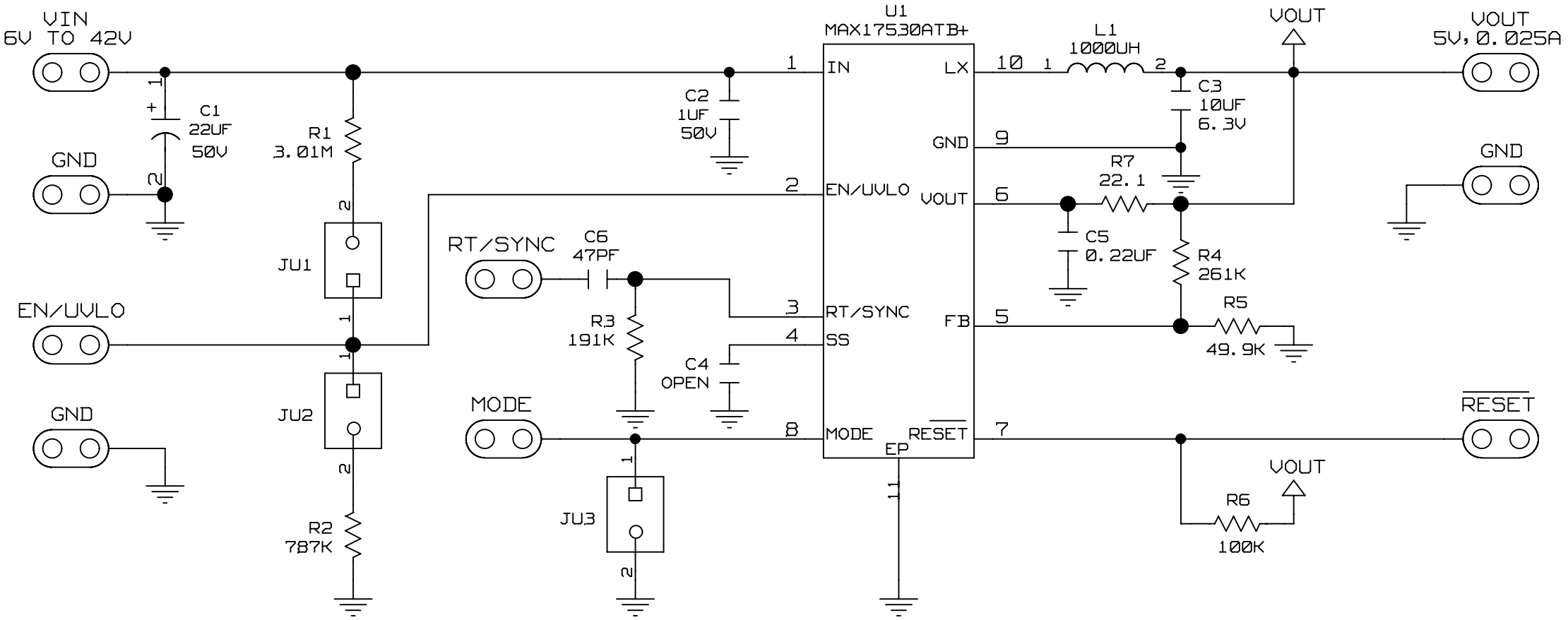
PART	TYPE
MAX17530ATBEVKIT#	EV Kit

#Denotes RoHS compliant.

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	6/15	Initial release	—

Maxim Integrated cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim Integrated product. No circuit patent licenses are implied. Maxim Integrated reserves the right to change the circuitry and specifications without notice at any time.


VIN
6V TO 42V
GND
C1
22UF
50V
R1
3.01M
JU1
EN/UVLO
GND
JU2
R2
787K
RT/SYNC
C6
47PF
R3
191K
C4
OPEN
MODE
JU3
C2
1UF
50V
U1
MAX17530ATB+
IN
EN/UVLO
RT/SYNC
SS
MODE
EP
LX
GND
VOUT
FB
RESET
L1
1000UH
C3
10UF
6.3V
R7
22.1
C5
0.22UF
R4
261K
R5
49.9K
R6
100K
VOUT
VOUT
5V, 0.025A
GND
RESET

BILL OF MATERIALS (BOM) Revision 5/15

Serial No.	Description	Quantity	Designator	Part Number
1	22µF, 50V electrolytic capacitor (6.3mm x 6.3mm)	**1**	C1	PANASONIC EEEFK1H220P
2	1µF ±10%, 50V X7R ceramic capacitor (0805)	**1**	C2	MURATA GRM21BR71H105K
3	10µF ±10%, 6.3V X7R ceramic capacitor (0805)	**1**	C3	Murata GRM21BR70J106K
4	Not installed, OPEN (0402)	**0**	C4	
5	0.22µF ±10%, 16V X7R ceramic capacitor (0402)	**1**	C5	Murata GRM155R71C224K
6	47pF ±5% 50V COG ceramic capacitor (0402)	**1**	C6	MURATA GRM1555C1E470J
7	2-pin headers	**3**	JU1-JU3	Sullins: PTC36SAAN
8	1000µH, 110mA inductor	**1**	**L1**	Coilcraft LPS5030-105M
9	3.01M ohm ±1%, resistor (0402)	**1**	R1	
10	787k ohm ±1%, resistor (0402)	**1**	R2	
11	191k ohm ±1%, resistor (0402)	**1**	R3	
12	261k ohm ±1%, resistor (0402)	**1**	R4	
13	49.9k ohm ±1%, resistor (0402)	**1**	R5	
14	100k ohm ±1%, resistor (0402)	**1**	R6	
15	22.1 ohm ±1%, resistor (0402)	**1**	R7	
16	42V, 25mA, ultra-small, highefficiency, synchronous step-down DC-DC converter with 22µA noload supply current (10 TDFN-EP*) Maxim MAX17530ATB+	**1**	U1	MAX17530ATB+
17	**Shunt**	**3**	**See Jumper Table**	SULLINS STC02SYAN

Jumper Table

JUMPER	SHUNT POSITION
JU1	1-2
JU2	1-2
JU3	OPEN