

SGF15N90D

General Description

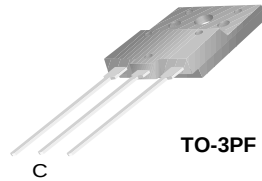
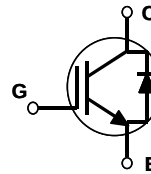
Insulated Gate Bipolar Transistors (IGBTs) with a trench gate structure provide superior conduction and switching performance in comparison with transistors having a planar gate structure. They also have wide noise immunity. These devices are very suitable for induction heating applications.

Features

- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.0 \text{ V @ } I_C = 15\text{A}$
- High input impedance
- Built-in fast recovery diode

Applications

Home appliances, induction heaters, induction heating JARs, and microwave ovens.



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Description	SGF15N90D	Units
V_{CES}	Collector-Emitter Voltage	900	V
V_{GES}	Gate-Emitter Voltage	± 25	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	15	A
	Collector Current @ $T_C = 100^\circ\text{C}$	12	A
$I_{CM(1)}$	Pulsed Collector Current	30	A
I_F	Diode Continuous Forward Current @ $T_C = 100^\circ\text{C}$	12	A
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	83	W
	Maximum Power Dissipation @ $T_C = 100^\circ\text{C}$	33	W
T_J	Operating Junction Temperature	-55 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temp. for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}(\text{IGBT})$	Thermal Resistance, Junction-to-Case	--	1.5	$^\circ\text{C/W}$
$R_{\theta JC}(\text{DIODE})$	Thermal Resistance, Junction-to-Case	--	2.86	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	--	40	$^\circ\text{C/W}$

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
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Off Characteristics

BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250μA	900	--	--	V
I _{CES}	Collector Cut-off Current	V _{CE} = V _{CES} , V _{GE} = 0V	--	--	1.0	mA
I _{GES}	G-E Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0V	--	--	± 500	nA

On Characteristics

V _{GE(th)}	G-E Threshold Voltage	I _C = 15mA, V _{CE} = V _{GE}	4.0	5.0	7.0	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 2.5A, V _{GE} = 15V	--	1.4	1.8	V
		I _C = 15A, V _{GE} = 15V	--	2.0	2.7	V

Dynamic Characteristics

C _{ies}	Input Capacitance	V _{CE} =10V, V _{GE} = 0V, f = 1MHz	--	1500	--	pF
C _{oes}	Output Capacitance		--	80	--	pF
C _{res}	Reverse Transfer Capacitance		--	50	--	pF

Switching Characteristics

t _{d(on)}	Turn-On Delay Time	V _{CC} = 600 V, I _C = 15A, R _G = 51Ω, V _{GE} = 15V, Resistive Load, T _C 25°C	--	50	80	ns
t _r	Rise Time		--	180	280	ns
t _{d(off)}	Turn-Off Delay Time		--	150	230	ns
t _f	Fall Time		--	200	320	ns
Q _g	Total Gate Charge	V _{CE} = 600 V, I _C = 15A, V _{GE} = 15V	--	60	80	nC
Q _{ge}	Gate-Emitter Charge		--	15	--	nC
Q _{gc}	Gate-Collector Charge		--	20	--	nC

Electrical Characteristics of DIODE T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{FM}	Diode Forward Voltage	I _F = 4A	--	1.1	1.6	V
		I _F = 15A	--	1.45	1.7	V
t _{rr}	Diode Reverse Recovery Time	I _F = 15A, di/dt = 20 A/μs	--	0.8	1.2	us
I _R	Instantaneous Reverse Current	V _{RRM} = 900V	--	0.03	1.2	uA

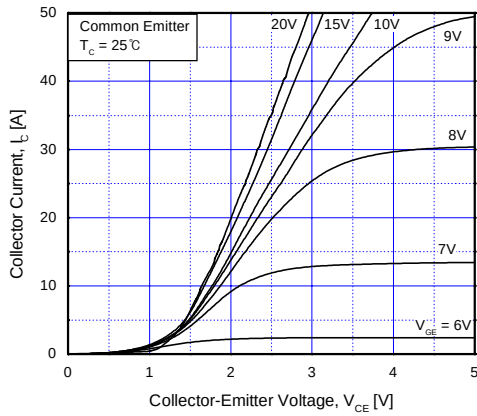


Fig 1. Typical Output Characteristics

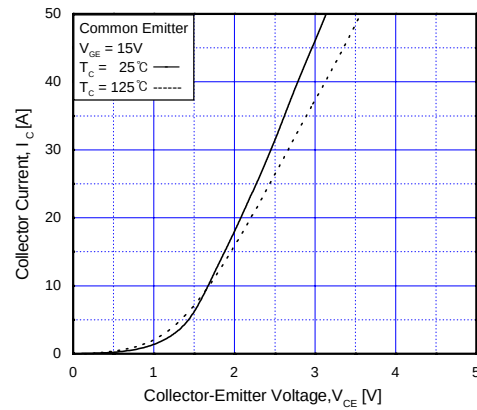


Fig 2. Typical Saturation Voltage Characteristics

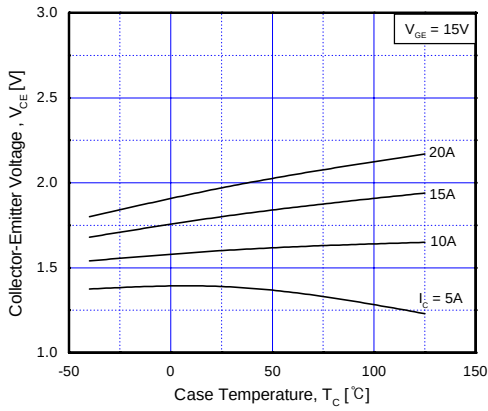


Fig 3. Saturation Voltage vs. Case Temperature at Variant Current Level

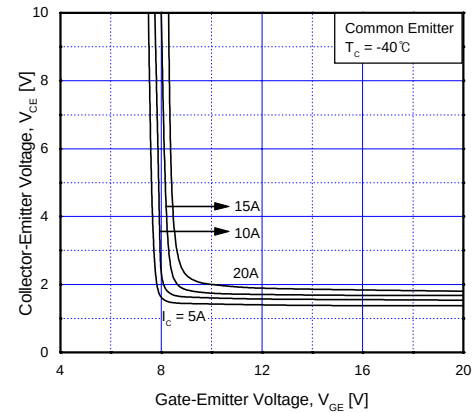


Fig 4. Saturation Voltage vs. V_{GE}

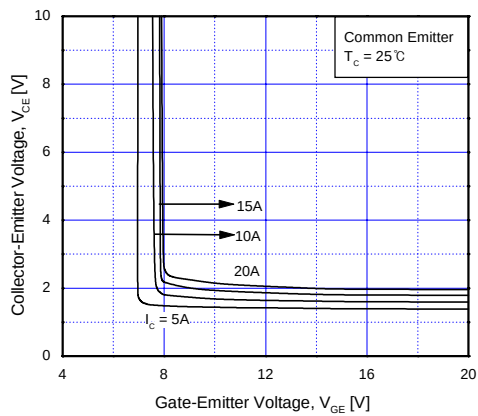


Fig 5. Saturation Voltage vs. V_{GE}

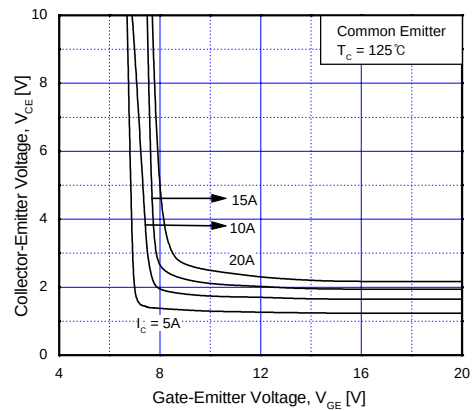


Fig 6. Saturation Voltage vs. V_{GE}

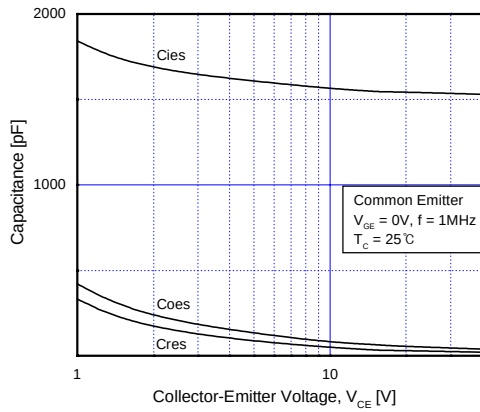


Fig 7. Capacitance Characteristics

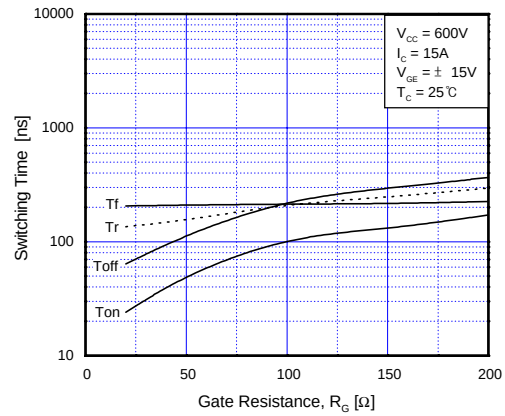


Fig 8. Switching Characteristics vs. Gate Resistance

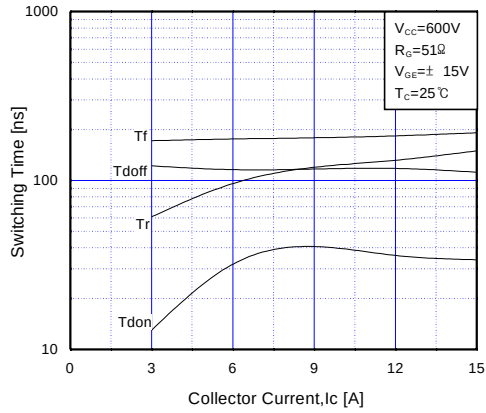


Fig 9. Switching Characteristics vs. Collector current

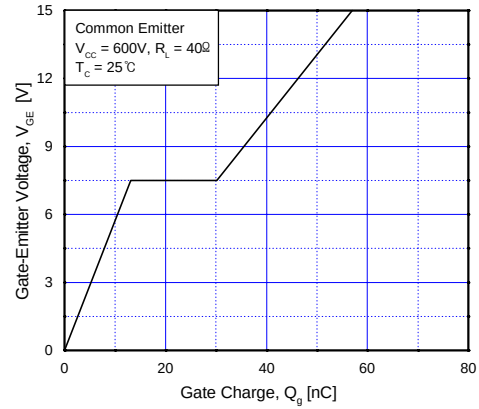


Fig 10. Gate Charge Characteristics

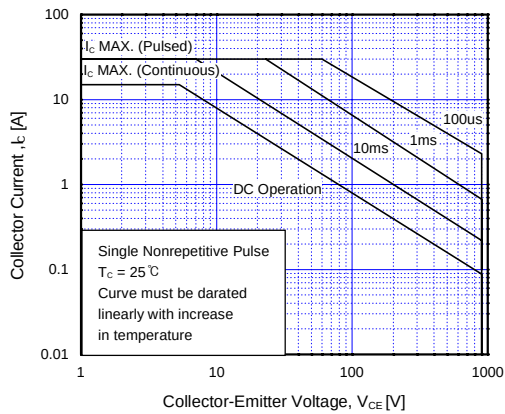


Fig 11. SOA Characteristics

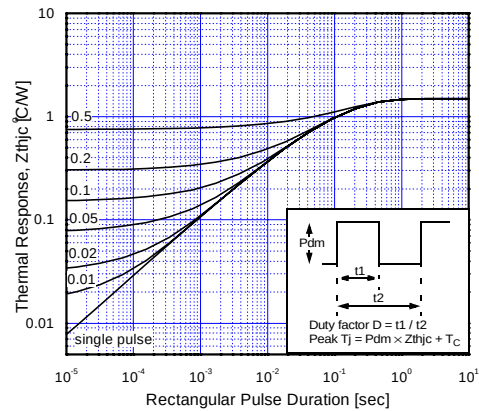
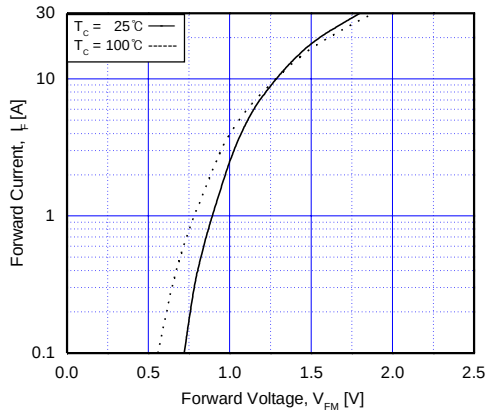
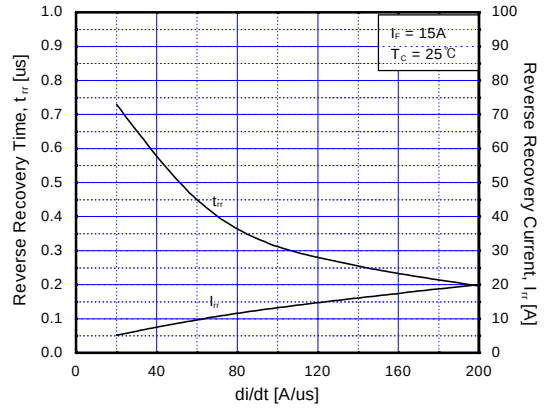
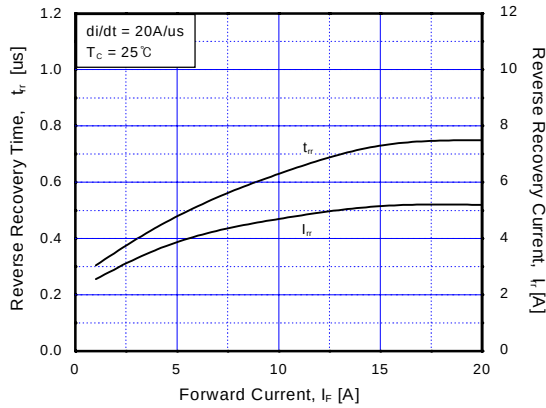
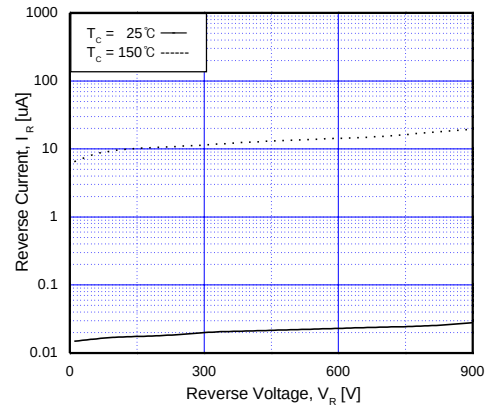
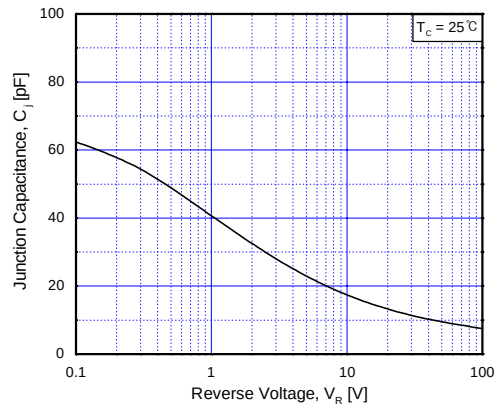


Fig 12. Transient Thermal Impedance of IGBT



SGF15N90D

Technical drawing of a mechanical part showing three views: front, side, and top. The front view shows a rectangular block with a central slot and three vertical pins. The side view shows the profile of the block with a 10-degree chamfer. The top view shows the top surface of the block. Dimensions are given in millimeters with tolerances.

Front View Dimensions:

- Overall width: 15.50 ± 0.20
- Overall height: 26.50 ± 0.20
- Top section height: 4.50 ± 0.20
- Central slot width: 2.00 ± 0.20
- Pin diameter: $\varnothing 3.60 \pm 0.20$
- Pin height: 14.80 ± 0.20
- Pin width: 2.00 ± 0.20
- Pin spacing: 4.00 ± 0.20
- Pin tip radius: $0.75^{+0.20}_{-0.10}$
- Bottom section height: 2.50 ± 0.20
- Material: 5.45TYP [5.45 ± 0.30]

Side View Dimensions:

- Overall height: 22.00 ± 0.20
- Top section height: 5.50 ± 0.20
- Central slot width: 2.00 ± 0.20
- Pin width: 3.30 ± 0.20
- Pin height: 14.80 ± 0.20
- Pin tip radius: $0.75^{+0.20}_{-0.10}$
- Bottom section height: 2.50 ± 0.20
- Material: 5.45TYP [5.45 ± 0.30]

Top View Dimensions:

- Overall width: 15.50 ± 0.20
- Overall height: 26.50 ± 0.20
- Top section height: 4.50 ± 0.20
- Central slot width: 2.00 ± 0.20
- Pin diameter: $\varnothing 3.60 \pm 0.20$
- Pin height: 14.80 ± 0.20
- Pin width: 2.00 ± 0.20
- Pin spacing: 4.00 ± 0.20
- Pin tip radius: $0.75^{+0.20}_{-0.10}$
- Bottom section height: 2.50 ± 0.20
- Material: 5.45TYP [5.45 ± 0.30]

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