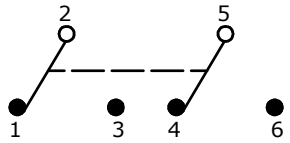
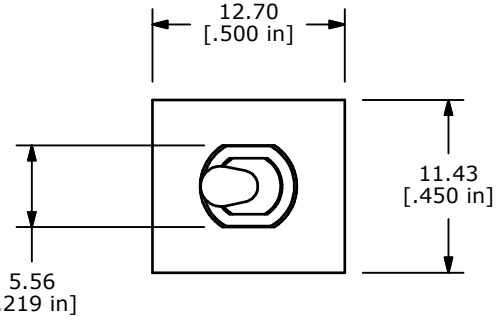


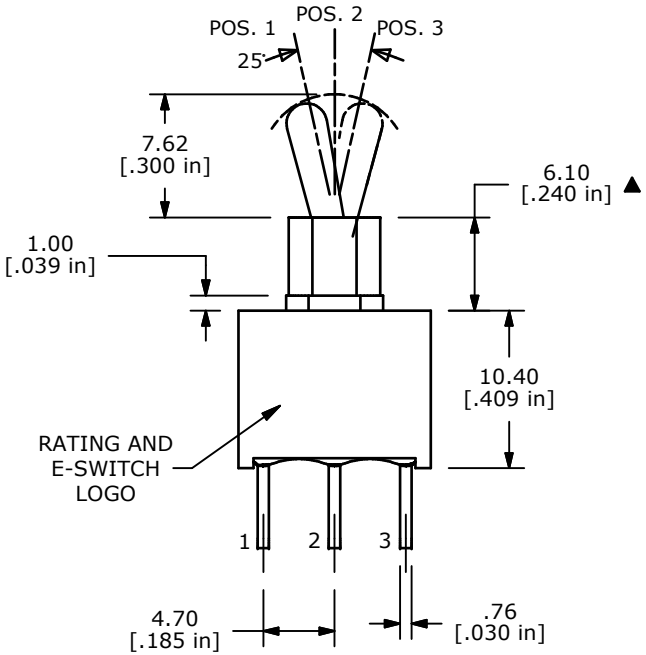
PCB LAYOUT

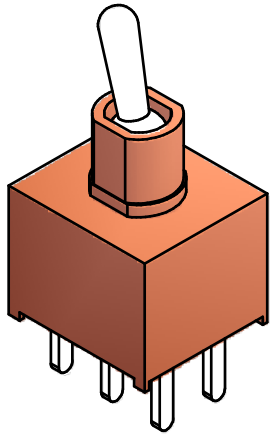
▲ SWITCH FUNCTION		
POS. 1	POS. 2	POS. 3
		
ON	NONE	ON
2-3, 5-6	N/A	2-1, 5-4



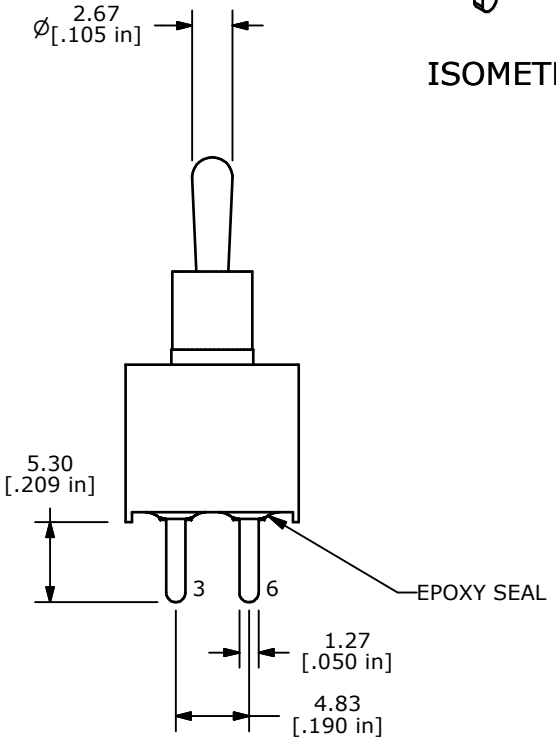
CIRCUIT: DPDT







ISOMETRIC



NOTE:
1. ALL DIMENSIONS IN MM [INCH]
2. GENERAL TOLERANCE: X.X=±0.4
   X.XX=±0.25
3. TERMINAL NUMBERS FOR REF ONLY
▲4. PLATING MATERIAL: GOLD
5. RATING: 0.4 VA @ 20 V (AC or DC)
6. LIFE EXPECTANCY: 30,000 MAKE-AND-BREAK CYCLES AT FULL LOAD.
7. INSULATION RESISTANCE: 1,000 MEGA-OHM MIN.
8. CONTACT RESISTANCE: 10 Milli-OHMS Max.
8. DIELECTRIC STRENGTH: 1,000 V RMS @SEA LEVEL.
9. OPERATING TEMP.: -30°C to 85°C
10. ▲ DENOTES CRITICAL PARAMETER.

REV	PCR NO	DESCRIPTION	DATE	BY
A	18620	RELEASED FOR PRODUCTION	8/25/09	BLR

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TITLE 100AWDP1T2B4M2RE

SCALE	DATE	DR	DWG	REV
2:1	8/25/2009	BLR	T102276	A