

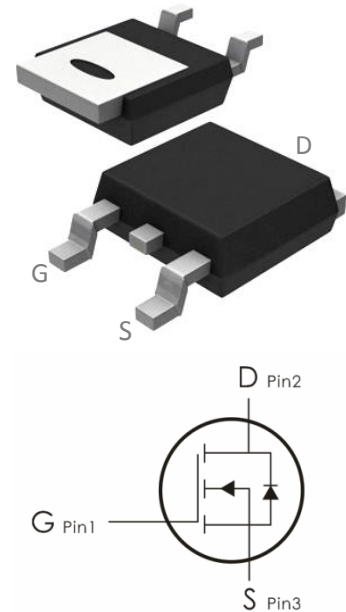
Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=40V, I_D=80A, R_{DS(on)} < 7m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current- $T_C=25^\circ C$	80	A
	Continuous Drain Current- $T_C=100^\circ C$	56	
	Pulsed Drain Current	350	
E_{AS}	Single Pulse Avalanche Energy ⁵	750	mJ
P_D	Power Dissipation	80	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case ²	1.88	$^\circ C/W$

Electrical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	40	45	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =40V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics ³						
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1.2	1.8	2.5	V
R _{DS(ON)}	Drain-Source On Resistance	V _{GS} =10V, I _D =20A	---	---	7	m Ω
G _{FS}	Forward Transconductance	V _{DS} =10V, I _D =20A	15	---	---	S
Dynamic Characteristics ⁴						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	5000	---	pF
C _{oss}	Output Capacitance		---	900	---	
C _{rss}	Reverse Transfer Capacitance		---	500	---	
Switching Characteristics ⁴						
t _{d(on)}	Turn-On Delay Time	V _{DD} =20V, R _L =1Ω V _{GS} =10V, R _G =3Ω	---	12	---	ns
t _r	Rise Time		---	11	---	ns
t _{d(off)}	Turn-Off Delay Time		---	39	---	ns
t _f	Fall Time		---	12	---	ns
Q _g	Total Gate Charge	V _{GS} =20V, V _{DS} =10V, I _D =20A	---	61	---	nC
Q _{gs}	Gate-Source Charge		---	15.3	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	14.5	---	nC
Drain-Source Diode Characteristics						
V _{SD}	Source-Drain Diode Forward Voltage ³	V _{GS} =0V, I _S =10A	---	---	1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_J=25^\circ\text{C}$, $V_{DD}=20\text{V}$, $V_G=10\text{V}$, $L=1\text{mH}$, $R_g=25\Omega$, $I_{AS}=42\text{A}$.

Typical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

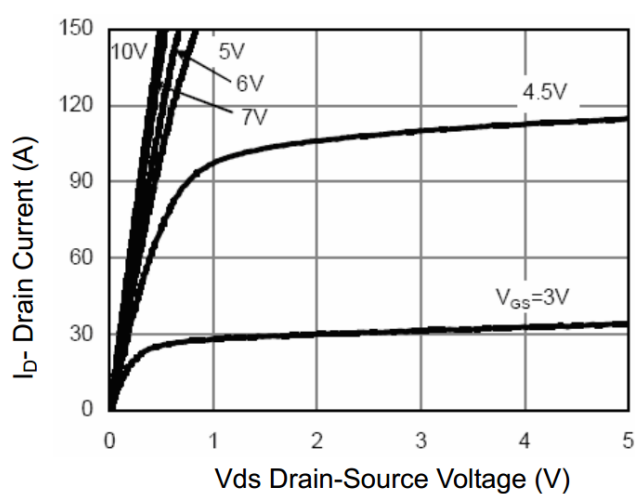


Figure 1 Output Characteristics

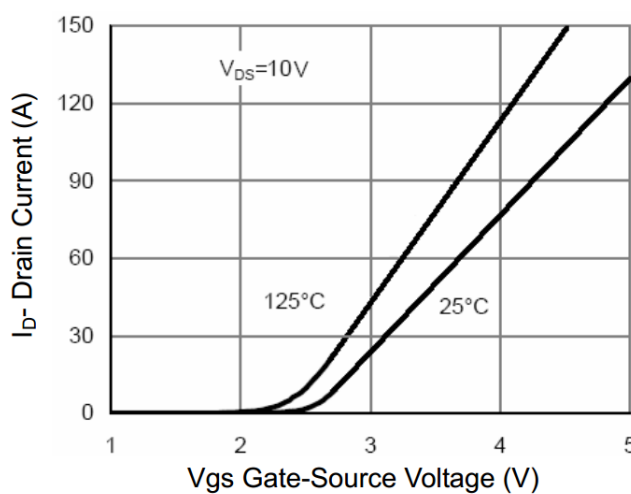


Figure 2 Transfer Characteristics

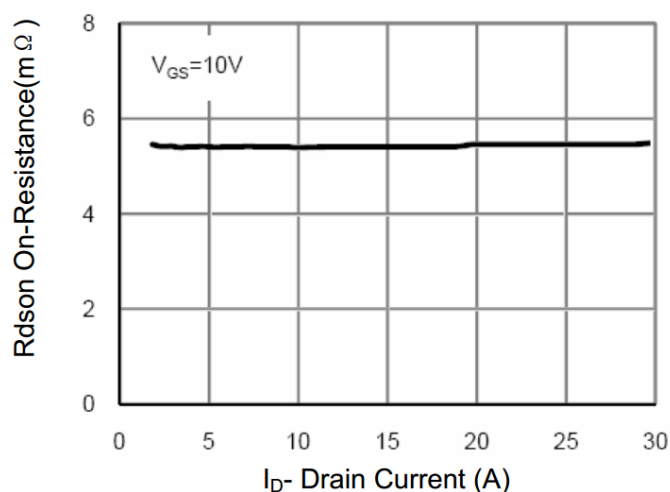


Figure 3 Rdson- Drain Current

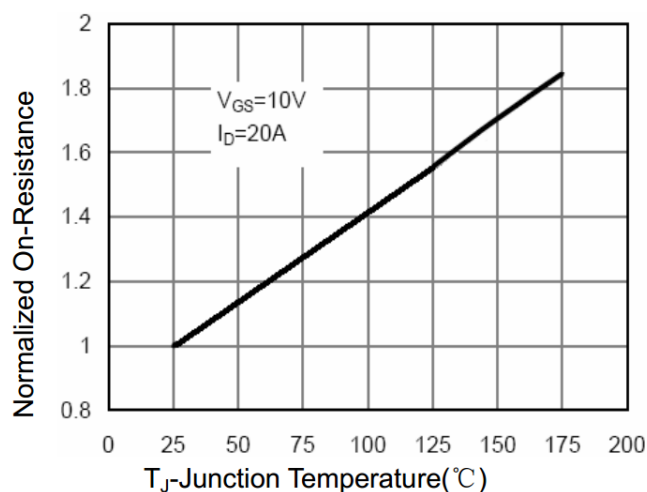


Figure 4 Rdson-Junction Temperature

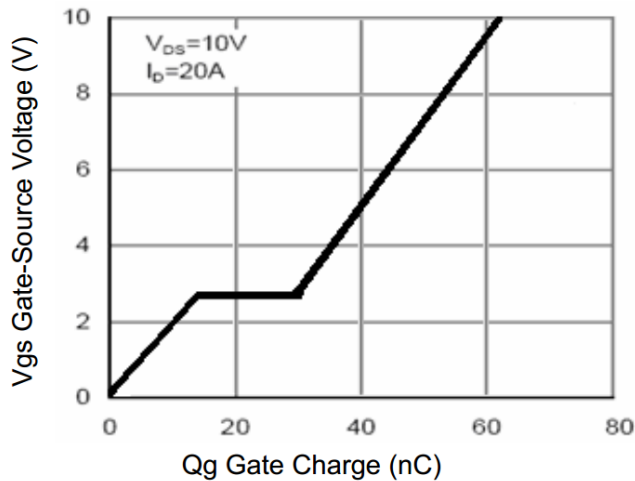


Figure 5 Gate Charge

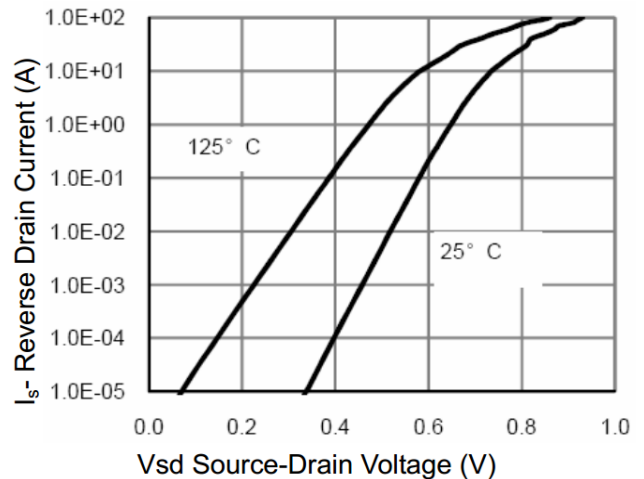


Figure 6 Source- Drain Diode Forward

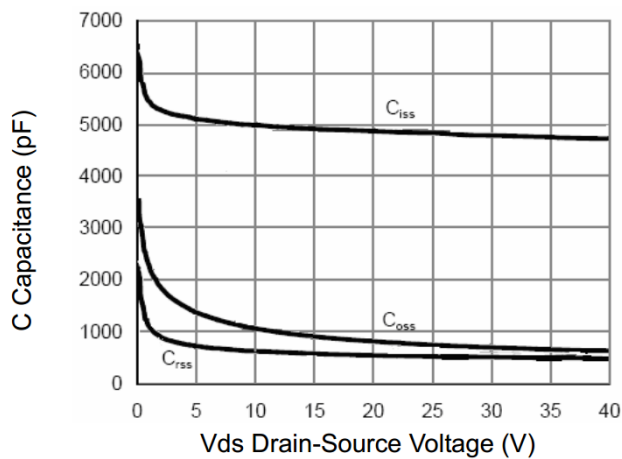


Figure 7 Capacitance vs Vds

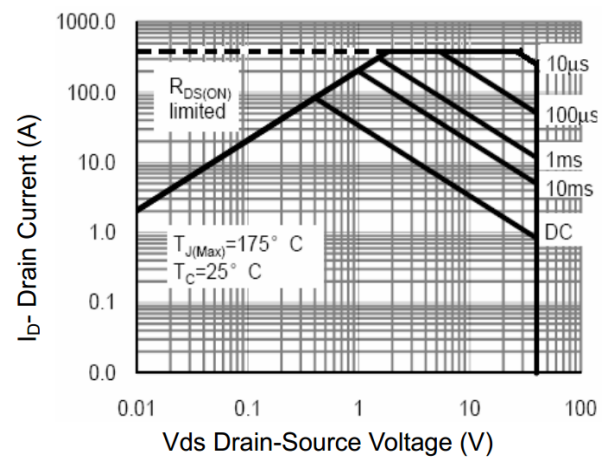


Figure 8 Safe Operation Area

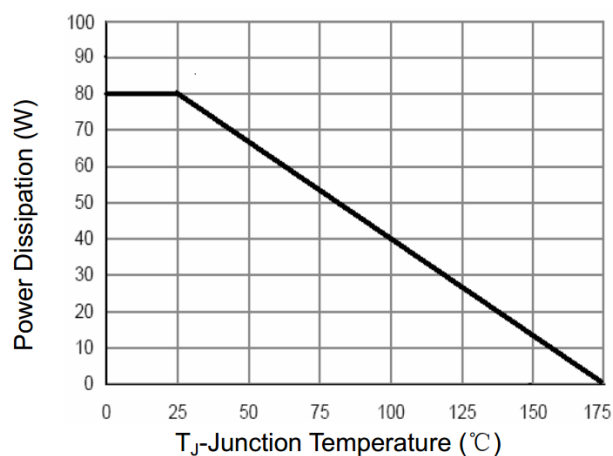


Figure 9 Power De-rating

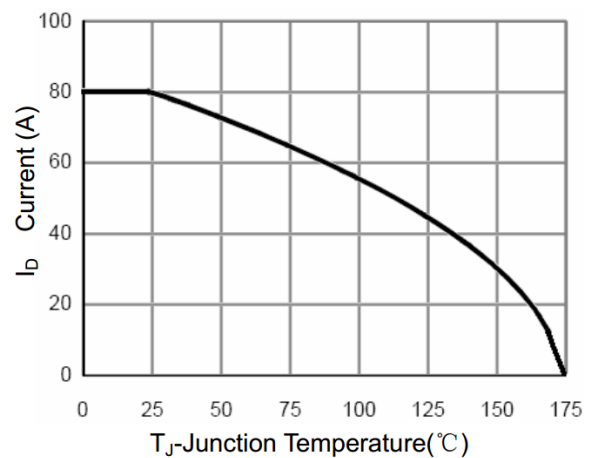


Figure 10 ID Current- Junction Temperature

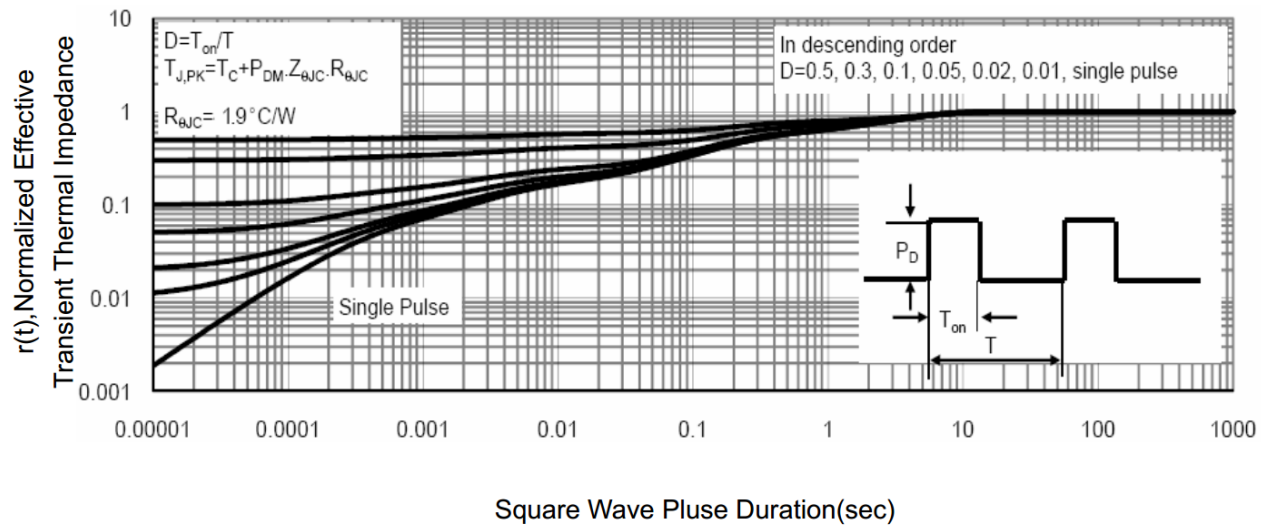


Figure 11 Normalized Maximum Transient Thermal Impedance



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