Appendix A - ATtiny87/ATtiny167 Automotive Specification at 150°C

DATASHEET

Description

This document contains information specific to devices operating at temperatures up to 150°C. Only deviations are covered in this appendix, all other information can be found in the complete Automotive datasheet. The complete Automotive datasheet can be found on http://www.atmel.com

1. Electrical Characteristics

1.1 Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameters	Test Conditions	Unit
Operating temperature	–55 to +150	°C
Storage temperature	–65 to +175	°C
Voltage on any pin except $\overline{\text{RESET}}$ with respect to ground	–0.5 to V_{CC} + 0.5	V
Voltage on $\overline{\text{RESET}}$ with respect to ground	–0.5 to +13.0	V
Maximum operating voltage	6.0	V
DC current per I/O pin DC current V_{CC} and GND	30 200.0	mA

1.2 DC Characteristics

T_A = –40°C to +150°C, V_{CC} = 4.5V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Input low voltage, except XTAL1 and $\overline{\text{RESET}}$ pin	V_{CC} = 4.5V – 5.5V	V_{IL}	–0.5		$+0.2V_{CC}$[1]	V
Input high voltage, except XTAL1 and $\overline{\text{RESET}}$ pins	V_{CC} = 4.5V – 5.5V	V_{IH}	$0.6V_{CC}$[2]		V_{CC} + 0.5	V
Input low voltage, XTAL1 pin	V_{CC} = 4.5V – 5.5V	V_{IL1}	–0.5		$+0.1V_{CC}$[1]	V
Input high voltage, XTAL1 pin	V_{CC} = 4.5V – 5.5V	V_{IH1}	$0.7V_{CC}$[2]		V_{CC} + 0.5	V
Input low voltage, $\overline{\text{RESET}}$ pin	V_{CC} = 4.5V – 5.5V	V_{IL2}	–0.5		$0.2V_{CC}$[1]	V
Input high voltage, $\overline{\text{RESET}}$ pin	V_{CC} = 4.5V – 5.5V	V_{IH2}	$0.9V_{CC}$[2]		V_{CC} + 0.5	V
Input low voltage, $\overline{\text{RESET}}$ pin as I/O	V_{CC} = 4.5V – 5.5V	V_{IL3}	–0.5		$0.2V_{CC}$[1]	V
Input high voltage, $\overline{\text{RESET}}$ pin as I/O	V_{CC} = 4.5V – 5.5V	V_{IH3}	$0.8V_{CC}$[2]		V_{CC} + 0.5	V
Output low voltage[3], I/O pin except RESET	I_{OL} = 10mA, V_{CC} = 5V	V_{OL}			0.8	V
Output high voltage[4], I/O pin except RESET	I_{OH} = –10mA, V_{CC} = 5V	V_{OH}	4.0			V

Notes:
1. “Max” means the highest value where the pin is guaranteed to be read as low
2. “Min” means the lowest value where the pin is guaranteed to be read as high
3. Although each I/O port can sink more than the test conditions (20mA at V_{CC} = 5V) under steady state conditions (non-transient), the following must be observed:
   1] The sum of all IOL, for all ports, should not exceed 400mA.
   2] The sum of all IOL, for ports C0 - C5, should not exceed 200mA.
   3] The sum of all IOL, for ports C6, D0 - D4, should not exceed 300mA.
   4] The sum of all IOL, for ports B0 - B7, D5 - D7, should not exceed 300mA.
   If IOL exceeds the test condition, VOL may exceed the related specification. Pins are not guaranteed to sink current greater than the listed test condition.
4. For temperature range +125°C to +150°C only. For –40°C to +125°C, refer to ATtiny167 automotive datasheet.

1.2 DC Characteristics (Continued)

T_A = –40°C to +150°C, V_{CC} = 4.5V to 5.5V (unless otherwise noted) (Continued)

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Input leakage current I/O pin	V_{CC} = 5.5V, pin low (absolute value)	I_{IL}			1	µA
Input leakage current I/O Pin	V_{CC} = 5.5V, pin high (absolute value)	I_{IH}			1	µA
Reset pull-up resistor		R_{RST}	35		65	kΩ
I/O pin pull-up resistor		R_{PU}	20		50	kΩ
Power supply current active mode	16MHz, V_{CC} = 5V	I_{CC}		10	15	mA
	8MHz, V_{CC} = 5V	I_{CC}		5.5	8.0	mA
	8MHz, V_{CC} = 3V	I_{CC}		2.8	4.0	mA
	4MHz, V_{CC} = 3V	I_{CC}		1.8	3.0	mA
Power supply current idle mode	16MHz, V_{CC} = 5V	I_{CC}		3.5	6.0	mA
	8MHz, V_{CC} = 5V	I_{CC}		1.8	3.0	mA
	8MHz, V_{CC} = 3V	I_{CC}		1.0	2.0	mA
	4MHz, V_{CC} = 3V	I_{CC}		0.5	1.0	mA
Power supply current power-down mode	WDT enabled, V_{CC} = 5V	I_{CC}		7	150	µA
	WDT disabled, V_{CC} = 5V	I_{CC}		0.18	150	µA
	WDT enabled, V_{CC} = 3V	I_{CC}		5	150	µA
	WDT disabled, V_{CC} = 3V	I_{CC}		0.15	100	µA
Analog comparator input leakage current	V_{CC} = 5V V_{in} = V_{CC}/2	I_{ACLK}	–50		+50	nA
Analog comparator propagation delay	V_{CC} = 4.0V	t_{ACPD}		180		ns

Notes:
1. “Max” means the highest value where the pin is guaranteed to be read as low
2. “Min” means the lowest value where the pin is guaranteed to be read as high
3. Although each I/O port can sink more than the test conditions (20mA at V_{CC} = 5V) under steady state conditions (non-transient), the following must be observed:
   1] The sum of all IOL, for all ports, should not exceed 400mA.
   2] The sum of all IOL, for ports C0 - C5, should not exceed 200mA.
   3] The sum of all IOL, for ports C6, D0 - D4, should not exceed 300mA.
   4] The sum of all IOL, for ports B0 - B7, D5 - D7, should not exceed 300mA.
   If IOL exceeds the test condition, VOL may exceed the related specification. Pins are not guaranteed to sink current greater than the listed test condition.
4. For temperature range +125°C to +150°C only. For –40°C to +125°C, refer to ATtiny167 automotive datasheet.

1.3 ADC Characteristics

T_A = –40°C to +150°C, V_{CC} = 4.5V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Resolution	Single ended			10		Bit
Absolute accuracy	V_{CC} = 4V, V_{REF} = 4V, ADC clock = 200kHz	TUE		2.0	3.5	LSB
Integral non linearity	V_{CC} = 4V, V_{REF} = 4V, ADC clock = 200kHz	INL		0.6	2.0	LSB
Differential non linearity	V_{CC} = 4V, V_{REF} = 4V, ADC clock = 200kHz, Temp = –40°C to +150°C	DNL		0.4	1.5	LSB
Gain error	V_{CC} = 4V, V_{REF} = 4V, ADC clock = 200kHz, Temp = –40°C to +150°C		–6.0	–2.5	+2.0	LSB
Offset error	V_{CC} = 4V, V_{REF} = 4V, ADC clock = 200kHz, Temp = –40°C to +150°C		–3.5	+1.5	+3.5	LSB
Ref voltage		V_{REF}	2.56		AVCC	V

1.4 Memory Endurance

EEPROM endurance: 50,000 write/erase cycles.

Flash endurance: 10,000 write/erase cycles.

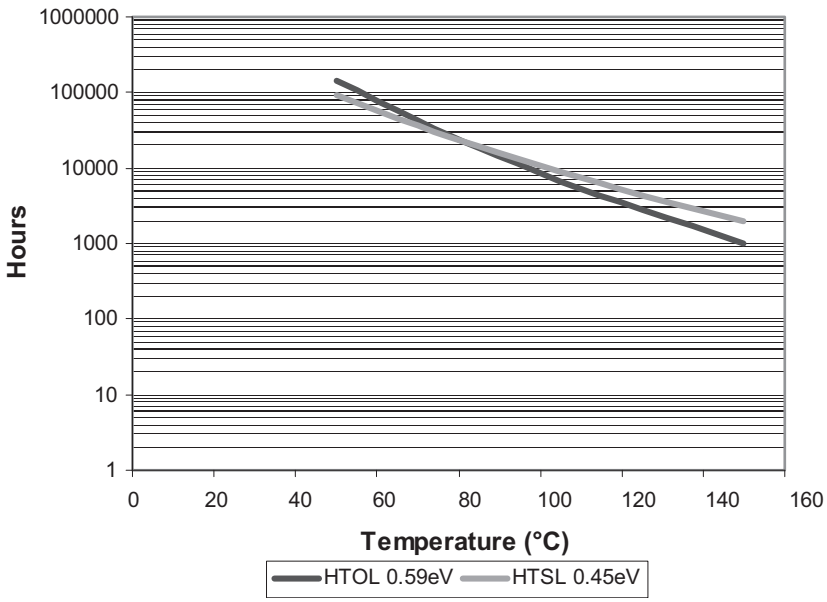
2. Grade 0 Qualification

The ATtiny87/ATtiny167 has been developed and manufactured according to the most stringent quality assurance requirements of ISO-TS-16949 and verified during product qualification as per AEC-Q100 grade 0.

AEC-Q100 qualification relies on temperature accelerated stress testing. High temperature field usage however may result in less significant stress test acceleration. In order to prevent the risk that ATtiny87/ATtiny167 lifetime would not satisfy the application end-of-life reliability requirements, Atmel® has extended the testing, whenever applicable (high temperature operating life test, high temperature storage life, data retention, thermal cycles), far beyond the AEC-Q100 requirements. Thereby, Atmel verified the ATtiny87/ATtiny167 has a long safe lifetime period after the grade 0 qualification acceptance limits.

The valid domain calculation depends on the activation energy of the potential failure mechanism that is considered. Therefore any temperature mission profile which could exceed the AEC-Q100 equivalence domain shall be submitted to Atmel for a thorough reliability analysis

Figure 2-1. AEC-Q100 Lifetime Equivalence



3. Ordering Information

Speed (MHz)	Power Supply	Ordering Code	Package[1]	Operation Range
16[2]	4.5V - 5.5V	ATtiny87-A15MD	PN	Extended (–40°C to +150°C)
16[2]	4.5V - 5.5V	ATtiny87-A15XD	6G	Extended (–40°C to +150°C)
16[2]	4.5V - 5.5V	ATtiny167-A15MD	PN	Extended (–40°C to +150°C)
16[2]	4.5V - 5.5V	ATtiny167-A15XD	6G	Extended (–40°C to +150°C)

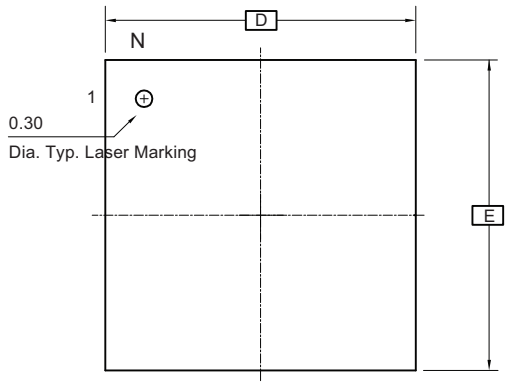
Notes:
1. Pb-free packaging, complies to the european directive for restriction of hazardous substances (RoHS directive). Also halide free and fully green.
2. For speed versus V_{cc}, see complete datasheet.

4. Package Information

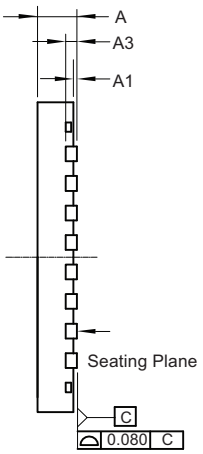
Package Type	
PN	32-pad, 5 × 5 × 1.0mm body, lead pitch 0.50mm, quad flat no-lead/micro lead frame package (QFN/MLF): E2/D2 3.1 ±0.1mm
6G	20-leads, 4.4 × 6.5mm body - 0.65mm pitch - lead length: 0.6mm, thin shrink small outline package (TSSOP)

Figure 4-1. PN

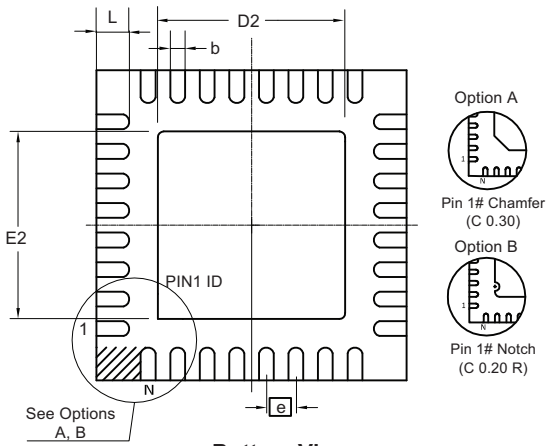
Drawings not scaled



Top View



Side View



Bottom View

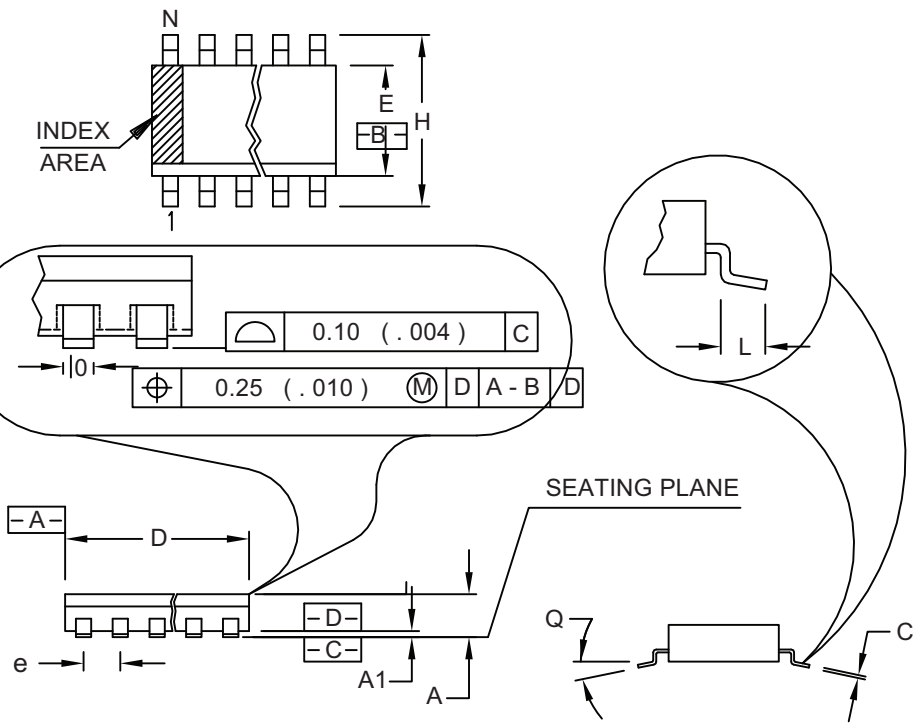
COMMON DIMENSIONS (Unit of Measure = mm)				
Symbol	**MIN**	**NOM**	**MAX**	**NOTE**
A	0.80	0.85	0.90	
A1	0.00	----	0.05	
A3		0.20 REF		
D/E		5.00 BSC		
D2/E2	3.00	3.10	3.20	
L	0.30	0.40	0.50	
b	0.18	0.25	0.30	2
e		0.50 BSC		
n		32		

Notes: 1. This drawing is for general information only. Refer to JEDEC Drawing MO-220, Variation VHHD-2, for proper dimensions, tolerances, datums, etc.
2. Dimensions b applies to metallized terminal and is measured between 0.15mm and 0.30mm from the terminal tip. If the terminal has the optical radius on the other end of the terminal, the dimensions should not be measured in that radius area.

01/31/12

Atmel **Package Drawing Contact:** packagedrawings@atmel.com	TITLE	GPC	DRAWING NO.	REV.
	PN, 32 Leads - 0.50mm Pitch, 5x5mm Very Thin Quad Flat no Lead Package (VQFN) Sawn	ZMF	PN	I

Figure 4-2. 6G



	MM		INCH	
A	----	1.10	----	.043
A1	0.05	0.15	.002	.006
b	0.19	0.30	.007	.012
C	0.09	0.20	.003	.008
D	6.40	6.60	.252	.260
E	4.30	4.50	.169	.177
e	0.65 BSC		.026 BSC	
H	6.40 BSC		.252 BSC	
L	0.50	0.70	.020	.028
N	20		20	
Q	0° ~8°		0° ~8°	

20/12/07

Atmel Package Drawing Contact: packagedrawings@atmel.com	TITLE	GPC	DRAWING NO.	REV.
	6G, 20 Leads - 4.4x6.5mm Body - 0.65mm Pitch - Lead length: 0.6mm THIN SHRINK SMALL OUTLINE		6G	A

5. Revision History

Please note that the following page numbers referred to in this section refer to the specific revision mentioned, not to this document.

Revision No.	History
7792D-AVR-08/14	• Put datasheet in the latest template
7792C-AVR-01/14	• Section 3 “Ordering Information” on page 6 updated
7792B-AVR-05/09	• Section 1 “Electrical Characteristics” on pages 2 to 4 updated





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