EVALUATION KIT AVAILABLE

MAX44281

Ultra-Small, Ultra-Thin, 4-Bump Op Amp

General Description

The MAX44281 is the industry's first op amp in a 4-bump WLP package, designed for use in portable consumer and medical applications. This device is offered as a noninverting amplifier with gain (A_V) of +1V/V, +2V/V, or +10V/V.

The device features rail-to-rail output, low 100µV input voltage offset, and 15MHz of bandwidth with only 700µA of supply current.

The device features shutdown functionality by pulling V_{DD} to ground. During shutdown, both the input and the output are high impedance.

The device is available in an ultra-small, 0.86mm x 0.86mm, 4-bump WLP package with 0.5mm height. It is specified over the -40°C to +125°C temperature range.

Applications

3G/4G Handsets

Sensor Interfaces

Portable Medical Devices

Battery-Powered Devices

General-Purpose Signal Processing

Notebooks

Transimpedance Amplifiers

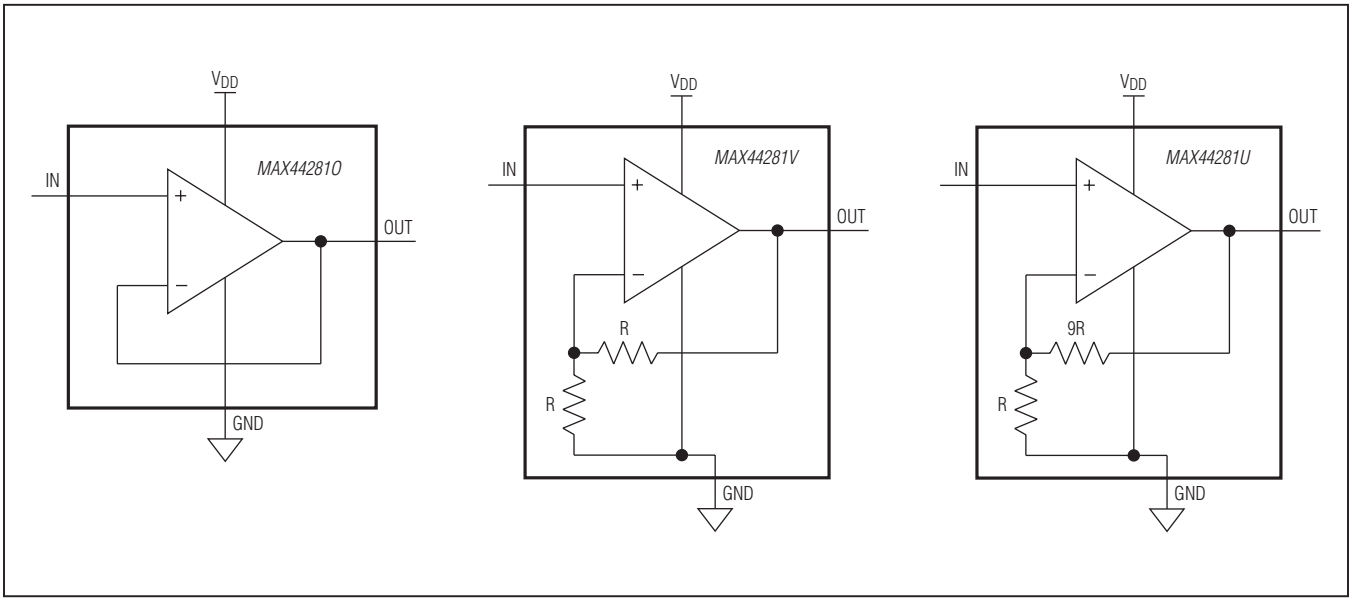
Benefits and Features

- **0.86mm x 0.86mm 4-Bump Thin WLP Package**
- **Low 100µV Input Offset Voltage**
- **Rail-to-Rail Output**
- **15MHz GBW with Only 700µA Supply Current**
- **Shutdown Input Through V_{DD}**
- **Ultra-Low 10pA Input Bias Current**
- **1.8V to 5.5V Wide Supply Range**
- **Three Gain Versions Available**
  - **1V/V (MAX44281O)**
  - **2V/V (MAX44281V)**
  - **10V/V (MAX44281U)**

***Ordering Information* appears at end of data sheet.**

For related parts and recommended products to use with this part, refer to **www.maximintegrated.com/MAX44281.related**.

Functional Diagrams



For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

19-6541; Rev 0; 12/12

ABSOLUTE MAXIMUM RATINGS

V_{DD} to GND .....-0.3V to +6V
IN to GND.....-0.3V to +6V
OUT to GND.....(V_{GND} - 0.3V) to (V_{DD} + 0.3V)
Continuous Power Dissipation (T_A = +70°C)
WLP (derate 9.7mW/°C above +70°C).....303mW

Maximum Continuous Current into Any Pin .....≤ 20mA
Operating Temperature Range..... -40°C to +125°C
Storage Temperature Range..... -65°C to +150°C
Soldering Temperature (reflow) .....+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

PACKAGE THERMAL CHARACTERISTICS (Note 1)

WLP
Junction-to-Ambient Thermal Resistance (θ_{JA}) .....103°C/W

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to **www.maximintegrated.com/thermal-tutorial**.

ELECTRICAL CHARACTERISTICS

(V_{DD} = 3.3V, V_{GND} = 0V, R_L = 10kΩ to V_{DD}/2, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
DC PERFORMANCE (Note 1)							
Input Voltage Range (Note 3)	V_{IN}	A_V = 1V/V (MAX44281O)		0.1		V_{DD} - 0.1	V
		A_V = 2V/V (MAX44281V)		0.05		(V_{DD} - 0.1)/2	
		A_V = 10V/V (MAX44281U)		0.01		(V_{DD} - 0.1)/10	
Gain Error	GE	A_V = 1V/V (MAX44281O)			0.01	0.02	%
		A_V = 2V/V (MAX44281V)			0.2	0.6	
		A_V = 10V/V (MAX44281U)			0.3	0.7	
Input Offset Voltage (Note 2)	V_{OS}	V_{IN} = 150mV, no load	A_V = 1		20	100	µV
			A_V = 2V/V		20	100	
			A_V = 10V/V		20	100	
Input Offset Voltage Drift (Note 2)	V_{OS} - TC				2	9	µV/°C
Input Bias Current (Note 2)	I_B	T_A = +25°C (Note 2)			1	10	pA
		T_A = -40°C to +125°C				2000	
Output Short-Circuit Current	I_{SC}	OUT to V_{DD} or GND			50		mA
Output Voltage Low	V_{OL}	R_{OUT} = 10kΩ				0.02	V
		R_{OUT} = 600Ω				0.1	

ELECTRICAL CHARACTERISTICS (continued)

(V_{DD} = 3.3V, V_{GND} = 0V, R_L = 10kΩ to V_{DD}/2, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

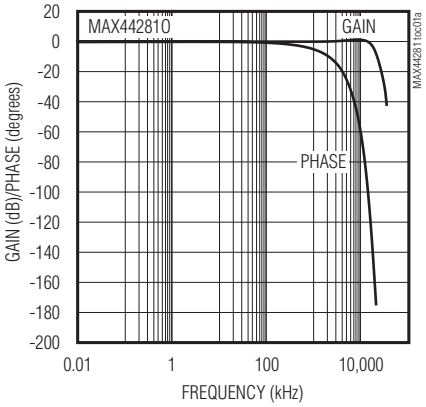
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Output Voltage High	V_{OH}	R_{OUT} = 10kΩ		V_{DD} – 0.1			V
		R_{OUT} = 600Ω		V_{DD} – 0.1			
Input Voltage-Noise Density	e_n	A_V = 1V/V, f = 10kHz			15		nV/√Hz
		A_V = 2V/V, f = 10kHz			19		
		A_V = 10V/V, f = 10kHz			16		
Gain Bandwidth	GBW				15		MHz
Slew Rate	SR				8		V/µs
Capacitive Load	C_{LOAD}	No sustained oscillations			100		pF
POWER-SUPPLY CHARACTERISTICS							
Power-Supply Range	V_{DD}	Guaranteed by PSRR	T_A = -40°C to +125°C	1.8		5.5	V
			T_A = 0°C to +70°C	1.7		5.5	
Power-Supply Rejection Ratio	PSRR			75	100		dB
Supply Current	I_{CC}	V_{OUT} = V_{DD}/2, no load			700	1200	µA
Shutdown Supply Current	I_{SHDN}					1	µA
Shutdown Threshold	V_{IL}					0.5	V
Turn-On Time	t_{ON}	T_A = +25°C			10	20	ms
		T_A = -40°C to +125°C			10	30	

Note 1: All devices are 100% production tested at T_A = +25°C. Specifications over temperature limits are guaranteed by design.
Note 2: Guaranteed by design.
Note 3: Guaranteed by gain accuracy specifications.

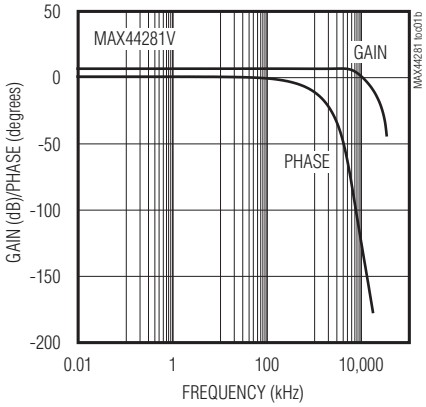
Typical Operating Characteristics

(T_A = +25°C, unless otherwise noted.)

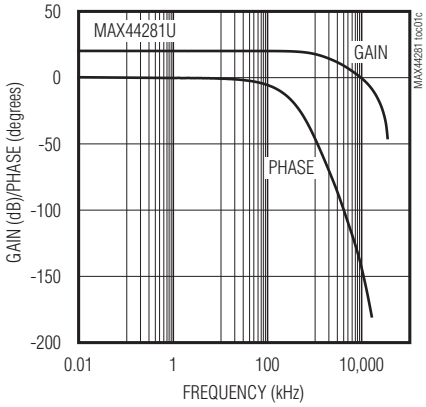
GAIN AND PHASE vs. FREQUENCY (NO LOAD)



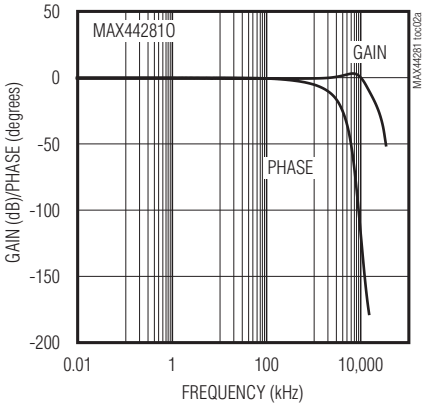
GAIN AND PHASE vs. FREQUENCY (NO LOAD)



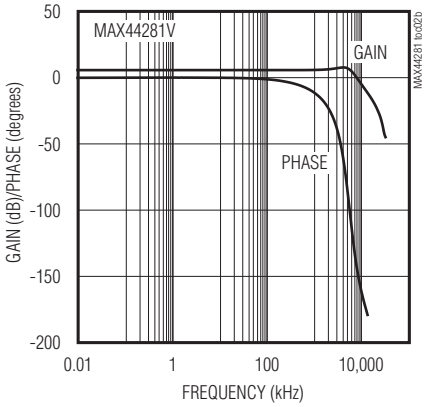
GAIN AND PHASE vs. FREQUENCY (NO LOAD)



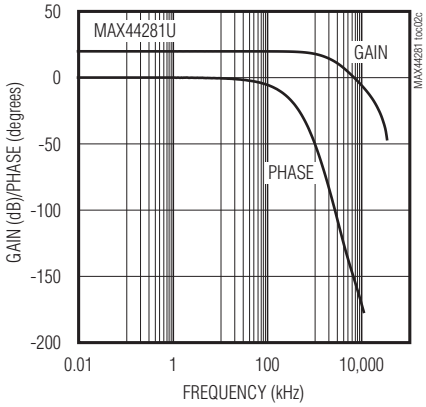
GAIN AND PHASE vs. FREQUENCY (C_L = 100pF)



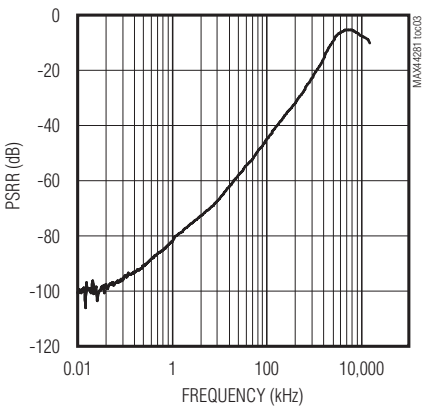
GAIN AND PHASE vs. FREQUENCY (C_L = 100pF)



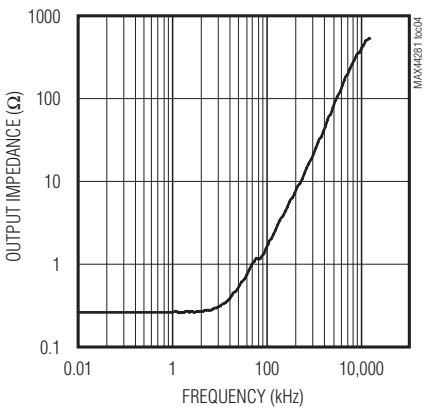
GAIN AND PHASE vs. FREQUENCY (C_L = 100pF)



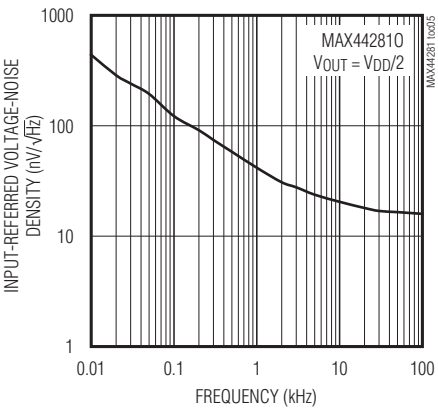
POWER-SUPPLY REJECTION RATIO vs. FREQUENCY



OUTPUT IMPEDANCE vs. FREQUENCY



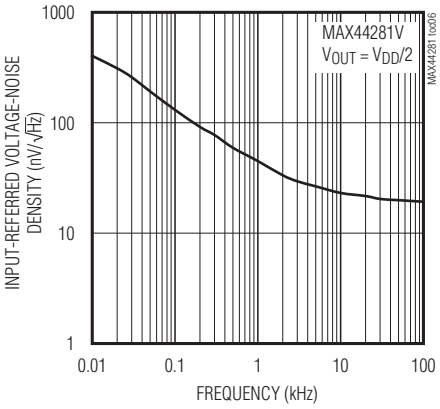
INPUT-REFERRED VOLTAGE-NOISE DENSITY vs. FREQUENCY



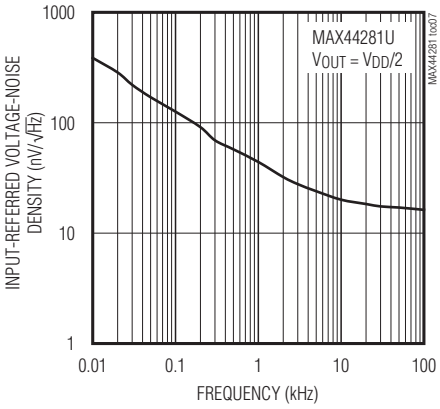
Typical Operating Characteristics (continued)

(T_A = +25°C, unless otherwise noted.)

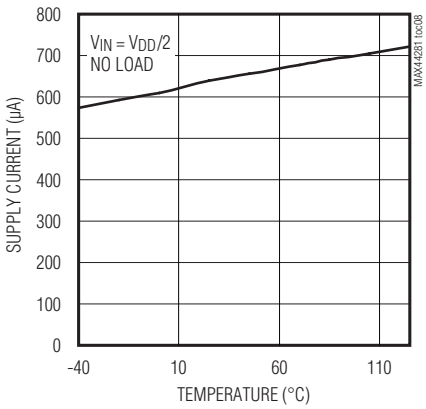
INPUT-REFERRED VOLTAGE-NOISE DENSITY vs. FREQUENCY



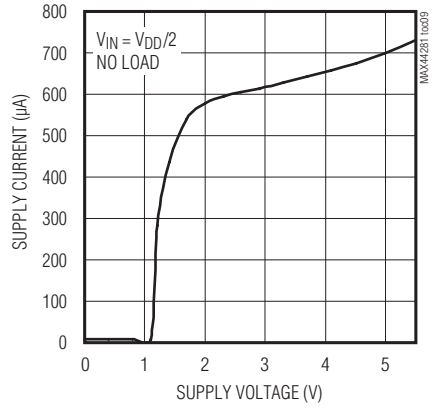
INPUT-REFERRED VOLTAGE-NOISE DENSITY vs. FREQUENCY



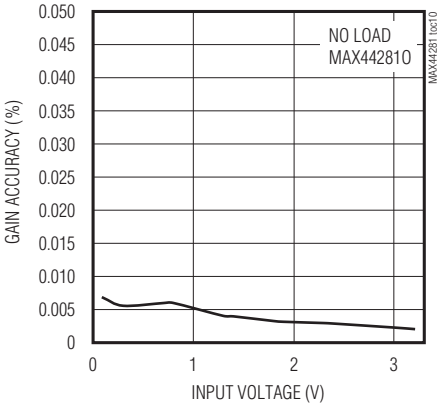
SUPPLY CURRENT vs. TEMPERATURE



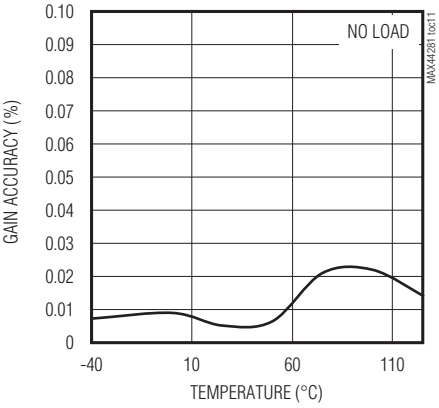
SUPPLY CURRENT vs. SUPPLY VOLTAGE



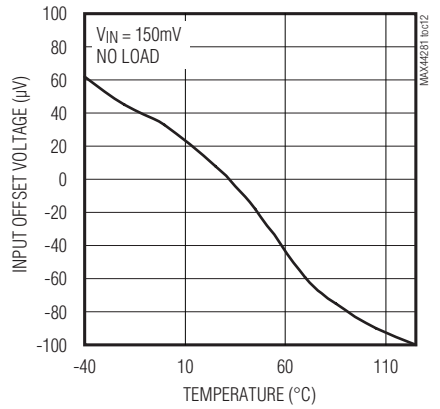
GAIN ACCURACY vs. INPUT VOLTAGE



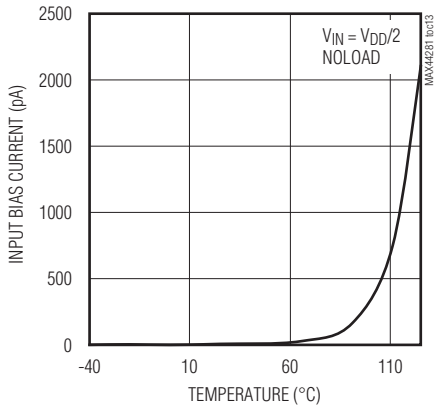
GAIN ACCURACY vs. TEMPERATURE



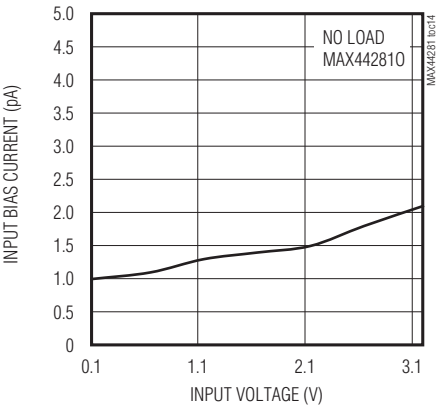
INPUT OFFSET VOLTAGE vs. TEMPERATURE



INPUT BIAS CURRENT vs. TEMPERATURE



INPUT BIAS CURRENT vs. INPUT VOLTAGE

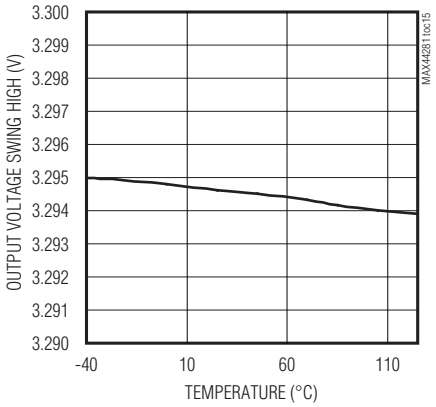


Ultra-Small, Ultra-Thin, 4-Bump Op Amp

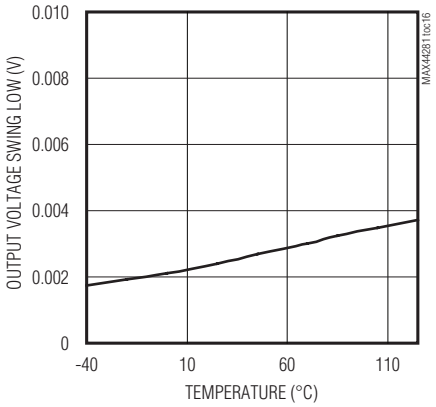
Typical Operating Characteristics

(T_A = +25°C, unless otherwise noted.)

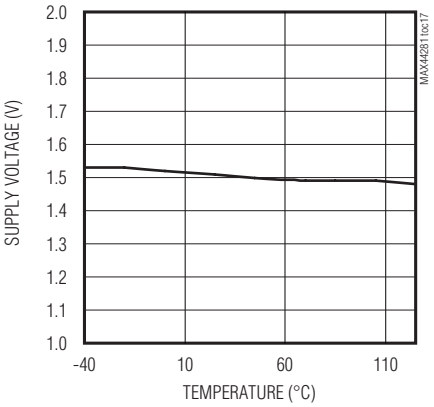
OUTPUT VOLTAGE SWING HIGH vs. TEMPERATURE



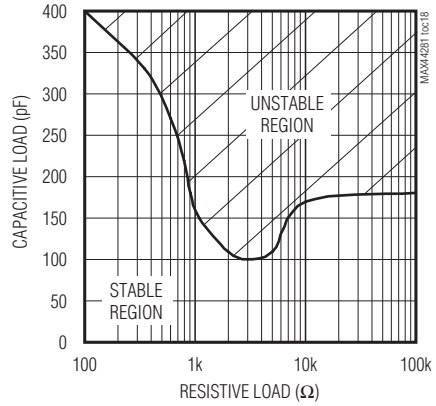
OUTPUT VOLTAGE SWING LOW vs. TEMPERATURE



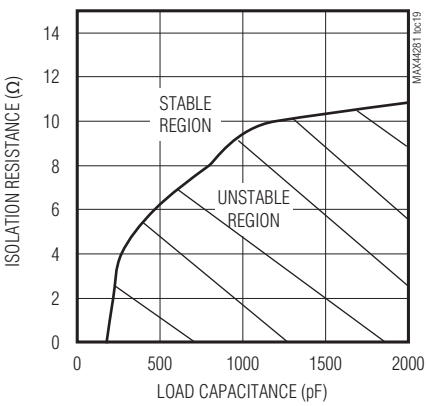
MINIMUM OPERATING VOLTAGE vs. TEMPERATURE



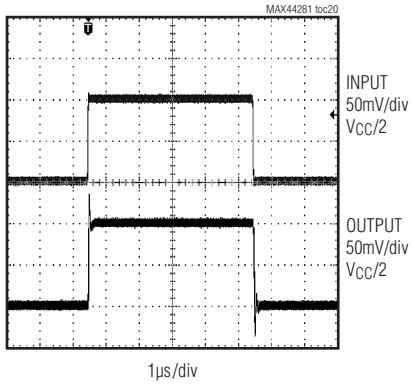
CAPACITIVE-LOAD STABILITY vs. RESISTIVE LOAD



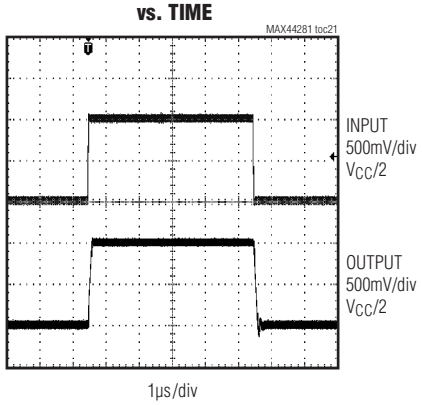
ISOLATION RESISTANCE vs. CAPACITIVE-LOAD STABILITY



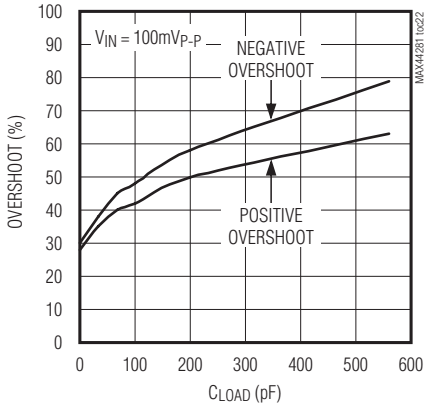
SMALL-SIGNAL STEP RESPONSE vs. TIME



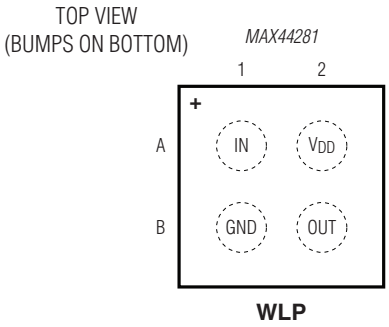
LARGE-SIGNAL STEP RESPONSE vs. TIME



INPUT OFFSET VOLTAGE vs. TEMPERATURE



Bump Configuration



WLP

Bump Description

BUMP	NAME	FUNCTION
A1	IN	Input
A2	V_{DD}	Positive Supply Voltage
B1	GND	Ground
B2	OUT	Output

Detailed Description

Rail-to-Rail Output Stage

The MAX44281 is a high-bandwidth, low-power op amp in the smallest 4-bump WLP industry package. Its combination of precision, low-noise, and low-power specifications makes it ideal for use in a wide variety of sensor interface applications in portable consumer and medical equipment.

Shutdown Mode

The MAX44281 features shutdown functionality by pulling V_{DD} to ground. During shutdown, both the input and the output are high-impedance. The input and output of the MAX44281 enter a high-impedance state when the device enters shutdown. Since shutdown is triggered by lowering the supply voltage to below 0.5V, the ESD protection diodes at the output limit the high-Z voltage range of the output. Diode D2 in Figure 2, from the output of the amplifier to V_{DD}, is forward biased when the output voltage becomes too high in shutdown mode. The diodes combined with the low-voltage shutdown functionality effectively limit the high-impedance output range to plus and minus a diode voltage around ground (~≤ 0.6V).

Driving Capacitive Loads

Driving a capacitive load can cause instability in many op amps, especially those with low quiescent current. The MAX44281 is stable for a range of capacitive loads to above 400pF. Adding a series resistor between the output and the load capacitor improves the circuit's response by isolating the load capacitance from the op amp's output. See the Isolation Resistance vs. Capacitive-Load Stability graph in the *Typical Operating Characteristics* section for details.

Applications Information

The MAX44281 features low input voltage offset and low bias current, which makes the device ideal for single-supply DC operations. However, its wide bandwidth and low noise make it suitable also for AC applications. The circuit in Figure 1 enables AC-coupled applications with the simple addition of two external resistors R1 and R2. Such resistors set the output bias voltage at midsupply. The values of R1 and R2 depend on the gain option of the device, according to the equation:

$$V_{DD} \times (R2/(R1 + R2)) = V_{DD}/(2 \times A_V)$$

Driver for Interfacing with the MAX11645 ADC

The MAX44281's tiny size and low noise make it a good fit for driving 12- to 16-bit resolution ADCs in space-constrained applications. The *Typical Application Circuit* shows the MAX44281 amplifier output connected to a lowpass filter driving the MAX11645 ADC. The MAX11645 is part of a family of 3V and 5V, 12-bit and 10-bit, 2-channel ADCs.

The MAX11645 offers sample rates up to 94ksps and measures two single-ended inputs or one differential input. These ADCs dissipate 670µA at the maximum sampling rate, but just 6µA at 1ksps and 0.5µA in shutdown. Offered in the ultra-tiny, 1.9mm x 2.2mm WLP and µMAX-8 packages, the MAX11645 ADCs are an ideal fit to pair with the MAX44281 amplifier in portable applications.

Where higher resolution is required, refer to the MAX1069 (14-bit) and MAX1169 (16-bit) ADC families.

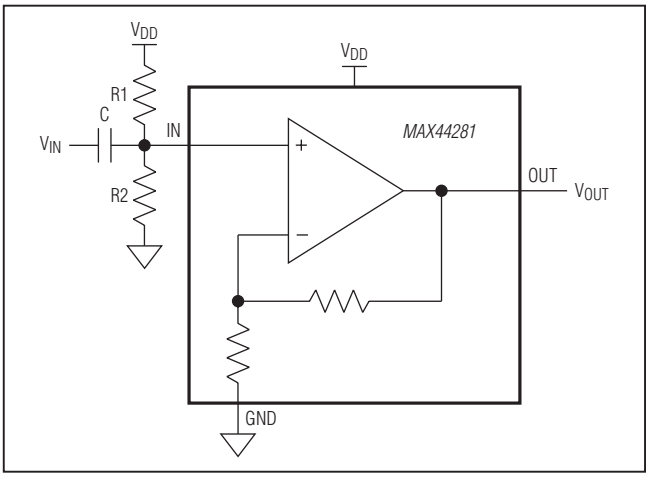


Figure 1. How to Use the MAX44281 in AC-Coupled Applications

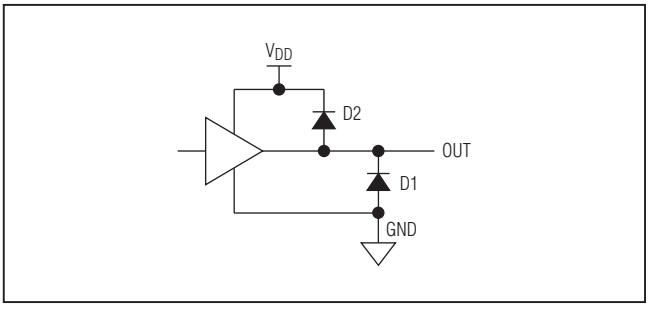
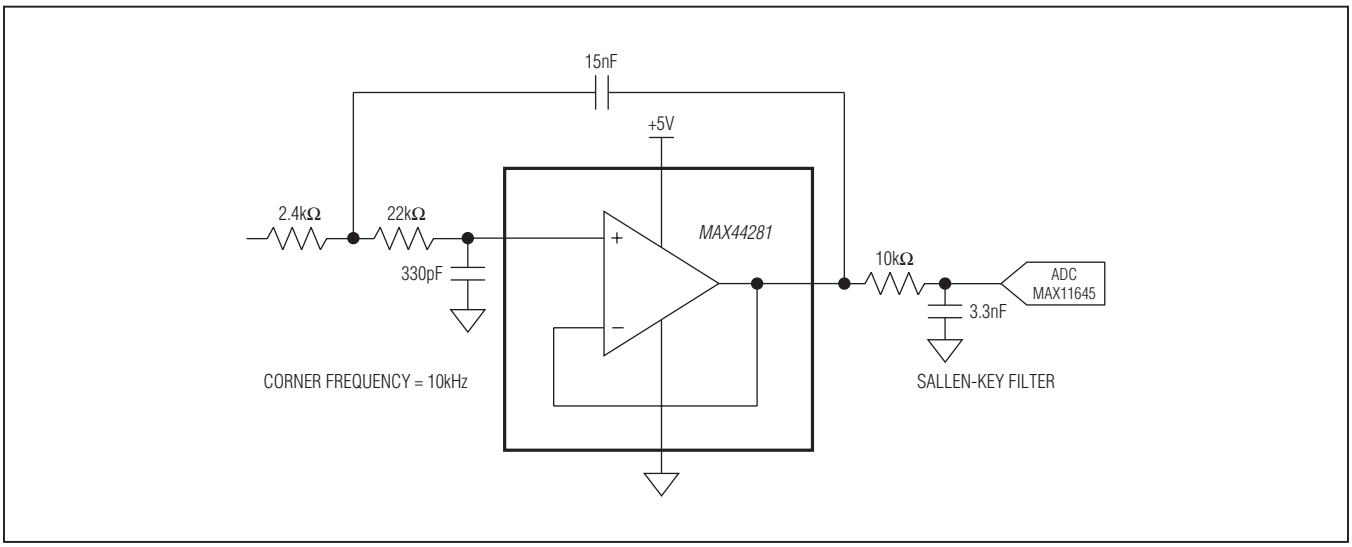


Figure 2. Output ESD Protection Diodes

Ultra-Small, Ultra-Thin, 4-Bump Op Amp

Typical Application Circuit



WLP Applications Information

For the latest application details on WLP construction, dimensions, tape carrier information, PCB techniques, bump-pad layout, and recommended reflow temperature profile, as well as the latest information on reliability testing results, refer to Application Note 1891: *Wafer-Level Packaging (WLP) and Its Applications*.

Package Information

For the latest package outline information and land patterns (footprints), go to **www.maximintegrated.com/packages**. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
4 WLP	N40A0-1	21-0612	Refer to Application Note 1891

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	FUNCTION
MAX44281OANS+T*	-40°C to +125°C	4 WLP	Noninverting, A_V = 1V/V
MAX44281VANS+T	-40°C to +125°C	4 WLP	Noninverting, A_V = 2V/V
MAX44281UANS+T	-40°C to +125°C	4 WLP	Noninverting, A_V = 10V/V

+Denotes a lead(Pb)-free/RoHS-compliant package.
**Future product—contact factory for availability.*

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	12/12	Initial release	—

Maxim Integrated cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim Integrated product. No circuit patent licenses are implied. Maxim Integrated reserves the right to change the circuitry and specifications without notice at any time. The parametric values (min and max limits) shown in the Electrical Characteristics table are guaranteed. Other parametric values quoted in this data sheet are provided for guidance.

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