

Super Low Distortion Multilayer Ceramic Capacitors (CFCAP(TM))

GMK212SD153KD-T



■ Features

- Item Summary
35V, 0.015 μ F, $\pm 10\%$, 0805
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping paper 4000pcs

■ Products characteristics table

CaseSize (EIA/JIS)	0805/2012
Rated Voltage (max)	35V
Capacitance	0.015 μ F
Capacitance Tolerance	$\pm 10\%$
Resistance	10,000 M Ω min.
Dielectric Loss Tangent	0.1%MAX
Temperature Range (EIA/JIS)	-55 to +125°C/-55 to +125°C
RoHS Compliance	Yes
Halogen Free	Yes
Soldering Method	Reflow

■ External Dimensions

L	2.0mm ± 0.10
W	1.25mm ± 0.10
T	0.85mm ± 0.10
e	0.5mm ± 0.25

2015.01.05

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
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 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor

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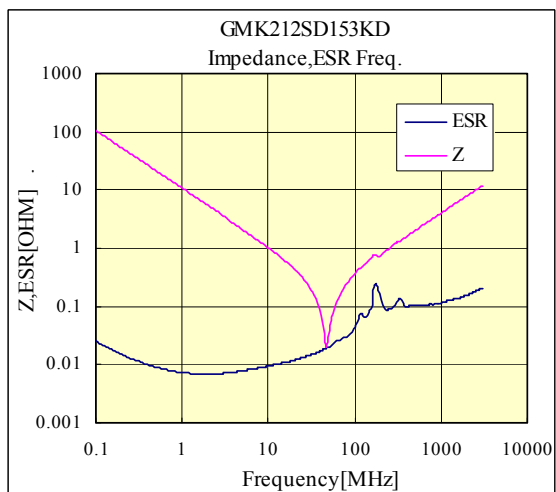
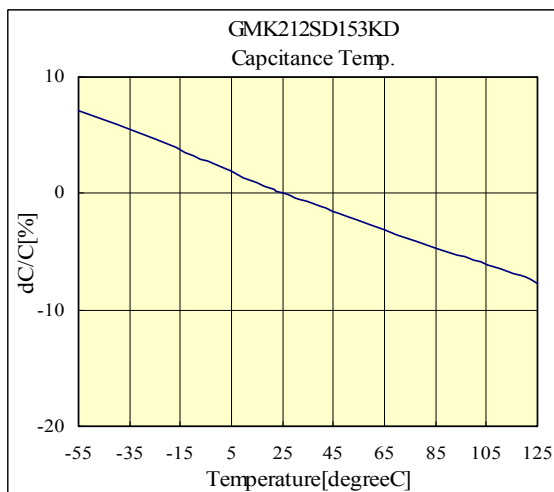
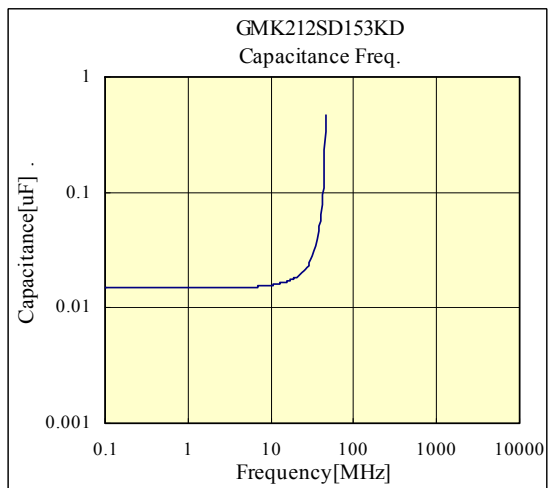
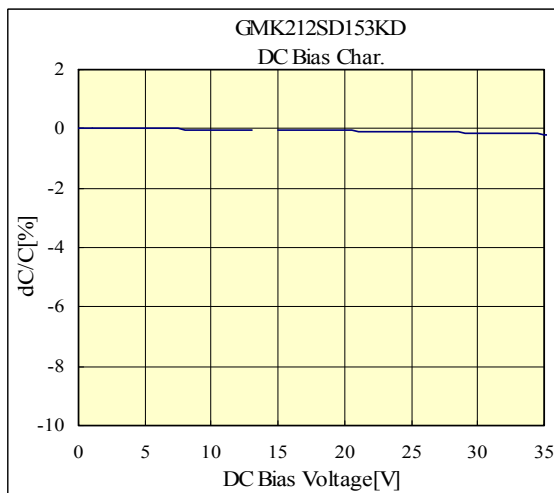
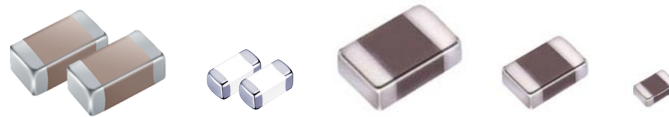
-Electrical Characteristics Data- Jun-2011

RoHS compliance product

○GMK212SD153KD

35V 2012 0.015uF +/-10% t=0.95mmMAX

35V 0805 0.015uF +/-10% t=0.037inchMAX



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