Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	134.8	135	135.2
Insertion Loss	dB	-	31.6	33
2 dB Bandwidth	MHz	24	24.03	-
3 dB Bandwidth	MHz		24.1	
35 dB Bandwidth	MHz	-	24.77	24.8
40 dB Bandwidth	MHz		24.84	
Group Delay Variation(f0±11MHz)	nsec	-	50	-
Phase Linearity(f0±11MHz)	degree	-	6	-
Passband Variation	dB	-	1	1.5
Ultimate Rejection	dB	48	53	-
Absolute delay	usec	-	3.74	-
Material Temperature coefficient	KHz/°C	-12.69		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.0x12.8x4.7mm^3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

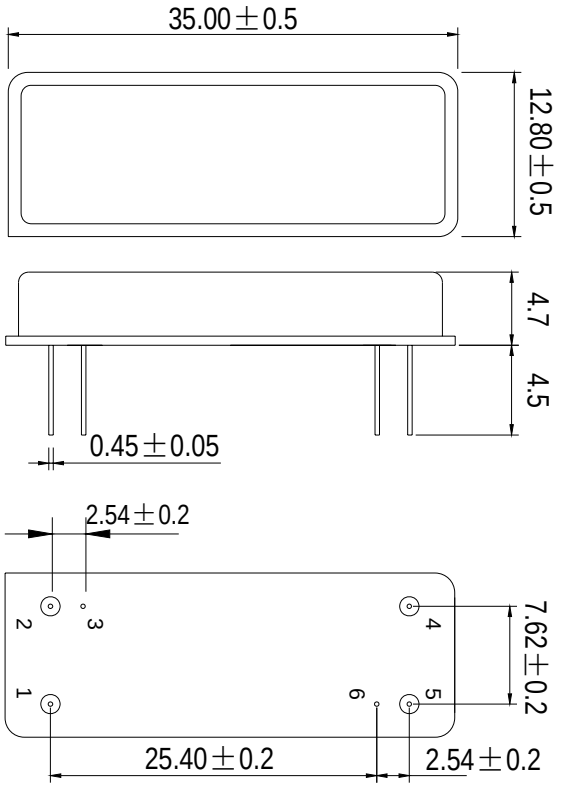
Matching Configuration



Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

Package Dimension



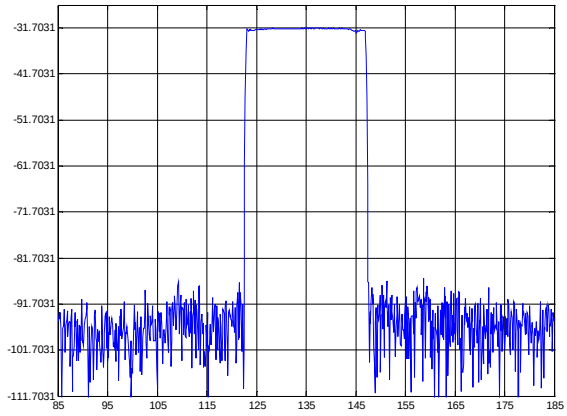
Input	1
Output	5
Ground	2,3,4,6

Package: DIP3512

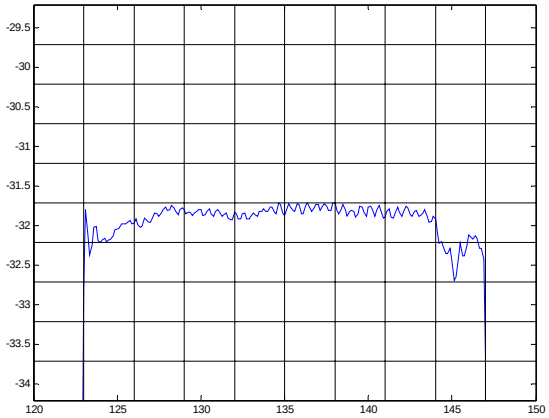
Unit: mm

Typical Performance

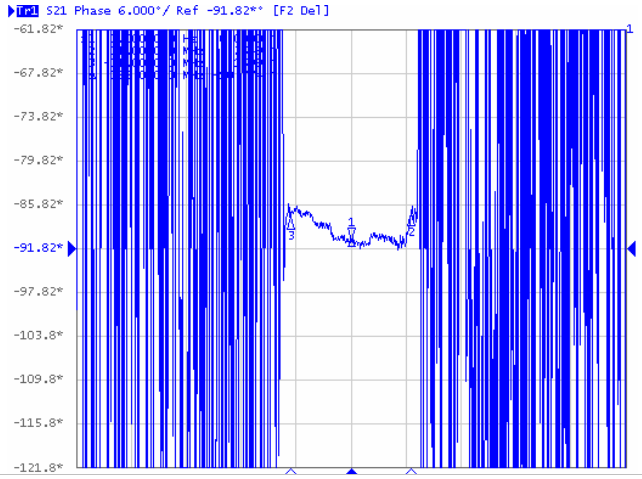
Frequency Response



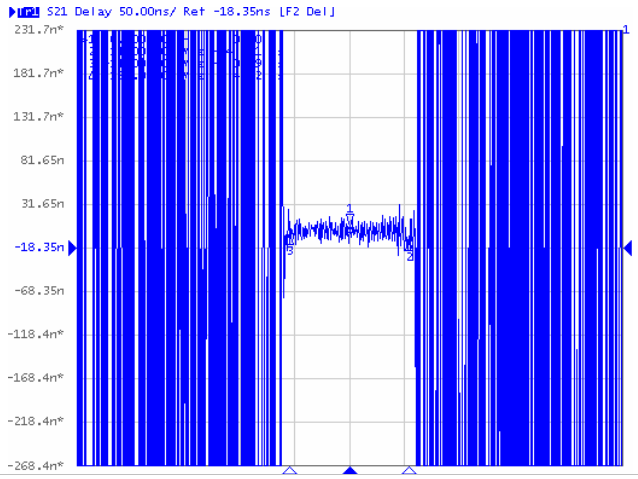
Passband Response



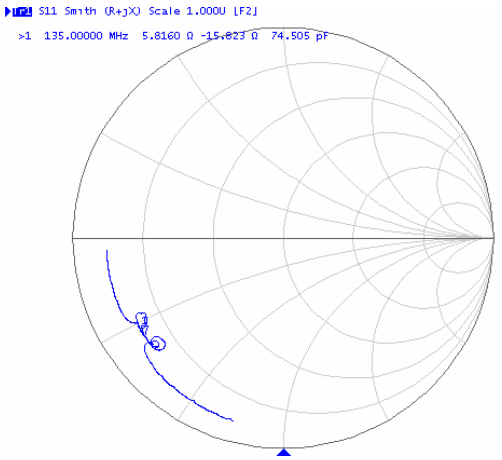
Phase Linearity(f0±11MHz)



Group delay Variation(f0±11MHz)



Simth Chart S11



Simth Chart S22

