

BIPOLAR ANALOG INTEGRATED CIRCUIT

μ PC4084

J-FET INPUT QUAD OPERATIONAL AMPLIFIER

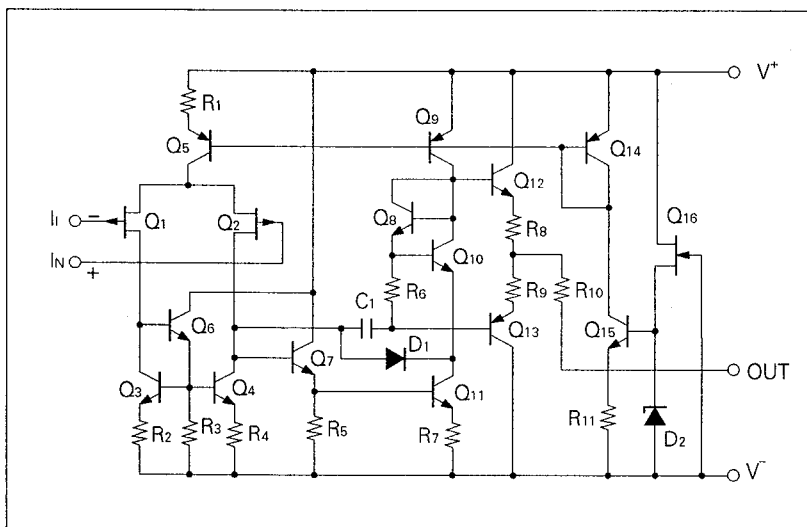
DESCRIPTION

The μ PC4084, is a quad operational amplifier incorporating well matched ion implant P-channel J-FET on the same chip with standard bipolar transistors. The key features of this op amp is very low input bias current and high slew rate ten times faster than conventional general purpose op amps. By these features μ PC4084, is excellent choice for wide variety of applications including integrator, active filter, pulse amp etc.

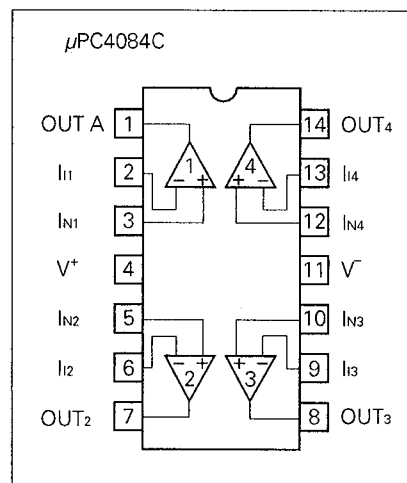
FEATURES

- Very low input bias and offset currents
- High input impedance...J-FET Input Stage
- High slew rate: 13 V/ μ s (TYP.)
- Internal frequency compensation
- Output short circuit protection

EQUIVALENT CIRCUIT (1/4 Circuit)



CONNECTION DIAGRAM (Top View)



ORDERING INFORMATION

PART NUMBER	PACKAGE	QUALITY GRADE
μ PC4084C	14 PIN PLASTIC DIP (300 mil)	Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS ($T_a = 25\text{ }^{\circ}\text{C}$)

PARAMETER		SYMBOL	μ PC4084	UNIT
Voltage between V^+ and V^- (Note 1)		$V^+ - V^-$	-0.3 to +36	V
Differential Input Voltage		V_{ID}	± 30	V
Input Voltage (Note 2)		V_I	$V^- - 0.3$ to $V^+ + 0.3$	V
Output Voltage (Note 3)		V_O	$V^- - 0.3$ to $V^+ + 0.3$	V
Power Dissipation	C Package (Note 4)	P_T	570	mW
Output Short Circuit Duration (Note 5)			Indefinite	sec
Operating Temperature Range		T_{opt}	-20 to + 80	$^{\circ}\text{C}$
Storage Temperature Range		T_{stg}	-55 to + 125	$^{\circ}\text{C}$

Note 1. Reverse connection of supply voltage can cause destruction.

Note 2. The input voltage should be allowed to input without damage destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The normal operation will establish when the both inputs are within the Common Mode Input Voltage Range of electrical characteristics.

Note 3. This specification is the voltage which should be allowed to supply to the output terminal from external without damage or destructive. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The output voltage of normal operation will be the Output Voltage Swing of electrical characteristics.

Note 4. Thermal derating factor is $-7.6\text{ mW} / ^{\circ}\text{C}$ when ambient temperature is higher than $50\text{ }^{\circ}\text{C}$.

Note 5. Pay careful attention to the total power dissipation not to exceed the absolute maximum ratings, Note 4.

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V^+	± 5		± 16	V

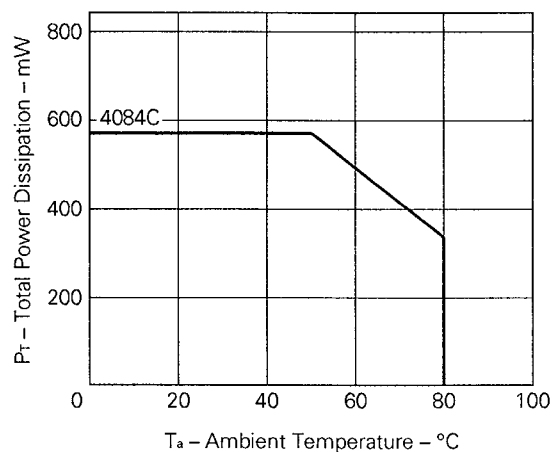
ELECTRICAL CHARACTERISTICS ($T_a = 25\text{ }^{\circ}\text{C}$, $V^{\pm} = \pm 15\text{ V}$)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITION
Input Offset Voltage	V_{io}		± 5.0	± 15.0	mV	$R_s \leq 50\text{ }\Omega$
Input Offset Current (Note 6)	I_{io}		± 5	± 200	pA	
Input Bias Current (Note 6)	I_b		30	400	pA	
Large Signal Voltage Gain	A_v	25	200		V/mV	$R_L \geq 2\text{ k}\Omega$, $V_o = \pm 10\text{ V}$
Supply Current	I_{cc}		8.0	11.2	mA	$I_o = 0\text{ A}$, All Amplifiers
Common Mode Rejection Ratio	CMR	70	76		dB	
Supply Voltage Rejection Ratio	SVR	70	76		dB	
Output Voltage Swing	V_{om}	± 12	$+13.5$		V	$R_L \geq 10\text{ k}\Omega$
Output Voltage Swing	V_{om}	± 10	± 12		V	$R_L \geq 2\text{ k}\Omega$
Common Mode Input Voltage Range	V_{icM}	± 10	$+15$ -12.7		V	
Slew Rate	SR		13		V/ μ s	$A_v = 1$
Unity Gain Frequency	f_{unity}		3		MHz	
Input Equivalent Noise Voltage Density	e_n		25		nV/ $\sqrt{\text{Hz}}$	$f = 1\text{ kHz}$, $R_s = 100\text{ }\Omega$
Channel Separation			120		dB	
Input Offset Voltage	V_{io}			± 20	mV	$R_s \leq 50\text{ }\Omega$, $T_a = -20\text{ to }+70\text{ }^{\circ}\text{C}$
Average V_{io} Temperature Drift	$\Delta V_{io}/\Delta T$		± 10		$\mu\text{V}/^{\circ}\text{C}$	$T_a = -20\text{ to }+70\text{ }^{\circ}\text{C}$
Input Offset Current (Note 6)	I_{io}			± 5	nA	$T_a = -20\text{ to }+70\text{ }^{\circ}\text{C}$
Input Bias Current (Note 6)	I_b			10	nA	$T_a = -20\text{ to }+70\text{ }^{\circ}\text{C}$

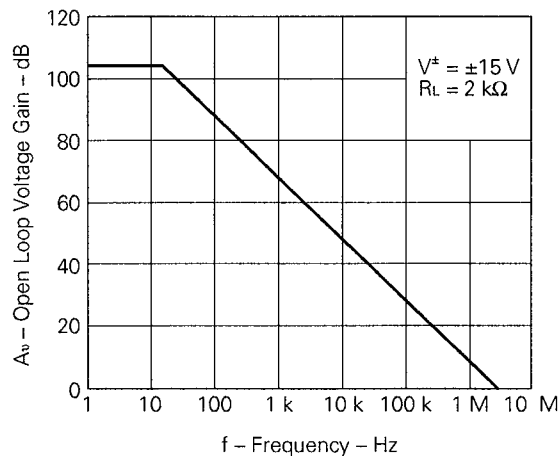
Note 6. Input bias currents flow into IC. Because each currents are gate leak current of P-channel J-FET on input stage. And that are temperature sensitive. Short time measuring method is recommendable to maintain the junction temperature close to the ambient temperature.

TYPICAL PERFORMANCE CHARACTERISTICS ($T_a = 25^\circ\text{C}$, TYP.)

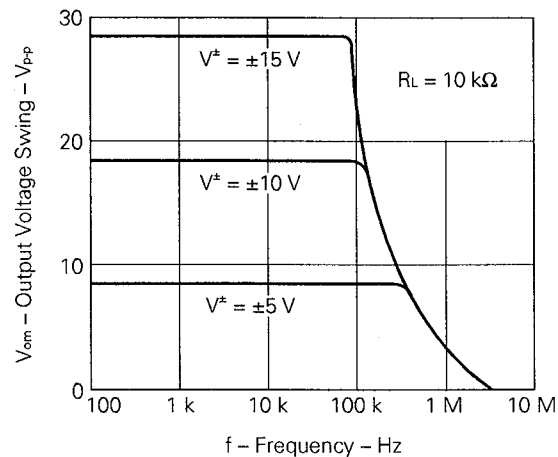
POWER DISSIPATION



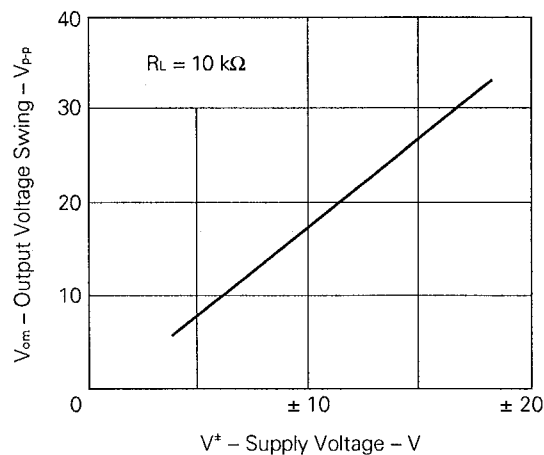
OPEN LOOP FREQUENCY RESPONSE



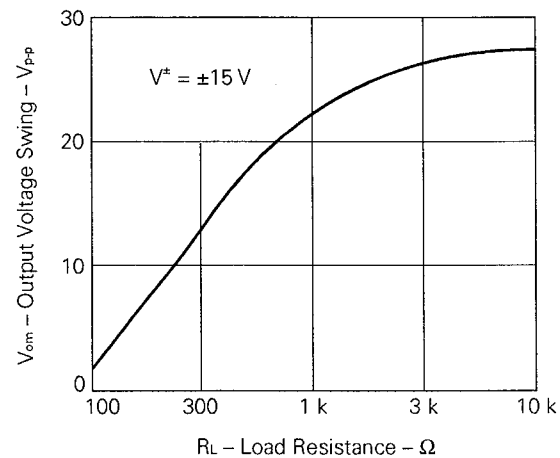
LARGE SIGNAL FREQUENCY RESPONSE



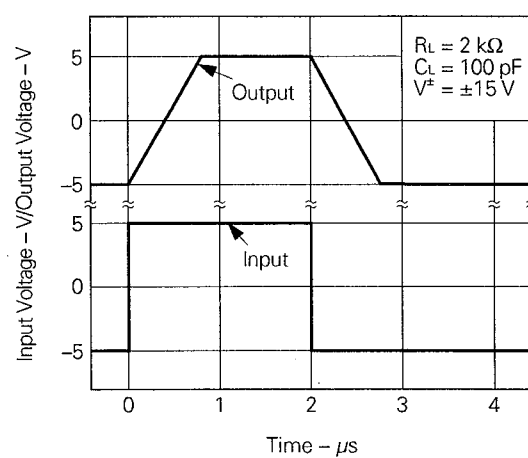
OUTPUT VOLTAGE SWING

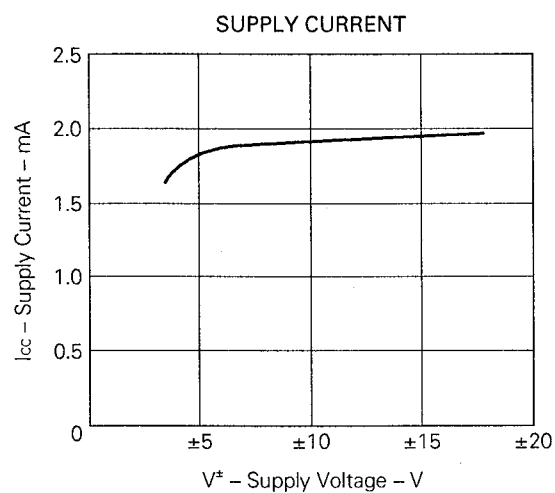
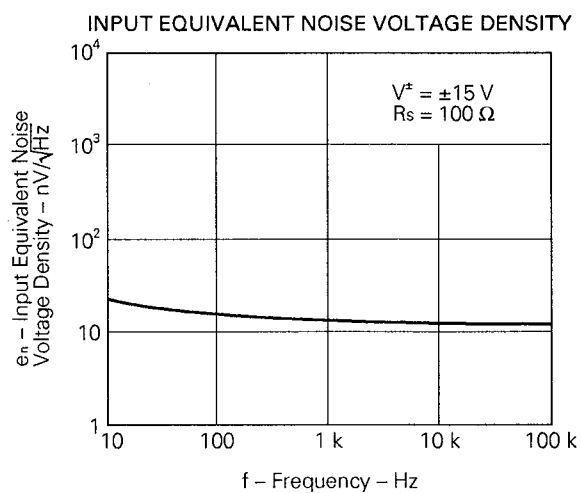
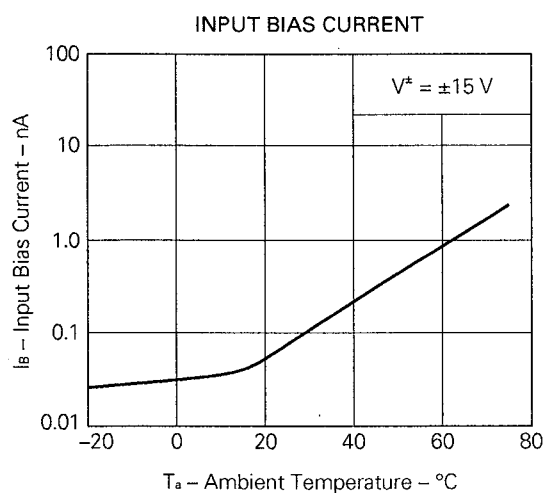


OUTPUT VOLTAGE SWING

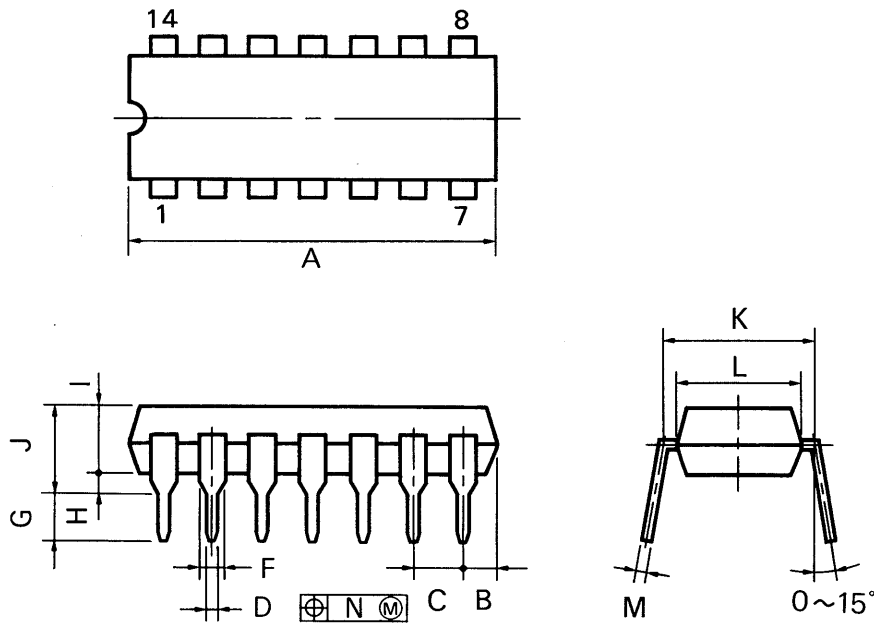


VOLTAGE FOLLOWER PULSE RESPONSE





14PIN PLASTIC DIP (300 mil)



P14C-100-300B1

NOTES

- Each lead centerline is located within 0.25 mm (0.01 inch) of its true position (T.P.) at maximum material condition.
- Item "K" to center of leads when formed parallel.

ITEM	MILLIMETERS	INCHES
A	20.32 MAX.	0.800 MAX.
B	2.54 MAX.	0.100 MAX.
C	2.54 (T.P.)	0.100 (T.P.)
D	0.50 ± 0.10	0.020 $\begin{smallmatrix} +0.004 \\ -0.005 \end{smallmatrix}$
F	1.2 MIN.	0.047 MIN.
G	3.6 ± 0.3	0.142 ± 0.012
H	0.51 MIN.	0.020 MIN.
I	4.31 MAX.	0.170 MAX.
J	5.08 MAX.	0.200 MAX.
K	7.62 (T.P.)	0.300 (T.P.)
L	6.4	0.252
M	0.25 $\begin{smallmatrix} +0.10 \\ -0.05 \end{smallmatrix}$	0.010 $\begin{smallmatrix} +0.004 \\ -0.003 \end{smallmatrix}$
N	0.25	0.01

RECOMMENDED SOLDERING CONDITIONS

The following conditions (see table below) must be met when soldering this product.

Please consult with our sales offices in case other soldering process is used, or in case soldering is done under different conditions.

TYPES OF THROUGH HOLE DEVICE

[μ PC4084C]

Soldering Method	Soldering conditions	Recommended condition symbol
Wave soldering	Solder temperature: 260 °C or below, Flow time: 10 seconds or below	

[MEMO]

No part of this document may be copied or reproduced in any form or by any means without the prior written consent of NEC Corporation. NEC Corporation assumes no responsibility for any errors which may appear in this document.

NEC Corporation does not assume any liability for infringement of patents, copyrights or other intellectual property rights of third parties by or arising from use of a device described herein or any other liability arising from use of such device. No license, either express, implied or otherwise, is granted under any patents, copyrights or other intellectual property rights of NEC Corporation or others.

The devices listed in this document are not suitable for use in aerospace equipment, submarine cables, nuclear reactor control systems and life support systems. If customers intend to use NEC devices for above applications or they intend to use "Standard" quality grade NEC devices for applications not intended by NEC, please contact our sales people in advance.

Application examples recommended by NEC Corporation.

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tools, Industrial robots, Audio and Visual equipment, Other consumer products, etc.

Special: Automotive and Transportation equipment, Traffic control systems, Antidisaster systems, Anticrime systems, etc.