S4504

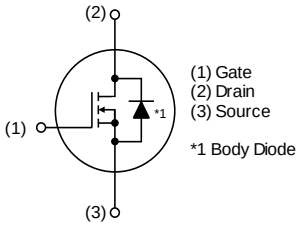
N-channel SiC power MOSFET bare die Datasheet

V_{DSS}	750V
$R_{DS(on)}$ (Typ.)	45mΩ
I_D *1	34A

● Features

1) Low on-resistance
2) Fast switching speed
3) Fast reverse recovery
4) Easy to parallel
5) Simple to drive

●Inner circuit



● Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

●Absolute maximum ratings (T_c = 25°C*1)

Parameter		Symbol	Value	Unit
Drain - source voltage		V_{DSS}	750	V
Continuous drain and source current	$V_{GS} = V_{GS_on}$	I_D, I_S *1	34	A
T_c = 100°C			24	A
Pulsed drain current	$V_{GS} = V_{GS_on}$	$I_{D,pulse}$ *2	61	A
Body diode pulsed forward current	V_{GS} = 0 V	$I_{S,pulse}$ *3	34	A
Body diode surge forward current	V_{GS} = 0 V	$I_{S,pulse}$ *4	61	A
Gate - source voltage (DC)		V_{GSS}	-4 to +21	V
Gate - source surge voltage (t_{surge} < 300ns)		V_{GSS_surge} *5	-4 to +23	V
Recommended turn-on gate - source drive voltage		V_{GS_on} *6	+15 to +18	V
Recommended turn-off gate - source drive voltage		V_{GS_off}	0	V
Virtual junction temperature		T_{vj}	175	°C
Range of storage temperature		T_{stg}	-40 to +175	°C

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●**Electrical characteristics** (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0 V, I_D = 5.3mA T_{vj} = 25°C	750	-	-	V
Zero Gate voltage Drain current	I_{DSS}	V_{GS} = 0 V, V_{DS} =750V T_{vj} = 25°C T_{vj} = 150°C	- -	1 10	80 -	μA
Gate - Source leakage current	I_{GSS+}	V_{GS} = +21V , V_{DS} = 0V	-	-	100	nA
Gate - Source leakage current	I_{GSS-}	V_{GS} = -4V , V_{DS} = 0V	-	-	-100	nA
Gate threshold voltage	$V_{GS\ (th)}$ *7	V_{DS} = 10V, I_D = 8.89mA	2.8	-	4.8	V
Static Drain - Source on - state resistance	$R_{DS(on)}$ *8	V_{GS} = 18V, I_D = 17A T_{vj} = 25°C T_{vj} = 150°C	- -	45 77	57 -	mΩ
Gate input resistance	R_G	f = 1MHz, open drain	-	4	-	Ω

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●**Electrical characteristics** (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Transconductance	g_{fs} *8	V_{DS} = 10V, I_D = 17A	-	9.4	-	S
Input capacitance	C_{iss}	V_{GS} = 0V	-	1460	-	pF
Output capacitance	C_{oss}	V_{DS} = 500V	-	69	-	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	5	-	
Effective output capacitance, energy related	$C_{o(er)}$	V_{GS} = 0V V_{DS} = 0V to 500V	-	90	-	pF
Total Gate charge	Q_g *8	V_{DS} = 500V	-	63	-	nC
Gate - Source charge	Q_{gs} *8	I_D = 17A V_{GS} = 18V	-	14	-	
Gate - Drain charge	Q_{gd} *8	See Fig. 1-1, 1-2.	-	19	-	
Turn - on delay time	$t_{d(on)}$ *8	V_{DS} = 500V	-	5.1	-	ns
Rise time	t_r *8	I_D = 17A V_{GS} = +18V / 0V	-	16	-	
Turn - off delay time	$t_{d(off)}$ *8	R_G = 3.3Ω, L = 250μH E_{on} includes diode reverse recovery L_σ = 50nH, C_σ = 10pF	-	27	-	
Fall time	t_f *8	See Fig. 2-1, 2-2, 2-3.	-	10	-	
Turn - on switching loss	E_{on} *8		-	112	-	μJ
Turn - off switching loss	E_{off} *8		-	17	-	

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●**Body diode electrical characteristics** (Source-Drain) (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Forward voltage	V_{SD} *8	V_{GS} = 0V, I_D = 17A	-	3.3	-	V
Reverse recovery time	t_{rr} *8	I_F = 17A V_R = 500V di/dt = 2900A/μs L_σ = 50nH, C_σ = 10pF See Fig. 3-1, 3-2.	-	9.3	-	ns
Reverse recovery charge	Q_{rr} *8		-	89	-	nC
Peak reverse recovery current	I_{rrm} *8		-	19	-	A

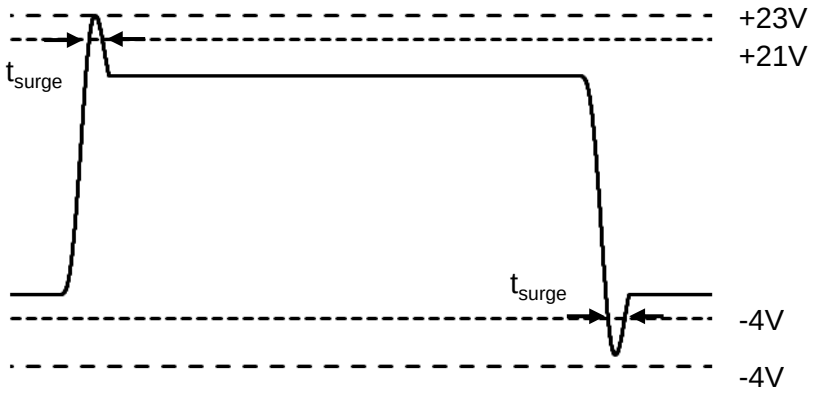
*1 Limited by maximum T_{vj} and assumes R_{thJC} < 1.3K/W.

*2 $P_W \leq$ 10μs, Duty cycle ≤ 1%

*3 Only for body-diode, Repititive pulse, PW ≤ 500ns, Duty cycle ≤ 5%

*4 When used as a protective function, PW ≤ 10μs

*5 Example of acceptable V_{GS} waveform



Please note especially when using driver source that V_{GSS_surge} must be in the range of absolute maximum rating.

*6 Please be advised not to use SiC-MOSFETs with V_{GS} below 10V as doing so may cause thermal runaway.

*7 Tested after applying V_{GS} = 21V for 100ms.

*8 Pulsed

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●Electrical characteristic curves

Fig.1 T_a = 25ºC Typical Output Characteristics(I)

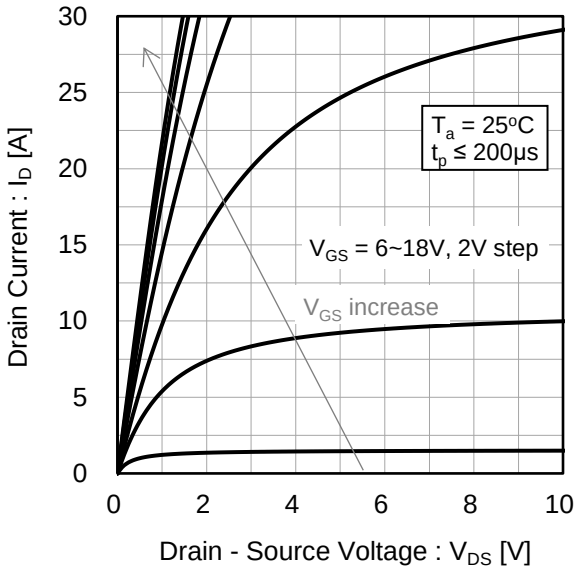


Fig.2 T_a = 25ºC Typical Output Characteristics(II)

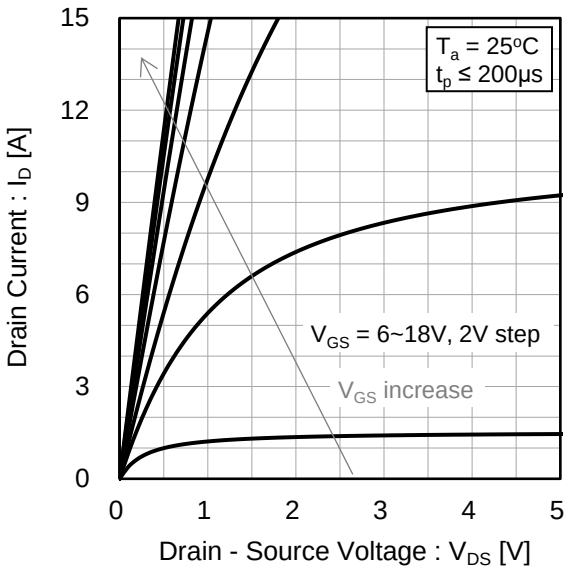
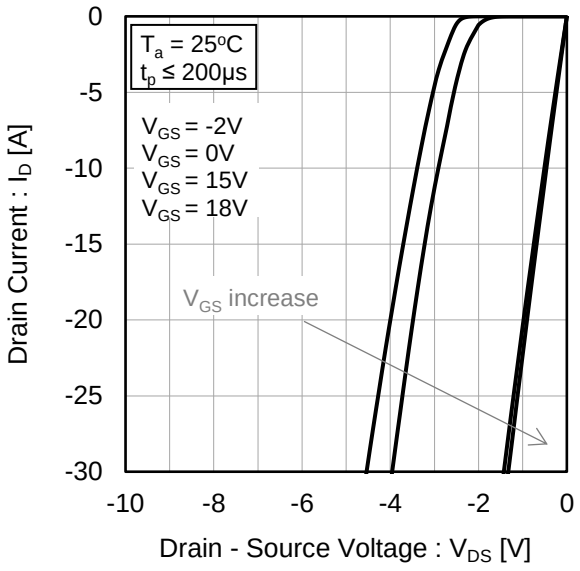


Fig.3 T_a = 25ºC 3rd Quadrant Characteristics



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●Electrical characteristic curves

Fig.4 T_a = 150ºC Typical Output Characteristics(I)

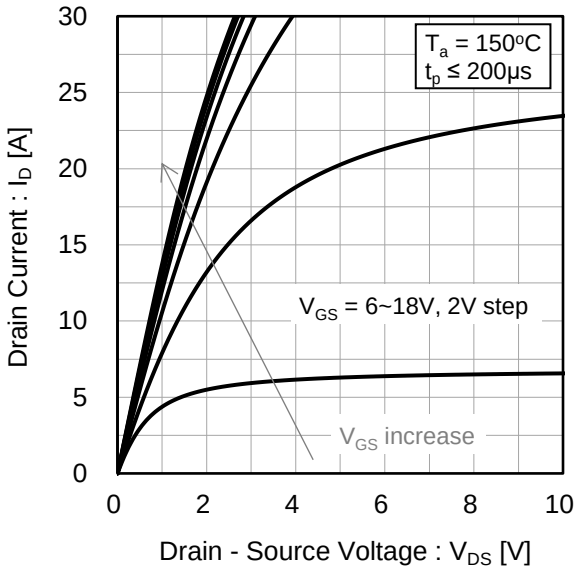


Fig.5 T_a = 150ºC Typical Output Characteristics(II)

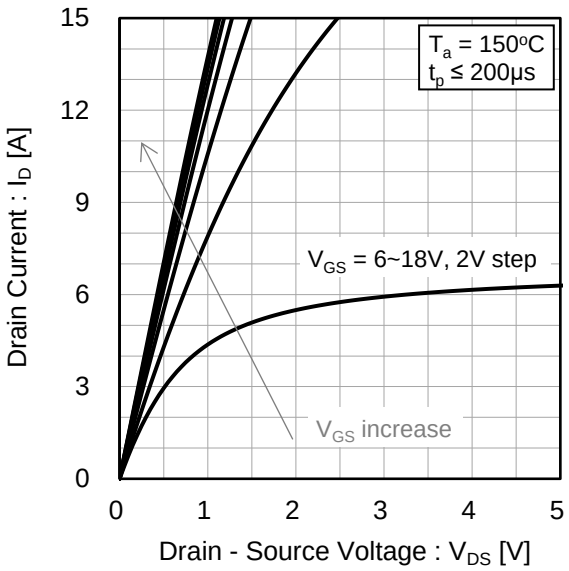


Fig.6 T_a = 150ºC 3rd Quadrant Characteristics

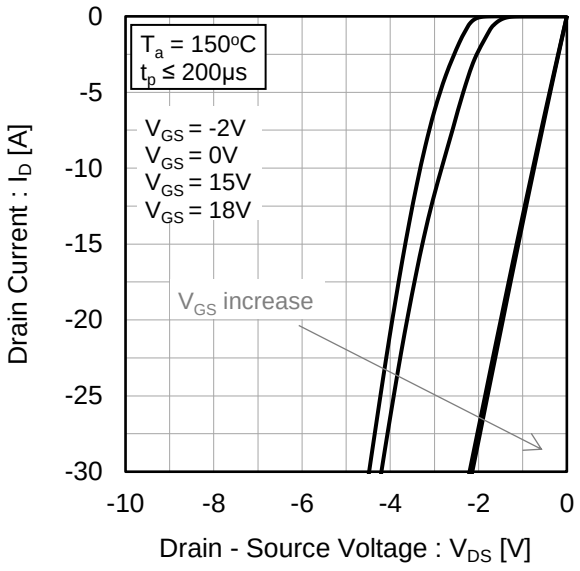
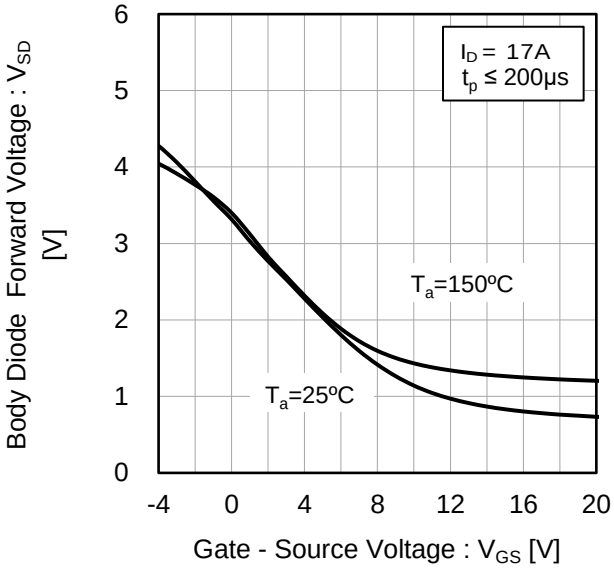


Fig.7 Body Diode Forward Voltage vs. Gate - Source Voltage



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●Electrical characteristic curves

Fig.8 Typical Transfer Characteristics (I)

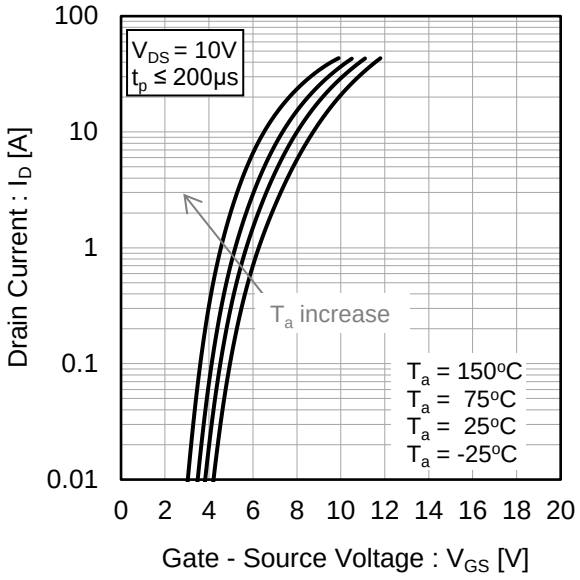


Fig.9 Typical Transfer Characteristics (II)

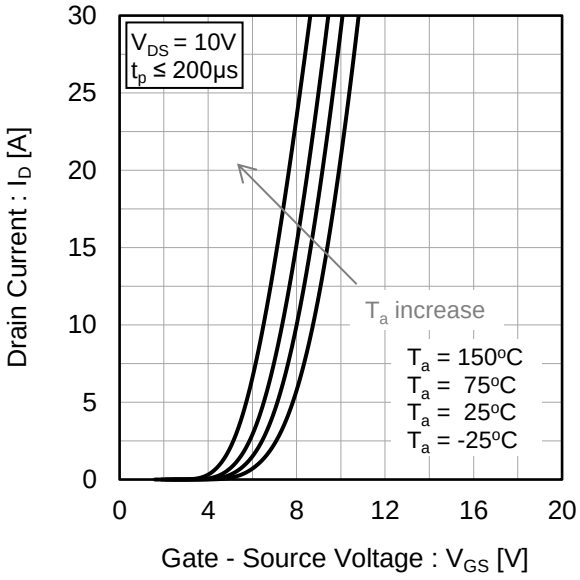


Fig.10 Gate Threshold Voltage vs. Junction Temperature

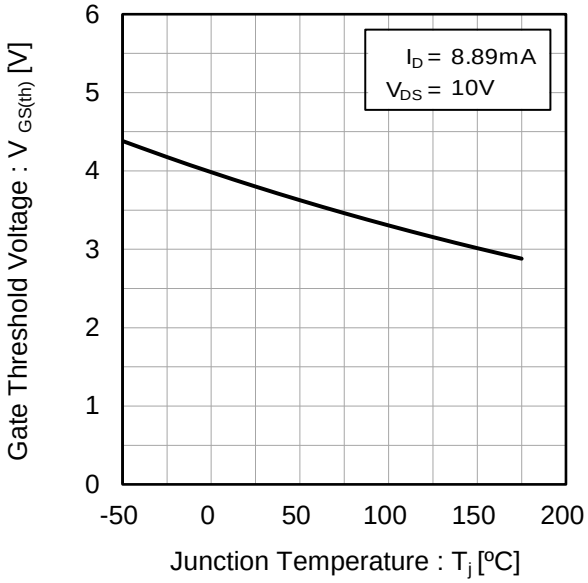
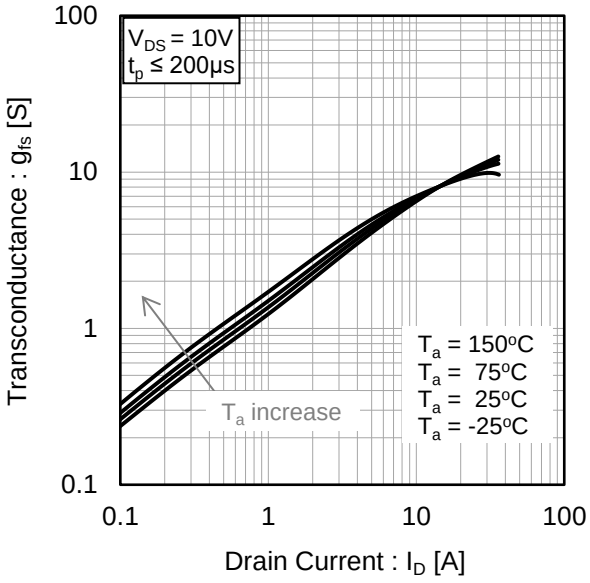


Fig.11 Transconductance vs. Drain Current



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●Electrical characteristic curves

Fig.12 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

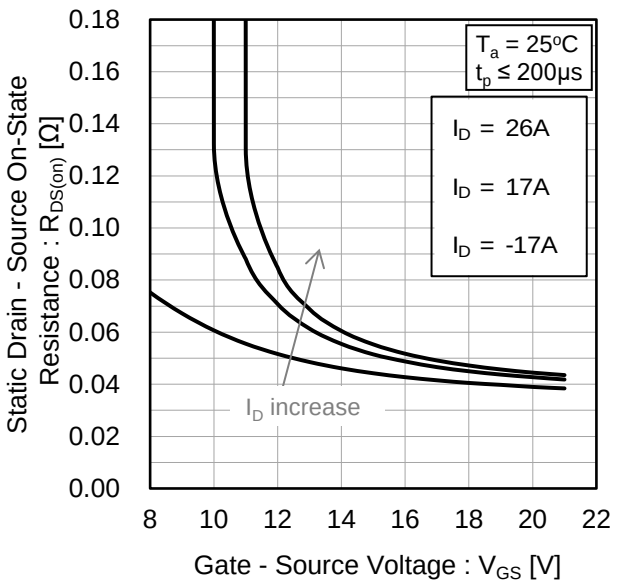


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

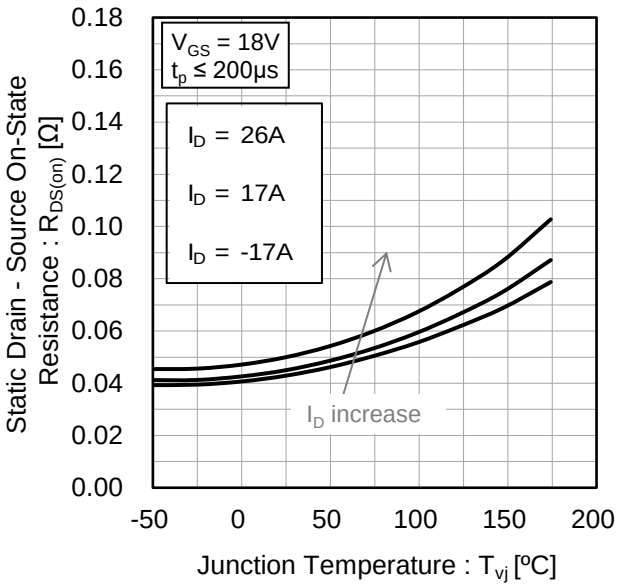


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current

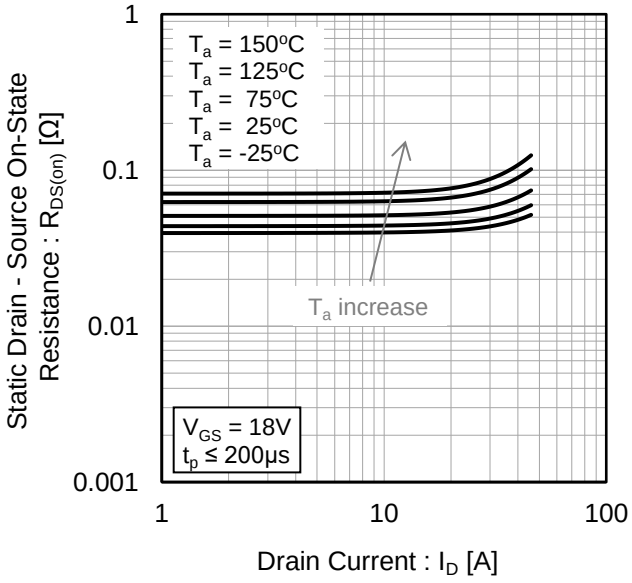
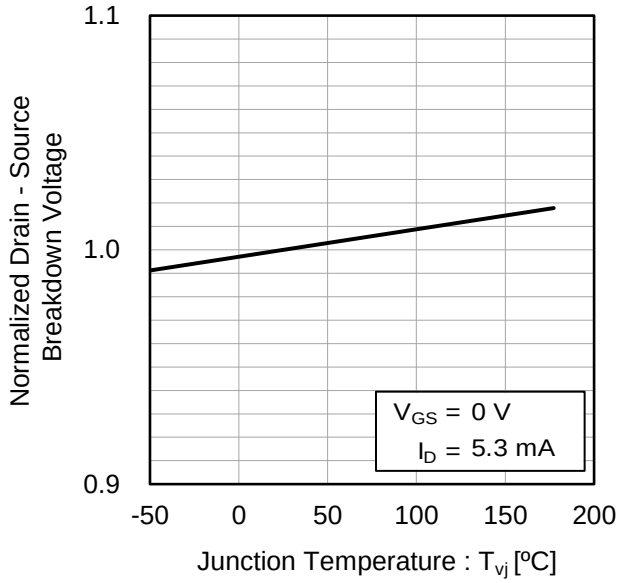


Fig.15 Normalized Drain - Source Breakdown Voltage vs. Junction Temperature



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●Electrical characteristic curves

Fig.16 Typical Capacitance vs. Drain - Source Voltage

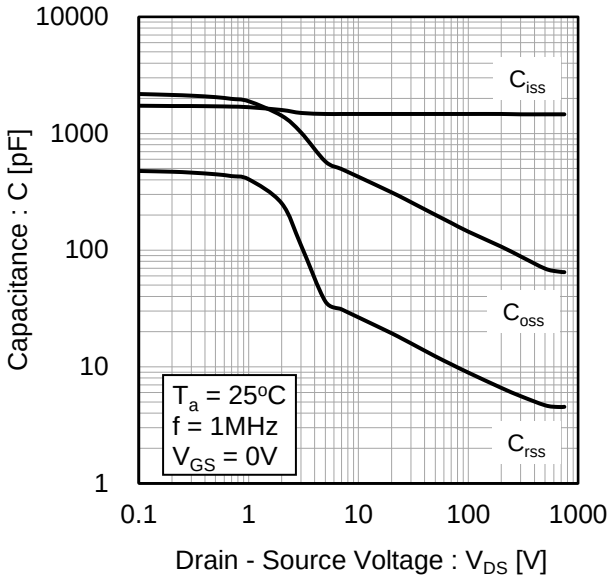


Fig.17 C_{oss} Stored Energy

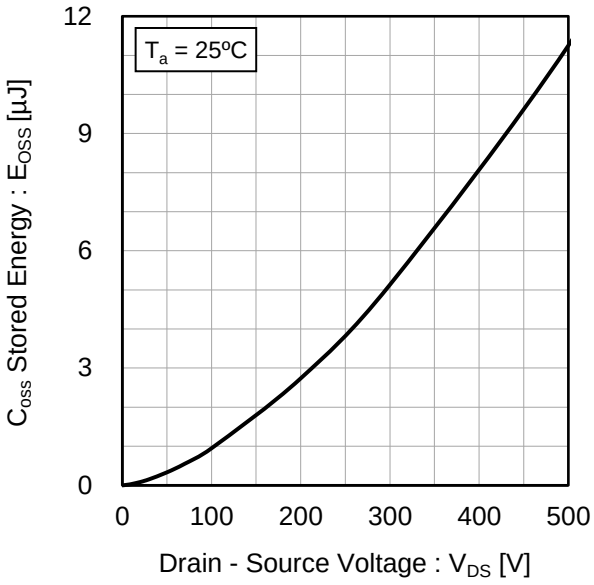
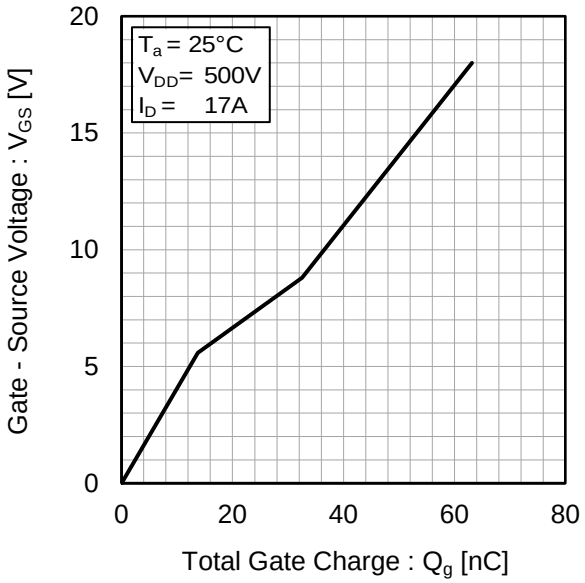


Fig.18 Dynamic Input Characteristics



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●Electrical characteristic curves

Fig.19 Typical Switching Time vs. External Gate Resistance

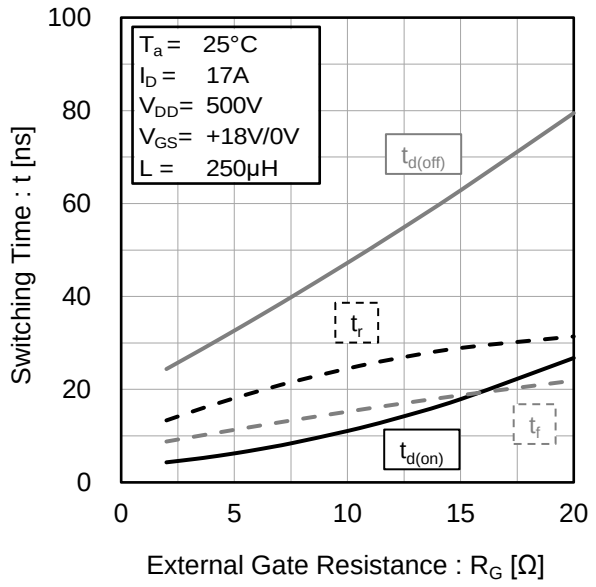


Fig.20 Typical Switching Loss vs. Drain - Source Voltage

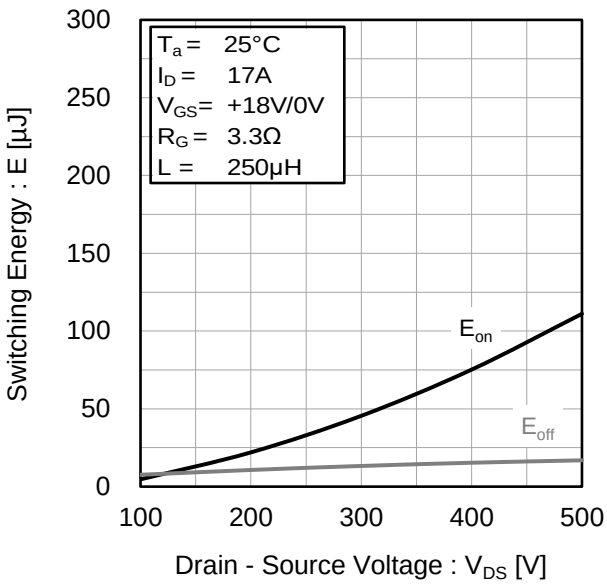


Fig.21 Typical Switching Loss vs. Drain Current

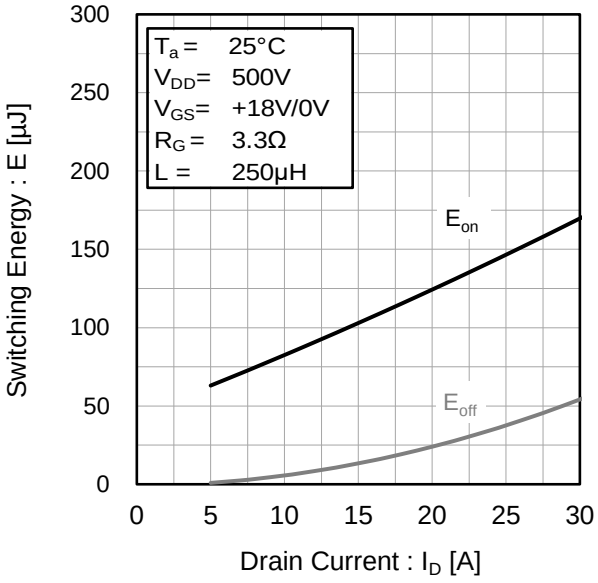
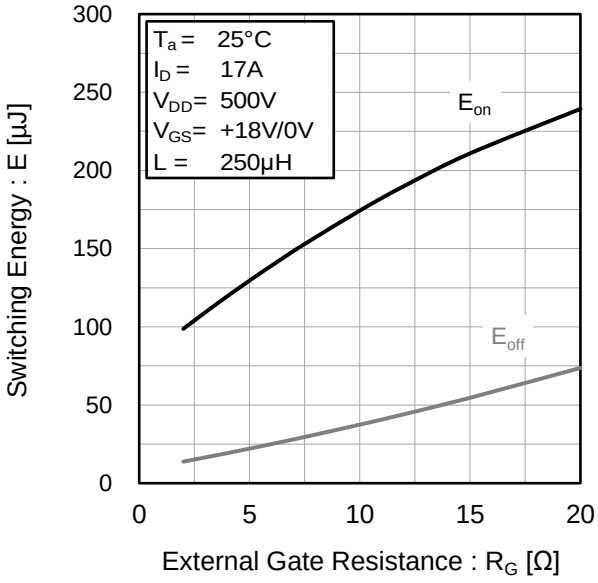


Fig.22 Typical Switching Loss vs. External Gate Resistance



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●Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

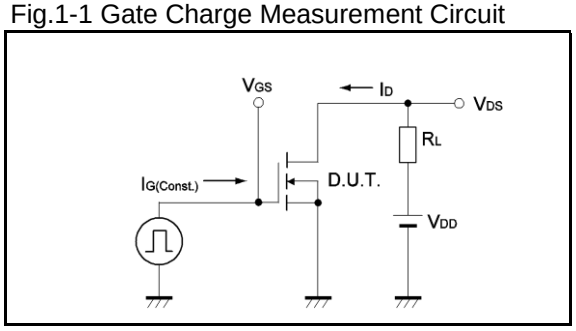


Fig.1-2 Gate Charge Waveform

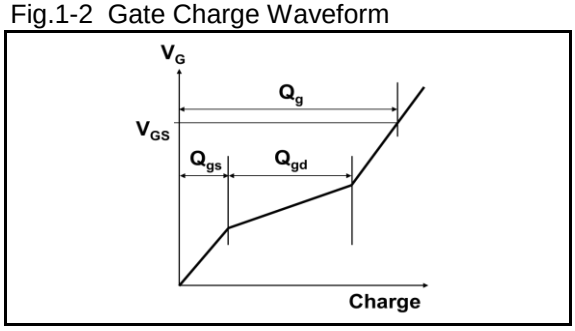


Fig.2-1 Switching Characteristics Measurement Circuit

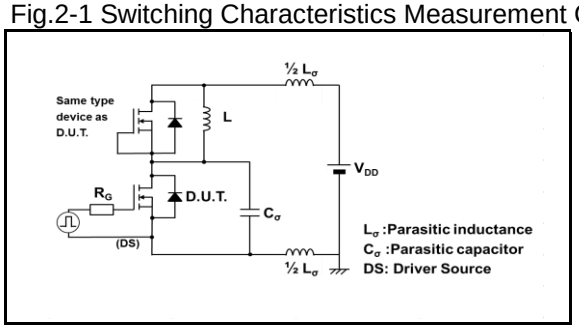


Fig.2-2 Waveforms for Switching Time

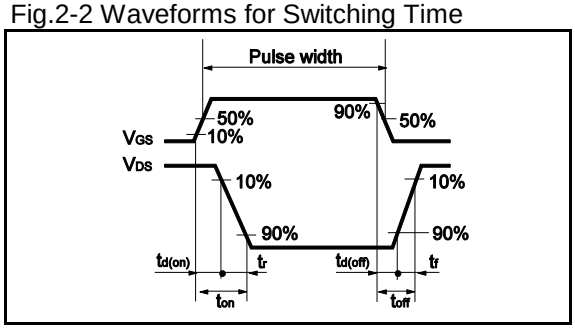


Fig.2-3 Waveforms for Switching Energy Loss

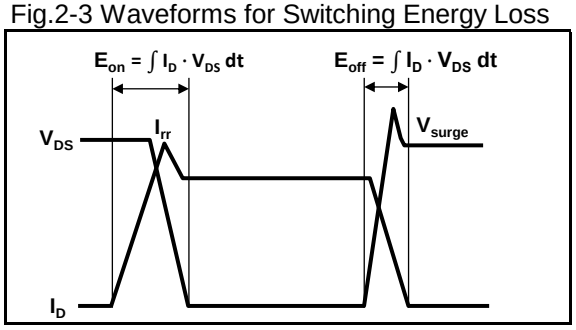


Fig.3-1 Reverse Recovery Time Measurement Circuit

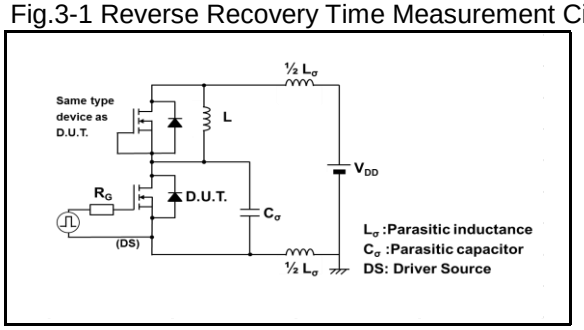
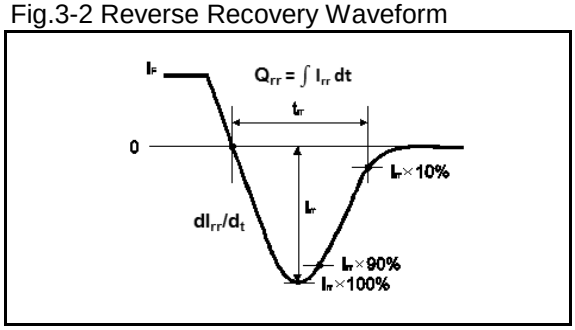


Fig.3-2 Reverse Recovery Waveform



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