

74AUP1G18

Low-power 1-of-2 demultiplexer with 3-state deselected output

Rev. 02 — 3 April 2008

Product data sheet

1. General description

The 74AUP1G18 is a high-performance, low-power, low-voltage, Si-gate CMOS device, superior to most advanced CMOS compatible TTL families.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V. This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

The 74AUP1G18 provides a 1-of-2 non-inverting demultiplexer with 3-state output. The 74AUP1G18 buffers the data on input pin (A) and passes it either to output 1Y or 2Y, depending on whether the state of the select input pin (S) is LOW or HIGH.

2. Features

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - ◆ JESD8-12 (0.8 V to 1.3 V)
 - ◆ JESD8-11 (0.9 V to 1.65 V)
 - ◆ JESD8-7 (1.2 V to 1.95 V)
 - ◆ JESD8-5 (1.8 V to 2.7 V)
 - ◆ JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - ◆ HBM JESD22-A114E Class 3A exceeds 5000 V
 - ◆ MM JESD22-A115-A exceeds 200 V
 - ◆ CDM JESD22-C101C exceeds 1000 V
- Low static power consumption; $I_{CC} = 0.9 \mu\text{A}$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot $< 10\%$ of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from $-40\text{ }^{\circ}\text{C}$ to $+85\text{ }^{\circ}\text{C}$ and $-40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$

3. Ordering information

Table 1. Ordering information

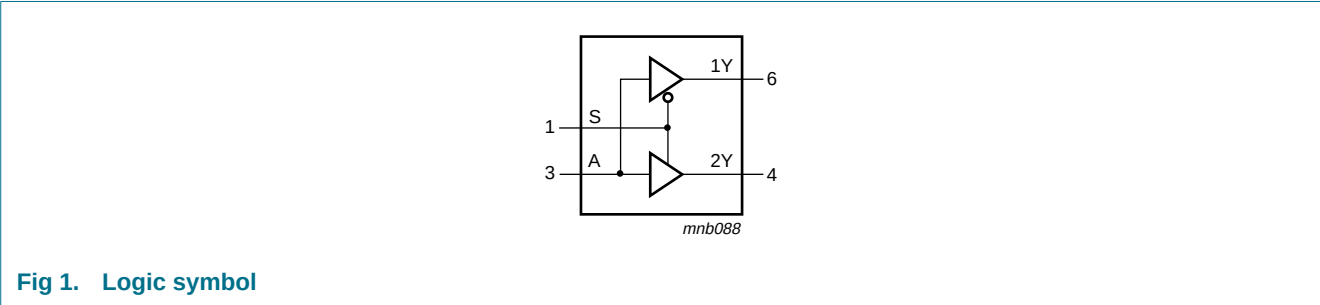
Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G18GW	−40 °C to +125 °C	SC-88	plastic surface-mounted package; 6 leads	SOT363
74AUP1G18GM	−40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886
74AUP1G18GF	−40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1 × 0.5 mm	SOT891

4. Marking

Table 2. Marking

Type number	Marking code
74AUP1G18GW	pW
74AUP1G18GM	pW
74AUP1G18GF	pW

5. Functional diagram



6. Pinning information

6.1 Pinning

Fig 2. Pin configuration SOT363 (SC-88)

Fig 3. Pin configuration SOT886 (XSON6)

Fig 4. Pin configuration SOT891 (XSON6)

6.2 Pin description

Table 3. Pin description

Symbol	Pin	Description
S	1	data select
GND	2	ground (0 V)
A	3	data input
2Y	4	data output
V _{CC}	5	supply voltage
1Y	6	data output

7. Functional description

Table 4. Function table^[1]

Input		Output	
S	A	1Y	2Y
L	L	L	Z
L	H	H	Z
H	L	Z	L
H	H	Z	H

[1] H = HIGH voltage level;
L = LOW voltage level;
Z = high-impedance OFF-state.

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+4.6	V
I_{IK}	input clamping current	$V_I < 0$ V	-50	-	mA
V_I	input voltage		[1] -0.5	+4.6	V
I_{OK}	output clamping current	$V_O > V_{CC}$ or $V_O < 0$ V	-	± 50	mA
V_O	output voltage	Active mode and Power-down mode	[1] -0.5	+4.6	V
I_O	output current	$V_O = 0$ V to V_{CC}	-	± 20	mA
I_{CC}	supply current		-	50	mA
I_{GND}	ground current		-50	-	mA
T_{stg}	storage temperature		-65	+150	°C
P_{tot}	total power dissipation	$T_{amb} = -40$ °C to +125 °C	[2] -	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SC-88 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.
For XSON6 packages: above 45 °C the value of P_{tot} derates linearly with 2.4 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		0.8	3.6	V
V_I	input voltage		0	3.6	V
V_O	output voltage	Active mode	0	V_{CC}	V
		Power-down mode; $V_{CC} = 0$ V	0	3.6	V
T_{amb}	ambient temperature		-40	+125	°C
$\Delta t/\Delta V$	input transition rise and fall rate	$V_{CC} = 0.8$ V to 3.6 V	0	200	ns/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{amb} = 25 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = 20 µA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.1	µA
I _{OZ}	OFF-state output current	V _I = V _{IH} or V _{IL} ; V _O = 0 V to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.1	µA
I _{OFF}	power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V	-	-	±0.2	µA
ΔI _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.2	µA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	0.5	µA
ΔI _{CC}	additional supply current	V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	[1] -	-	40	µA
C _I	input capacitance	V _{CC} = 0 V to 3.6 V; V _I = GND or V _{CC}	-	0.8	-	pF
C _O	output capacitance	V _O = GND; V _{CC} = 0 V	-	1.7	-	pF

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{amb} = -40 °C to +85 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = -20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = 20 µA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.5	µA
I _{OZ}	OFF-state output current	V _I = V _{IH} or V _{IL} ; V _O = 0 V to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.5	µA
I _{OFF}	power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V	-	-	±0.5	µA
ΔI _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.6	µA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	0.9	µA
ΔI _{CC}	additional supply current	V _I = V _{CC} - 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	1 -	-	50	µA

Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
T_{amb} = –40 °C to +125 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.75 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = –20 µA; V _{CC} = 0.8 V to 3.6 V	V _{CC} – 0.11	-	-	V
		I _O = –1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = –1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = –1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I _O = –2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = –3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = –2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = –4.0 mA; V _{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = 20 µA; V _{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I _I	input leakage current	V _I = GND to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.75	µA
I _{OZ}	OFF-state output current	V _I = V _{IH} or V _{IL} ; V _O = 0 V to 3.6 V; V _{CC} = 0 V to 3.6 V	-	-	±0.75	µA
I _{OFF}	power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V	-	-	±0.75	µA
ΔI _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.75	µA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	1.4	µA
ΔI _{CC}	additional supply current	V _I = V _{CC} – 0.6 V; I _O = 0 A; V _{CC} = 3.3 V	[1] -	-	75	µA

[1] One input at V_{CC} – 0.6 V, other input at V_{CC} or GND.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

Symbol	Parameter	Conditions	25 °C			−40 °C to +125 °C			Unit
			Min	Typ ^[1]	Max	Min	Max (85 °C)	Max (125 °C)	
C _L = 5 pF									
t _{pd}	propagation delay	A to nY; see Figure 5 ^[2]							
		V _{CC} = 0.8 V	-	20.4	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.7	5.6	10.6	2.4	10.7	10.7	ns
		V _{CC} = 1.4 V to 1.6 V	2.4	3.9	6.1	2.2	6.5	6.7	ns
		V _{CC} = 1.65 V to 1.95 V	1.8	3.1	4.7	1.6	5.3	5.6	ns
		V _{CC} = 2.3 V to 2.7 V	1.6	2.4	3.6	1.4	4.0	4.2	ns
		V _{CC} = 3.0 V to 3.6 V	1.4	2.2	3.1	1.2	3.4	3.5	ns
t _{en}	enable time	S to nY; see Figure 6 ^[3]		-					
		V _{CC} = 0.8 V	-	46.1	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.1	5.6	9.7	2.9	10.1	11.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.5	4.0	6.2	2.2	6.6	7.3	ns
		V _{CC} = 1.65 V to 1.95 V	2.1	3.3	5.1	1.8	5.5	6.1	ns
		V _{CC} = 2.3 V to 2.7 V	1.7	2.7	3.9	1.4	4.2	4.6	ns
		V _{CC} = 3.0 V to 3.6 V	1.5	2.4	3.5	1.2	3.7	4.1	ns
t _{dis}	disable time	S to nY; see Figure 6 ^[4]							
		V _{CC} = 0.8 V	-	12.6	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.0	4.7	7.5	2.9	7.9	8.7	ns
		V _{CC} = 1.4 V to 1.6 V	2.3	3.5	5.2	2.2	5.5	6.1	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	3.4	4.8	2.1	5.1	5.6	ns
		V _{CC} = 2.3 V to 2.7 V	1.7	2.5	3.6	1.5	3.9	4.3	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	2.9	3.8	1.8	4.1	4.5	ns
C _L = 10 pF									
t _{pd}	propagation delay	A to nY; see Figure 5 ^[2]							
		V _{CC} = 0.8 V	-	23.9	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.9	6.4	12.2	2.9	12.3	12.3	ns
		V _{CC} = 1.4 V to 1.6 V	2.7	4.5	7.1	2.4	7.6	7.9	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	3.7	5.5	2.1	6.0	6.3	ns
		V _{CC} = 2.3 V to 2.7 V	1.9	3.0	4.2	1.8	4.6	4.9	ns
		V _{CC} = 3.0 V to 3.6 V	1.8	2.7	3.9	1.6	4.1	4.3	ns

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

Symbol	Parameter	Conditions	25 °C			–40 °C to +125 °C			Unit
			Min	Typ ^[1]	Max	Min	Max (85 °C)	Max (125 °C)	
t _{en}	enable time	S to nY; see Figure 6 ^[3]							
		V _{CC} = 0.8 V	-	50.1	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	6.5	11.1	3.3	11.6	12.8	ns
		V _{CC} = 1.4 V to 1.6 V	2.9	4.6	7.0	2.6	7.6	8.4	ns
		V _{CC} = 1.65 V to 1.95 V	2.5	3.9	5.8	2.2	6.3	6.9	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.2	4.6	1.7	4.9	5.4	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	2.9	4.2	1.6	4.4	4.8	ns
t _{dis}	disable time	S to nY; see Figure 6 ^[4]							
		V _{CC} = 0.8 V	-	14.5	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.1	5.8	8.7	3.9	9.1	10.0	ns
		V _{CC} = 1.4 V to 1.6 V	3.2	4.4	6.1	3.0	6.5	7.2	ns
		V _{CC} = 1.65 V to 1.95 V	3.3	4.5	6.0	3.2	6.3	6.9	ns
		V _{CC} = 2.3 V to 2.7 V	2.4	3.3	4.4	2.2	4.7	5.2	ns
		V _{CC} = 3.0 V to 3.6 V	3.1	4.1	5.2	3.0	5.5	6.1	ns
C_L = 15 pF									
t _{pd}	propagation delay	A to nY; see Figure 5 ^[2]							
		V _{CC} = 0.8 V	-	27.4	-				ns
		V _{CC} = 1.1 V to 1.3 V	3.4	7.2	13.7	3.2	13.9	13.9	ns
		V _{CC} = 1.4 V to 1.6 V	3.2	5.0	7.9	2.8	8.7	9.1	ns
		V _{CC} = 1.65 V to 1.95 V	2.5	4.2	6.3	2.4	7.0	7.4	ns
		V _{CC} = 2.3 V to 2.7 V	2.3	3.4	4.9	2.2	5.3	5.7	ns
		V _{CC} = 3.0 V to 3.6 V	2.2	3.2	4.4	1.9	4.8	5.0	ns
t _{en}	enable time	S to nY; see Figure 6 ^[3]							
		V _{CC} = 0.8 V	-	53.9	-				ns
		V _{CC} = 1.1 V to 1.3 V	4.1	7.3	12.4	3.6	12.9	14.2	ns
		V _{CC} = 1.4 V to 1.6 V	3.3	5.2	7.8	2.9	8.4	9.2	ns
		V _{CC} = 1.65 V to 1.95 V	2.9	4.4	6.4	2.5	7.0	7.7	ns
		V _{CC} = 2.3 V to 2.7 V	2.5	3.6	5.2	2.1	5.5	6.1	ns
		V _{CC} = 3.0 V to 3.6 V	2.3	3.4	4.8	1.9	4.9	5.4	ns
t _{dis}	disable time	S to nY; see Figure 6 ^[4]							
		V _{CC} = 0.8 V	-	16.3	-				ns
		V _{CC} = 1.1 V to 1.3 V	5.1	6.9	10.0	4.9	10.4	11.4	ns
		V _{CC} = 1.4 V to 1.6 V	4.0	5.3	7.1	3.8	7.4	8.1	ns
		V _{CC} = 1.65 V to 1.95 V	4.3	5.6	7.3	4.2	7.6	8.4	ns
		V _{CC} = 2.3 V to 2.7 V	3.1	4.1	5.3	3.0	5.6	6.2	ns
		V _{CC} = 3.0 V to 3.6 V	4.2	5.3	6.6	4.1	6.9	7.6	ns

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

Symbol	Parameter	Conditions	25 °C			–40 °C to +125 °C			Unit
			Min	Typ ^[1]	Max	Min	Max (85 °C)	Max (125 °C)	
C _L = 30 pF									
t _{pd}	propagation delay	A to nY; see Figure 5 ^[2]							
		V _{CC} = 0.8 V	-	37.8	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.1	9.5	18.0	4.1	18.5	18.9	ns
		V _{CC} = 1.4 V to 1.6 V	3.7	6.6	10.4	3.8	11.5	12.1	ns
		V _{CC} = 1.65 V to 1.95 V	3.4	5.5	8.3	3.3	9.2	9.8	ns
		V _{CC} = 2.3 V to 2.7 V	3.2	4.5	6.3	3.0	6.8	7.3	ns
		V _{CC} = 3.0 V to 3.6 V	3.1	4.2	5.8	2.9	6.6	7.0	ns
t _{en}	enable time	S to nY; see Figure 6 ^[3]							
		V _{CC} = 0.8 V	-	66.3	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	5.3	9.6	16.4	4.7	17.0	18.7	ns
		V _{CC} = 1.4 V to 1.6 V	4.4	6.8	10.0	3.9	10.9	12.0	ns
		V _{CC} = 1.65 V to 1.95 V	4.0	5.7	8.2	3.4	8.9	9.8	ns
		V _{CC} = 2.3 V to 2.7 V	3.4	4.8	6.6	2.9	7.0	7.7	ns
		V _{CC} = 3.0 V to 3.6 V	3.2	4.5	6.1	2.8	6.5	7.2	ns
t _{dis}	disable time	S to nY; see Figure 6 ^[4]							
		V _{CC} = 0.8 V	-	21.8	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	8.2	10.4	14.3	8.0	14.7	16.2	ns
		V _{CC} = 1.4 V to 1.6 V	6.5	8.0	10.0	6.3	10.4	11.4	ns
		V _{CC} = 1.65 V to 1.95 V	7.4	9.0	11.0	7.3	11.3	12.4	ns
		V _{CC} = 2.3 V to 2.7 V	5.3	6.5	7.9	5.2	8.2	9.0	ns
		V _{CC} = 3.0 V to 3.6 V	7.6	9.0	10.7	7.4	11.0	12.1	ns

Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see [Figure 7](#).

Symbol	Parameter	Conditions	25 °C			–40 °C to +125 °C			Unit
			Min	Typ ^[1]	Max	Min	Max (85 °C)	Max (125 °C)	

$C_L = 5 \text{ pF}, 10 \text{ pF}, 15 \text{ pF}$ and 30 pF

C_{PD}	power dissipation capacitance	$f_i = 1 \text{ MHz};$ $V_i = \text{GND to } V_{CC}$	^[5]						
		$V_{CC} = 0.8 \text{ V}$	-	2.8	-	-	-	-	pF
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$	-	2.9	-	-	-	-	pF
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	-	3.0	-	-	-	-	pF
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	-	3.2	-	-	-	-	pF
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	3.7	-	-	-	-	pF
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	-	4.2	-	-	-	-	pF

[1] All typical values are measured at nominal V_{CC} .

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

[3] t_{en} is the same as t_{PZH} and t_{PZL} .

[4] t_{dis} is the same as t_{PHZ} and t_{PLZ} .

[5] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

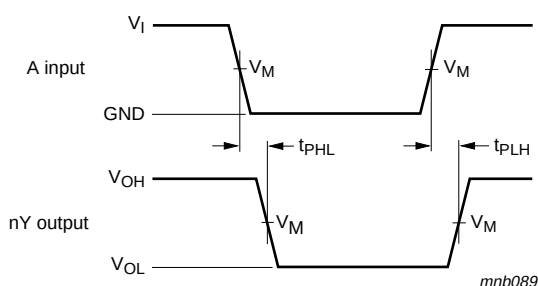
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

12. Waveforms



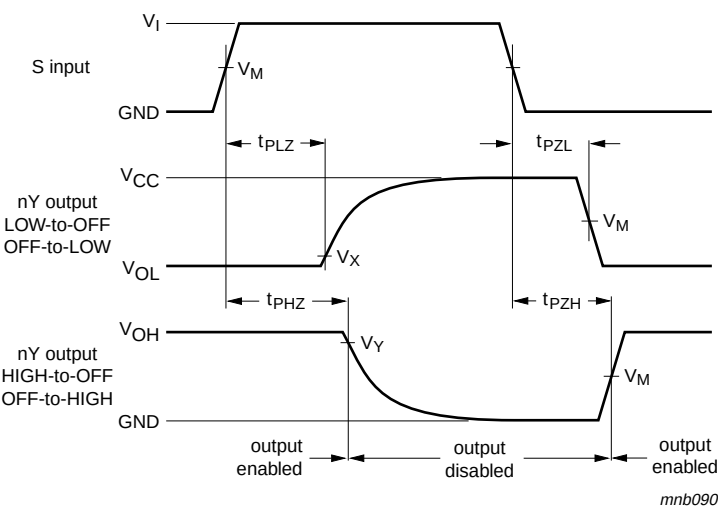
Measurement points are given in [Table 9](#).

Logic levels: V_{OL} and V_{OH} are typical output voltage drop that occur with the output load.

Fig 5. The data input (A) to output (nY) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input		
V_{CC}	V_M	V_M	V_i	$t_r = t_f$
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V_{CC}	$\leq 3.0 \text{ ns}$



Measurement points are given in [Table 10](#).
Logic levels: V_{OL} and V_{OH} are typical output voltage drop that occur with the output load.

Fig 6. Enable and disable times

Table 10. Measurement points

Supply voltage	Input	Output		
V_{CC}	V_M	V_M	V_X	V_Y
0.8 V to 1.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.1 \text{ V}$	$V_{OH} - 0.1 \text{ V}$
1.65 V to 2.7 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.15 \text{ V}$	$V_{OH} - 0.15 \text{ V}$
3.0 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.3 \text{ V}$	$V_{OH} - 0.3 \text{ V}$

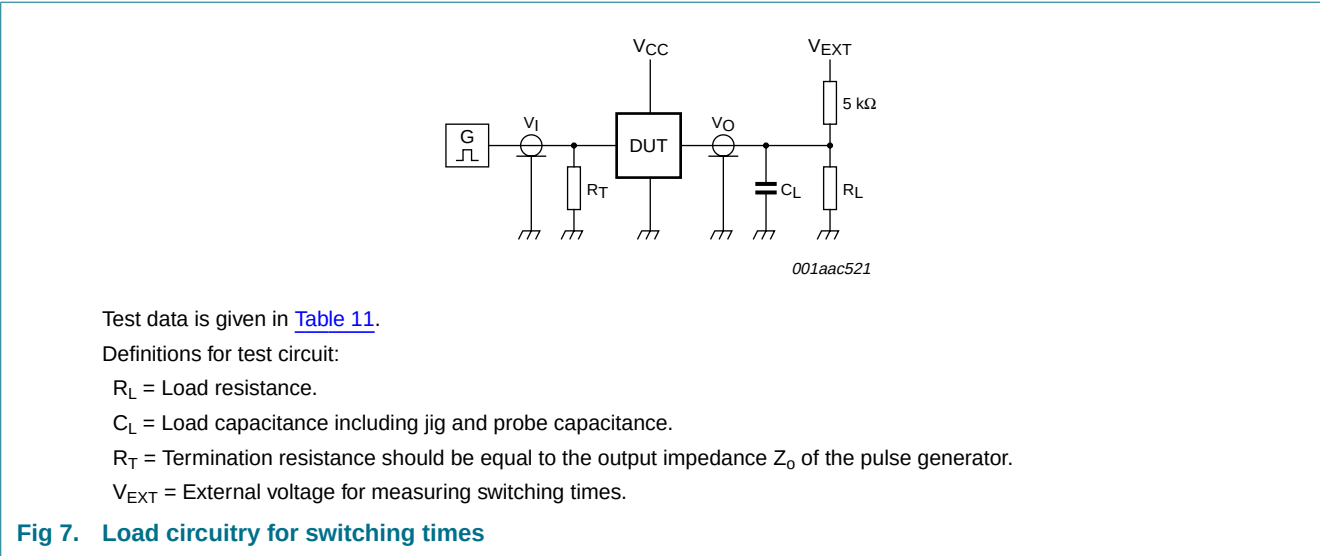


Table 11. Test data

Supply voltage	Load		V_{EXT}		
V_{CC}	C_L	R_L ^[1]	t_{PLH} , t_{PHL}	t_{PZH} , t_{PHZ}	t_{PZL} , t_{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	$2 \times V_{CC}$

[1] For measuring enable and disable times $R_L = 5\text{ k}\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1\text{ M}\Omega$.

13. Package outline

Plastic surface-mounted package; 6 leadsSOT363

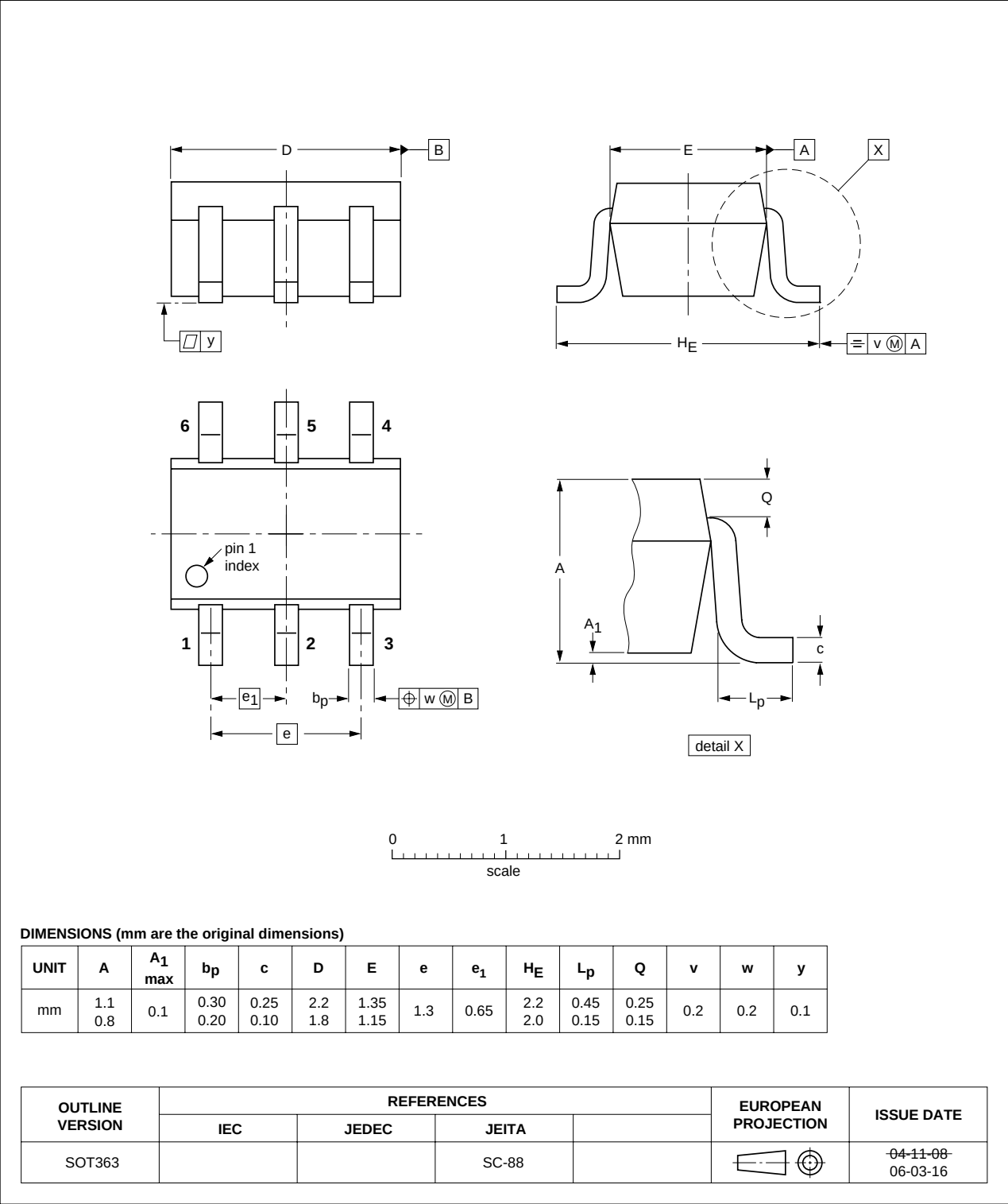


Fig 8. Package outline SOT363 (SC-88)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886

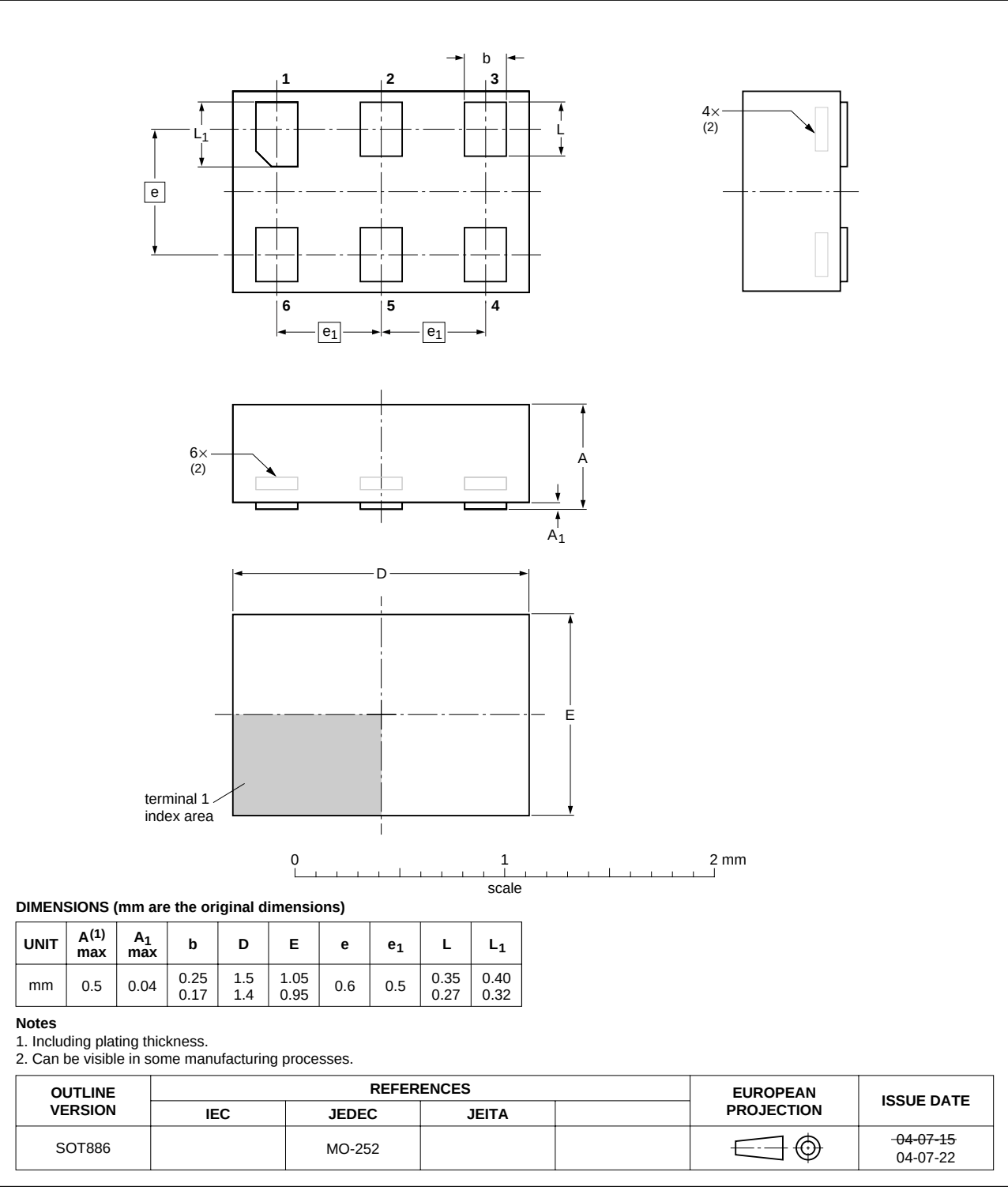


Fig 9. Package outline SOT886 (XSON6)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm

SOT891

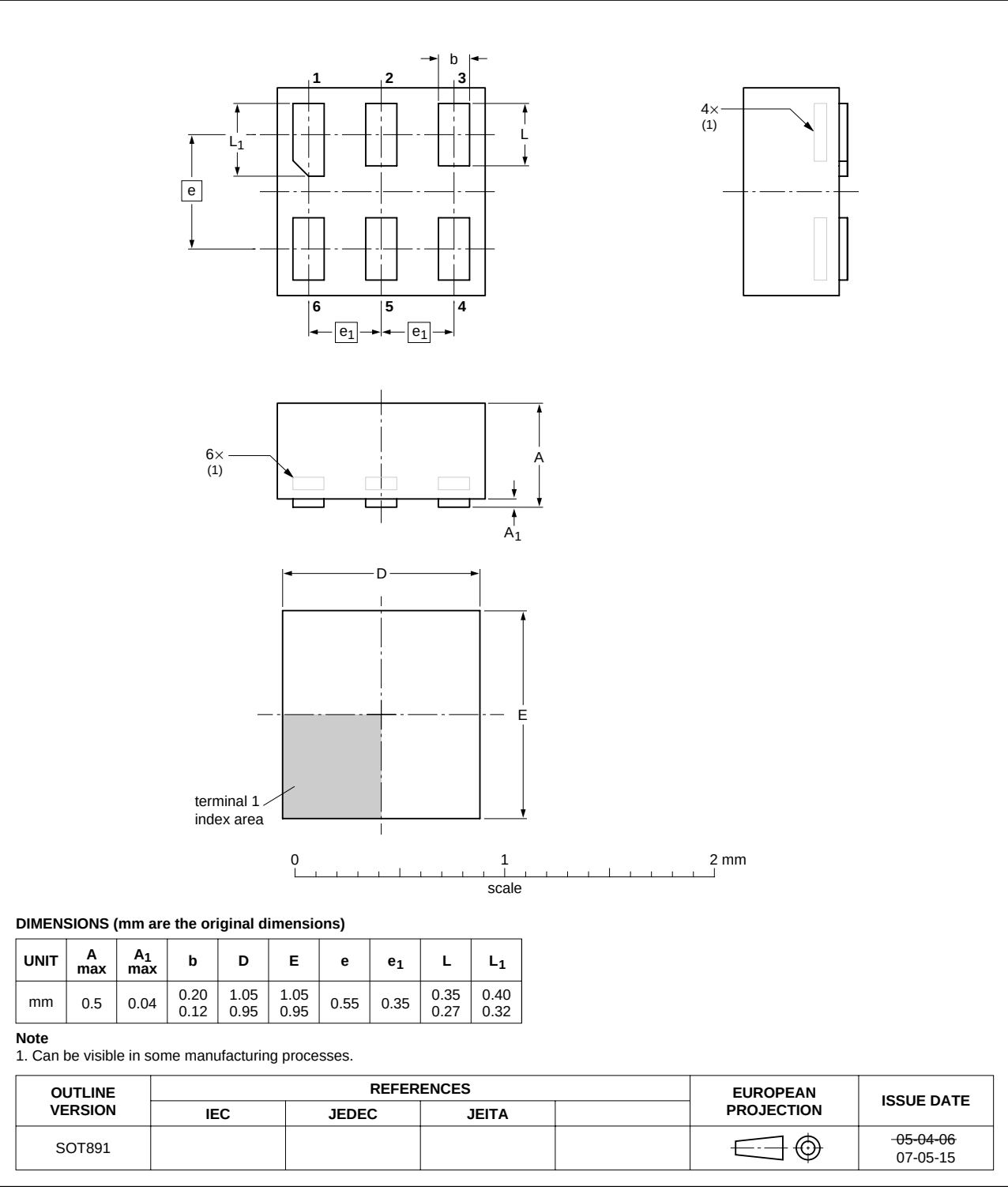


Fig 10. Package outline SOT891 (XSON6)

14. Abbreviations

Table 12. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

15. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G18_2	20080403	Product data sheet	-	74AUP1G18_1
Modifications:	• Section 2 “Features”: Changed: ESD HBM voltage from 4000 V to 5000 V. • Section 10 “Static characteristics”: Changed: input capacitance. • Section 11 “Dynamic characteristics”: Changed: propagation delays, enable times, disable times and typical power dissipation capacitance.			
74AUP1G18_1	20061013	Product data sheet	-	-

16. Legal information

16.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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