PRELIMINARY DATA SHEET

NEC

N-CHANNEL GaAs MES FET

NES1823P-50

50 W L-BAND PUSH-PULL POWER GaAs MES FET

DESCRIPTION

The NES1823P-50 is a 50 W push-pull type GaAs MES FET designed for high power transmitter applications for PCS, DCS, PHS, and IMT2000 base station systems. It is capable of delivering 50 W of output power (CW) with high linear gain, high efficiency and excellent distortion. Its primary band is 1.8 to 2.3 GHz, however with different matching, 60 MHz or less of instantaneous bandwidth can be achieved anywhere from 0.8 to 2.3 GHz. The device employs 0.9 μm Tungsten Silicide gates, via holes, plated heat sink, and silicon dioxide passivation for superior performance, thermal characteristics, and reliability.

Reliability and performance uniformity are assured by NEC's stringent quality and control procedures.

FEATURES

- Push-pull type N-channel GaAs MES FET
- High output power: P_{out} = 50 W TYP.
- High linear gain: G_L = 10.5 dB TYP.
- High power added efficiency: η_{add} = 40 % TYP. @ V_{DS} = 10.0 V, I_{Dset} = 4.0 A (total), f = 2.20 GHz

ORDERING INFORMATION (PLAN)

Part Number	Package	Supplying Form
NES1823P-50	T-86	ESD protective envelope

Remark To order evaluation samples, consult your NEC sales representative.

Caution Please handle this device at static-free workstation, because this is an electrostatic sensitive device.

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.
Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

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ABSOLUTE MAXIMUM RATINGS (Unless otherwise specified, T_A = +25 °C)

Operation in excess of any one of these parameters may result in permanent damage.

Parameter	Symbol	Ratings	Unit
Drain to Source Voltage	V_{DS}	15	V
Gate to Source Voltage	V_{GSO}	–7	V
Gate to Drain Voltage	V_{GDO}	–18	V
Drain Current	I_D	30	A
Gate Current	I_G	200	mA
Total Power Dissipation	P_{tot} Note	110	W
Channel Temperature	T_{ch}	175	°C
Storage Temperature	T_{stg}	–65 to +175	°C

Note T_C = +25 °C

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Drain to Source Voltage	V_{DS}		–	–	10.0	V
Gain Compression	Gcomp		–	–	3.0	dB
Channel Temperature	T_{ch}		–	–	+150	°C
Set Drain Current	I_{Dset}	V_{DS} = 10.0 V, RF OFF	–	4.0	7.0	A
Gate Resistance	R_g Note		–	–	20	Ω

Note R_g is the series resistance between the gate supply and the FET gate.

ELECTRICAL CHARACTERISTICS (T_A = +25 °C)

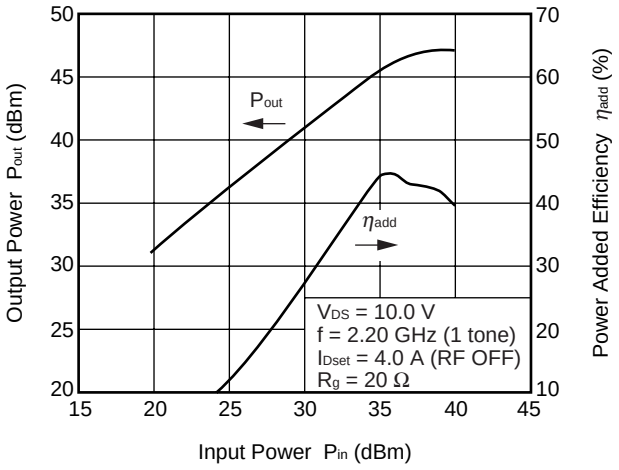
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Saturated Drain Current	I_{DSS}	V_{DS} = 2.5 V, V_{GS} = 0 V	–	30.0	–	A
Pinch-off Voltage	V_p	V_{DS} = 2.5 V, I_D = 130 mA	–4.0	–2.6	–	V
Thermal Resistance	R_{th}	Channel to Case	–	1.0	1.5	°C/W
Output Power	P_{out}	f = 2.20 GHz, V_{DS} = 10.0 V,	46.0	47.0	–	dBm
Drain Current	I_D	P_{in} = 39.5 dBm, R_g = 20 Ω,	–	12.5	16.0	A
Power Added Efficiency	η_{add}	I_{Dset} = 4.0 A Total (RF OFF) Note 1	–	40	–	%
Linear Gain	G_L Note 2		9.5	10.5	–	dB
3rd Order Intermodulation Distortion	IM_3	Δf = 5 MHz, P_{out} = 39 dBm (2 tones total)	–	–36	–	dBc

Notes 1. I_{Dset} = 2.0 A each drain

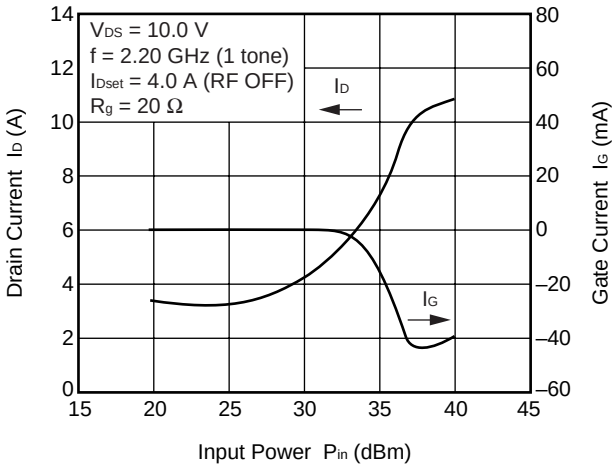
2. P_{in} = 22 dBm

TYPICAL CHARACTERISTICS (T_A = +25 °C)

OUTPUT POWER, POWER ADDED EFFICIENCY vs. INPUT POWER



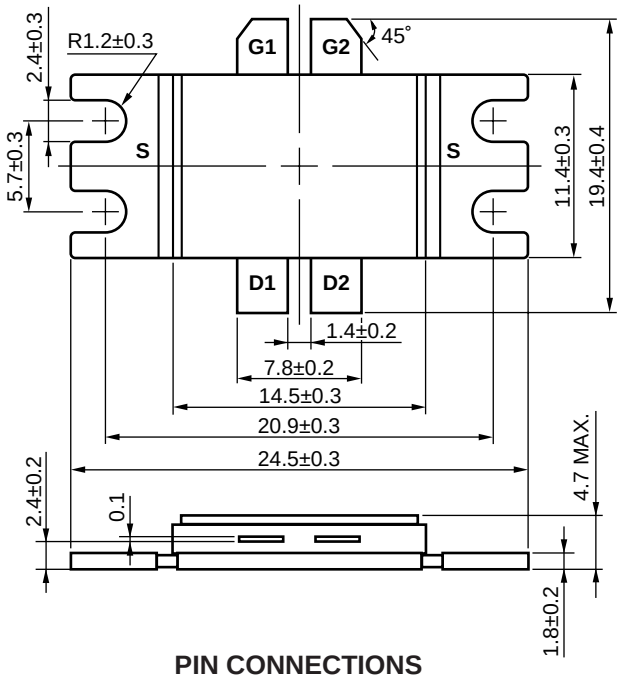
DRAIN CURRENT, GATE CURRENT vs. INPUT POWER



Remark The graphs indicate nominal characteristics.

PACKAGE DIMENSIONS

T-86 (UNIT: mm)



PIN CONNECTIONS

G1, G2 : Gate
D1, D2 : Drain
S : Source

RECOMMENDED MOUNTING CONDITIONS FOR CORRECT USE

(1) Fix to heat sink or mount surface completely with screws at the four holes of the flange.

(2) The recommended torque strength of the screws is 30 N typical using M2.3 type screws.

(3) The recommended flatness of the mount surface is less than ±10 μm (roughness of surface is $\nabla\nabla\nabla$).

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your NEC sales representative.

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Partial Heating	Pin temperature: 260 °C or below, Time: 5 seconds or less (per pin row)	–

For details of recommended soldering conditions, please contact your local NEC sales office.

[MEMO]

[MEMO]

CAUTION

The great care must be taken in dealing with the devices in this guide.
The reason is that the material of the devices is GaAs (Gallium Arsenide), which is designated as harmful substance according to the law concerned.
Keep the law concerned and so on, especially in case of removal.

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