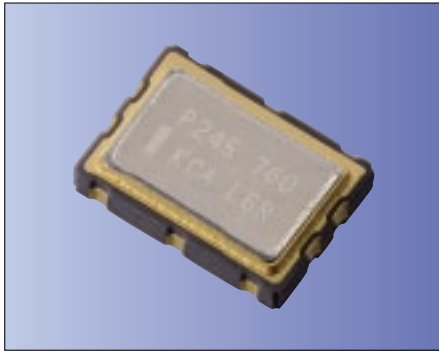


Voltage Controlled Crystal Oscillators (VCXO) Surface Mount Type KV7050R-P3 Series



CMOS/ 3.3V/ 7.0x5.0mm



RoHS Compliant

Features

- High frequency to 800MHz
- LV-PECL output
- Miniature ceramic package
- Highly reliable with seam welding
- for WDM, Networking Applications

Table 1

Freq. Tol. Code	Tol. $\times 10^{-6}$	Operating Temperature Range (°C)	Note
G	± 50	-40 to +85	Standard specifications With only certain frequencies

How to Order

KV7050R 622.080 P 3 G D 00
 ① ② ③ ④ ⑤ ⑥ ⑦

- ① Type (7.0x5.0mm SMD VCXO)
- ② Output Frequency
- ③ Output Type (LV-PECL)
- ④ Supply Voltage (3.3V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ INH Function (45/ 55%, Disable)
- ⑦ Customer Special Model Suffix (STD Specification is "00")

Packaging (Tape & Reel 1000 pcs./ reel)

Specifications

Item	Symbol	Conditions	Min.	Max.	Units
Output Frequency Range ^{Note1}	fo		10	800	MHz
Frequency Tolerance @Vc=+1.65V	f _{tol}	Initial tolerance, Operating temperature range, Rated power supply voltage change, Aging (1 year @25°C), Shock and vibration Op. Temp.: -40 to +85°C	-50	+50	$\times 10^{-6}$
Absolute Pull Range	APR		± 100	—	$\times 10^{-6}$
Control Voltage	Vc		0	+3.3	V
Storage Temperature Range	T _{stg}		-55	+125	°C
Operating Temperature Range	T _{use}		-40	+85	°C
Max. Supply Voltage	—		-0.5	+4.2	V
Supply Voltage	Vcc		+2.97	+3.63	V
Linearity	—	Vc=0V to +3.3V	-10	+10	%
Current Consumption	Icc		—	100	mA
Symmetry	SYM	50ohm @crossing point	45	55	%
Rise/ Fall Time (20% to 80% Output Level)	tr/ tf	50ohm	—	0.4	ns
Low Level Output Voltage ^{Note2}	VOL		—	Vcc-1.620	V
High Level Output Voltage ^{Note2}	VOH		Vcc-1.025	—	V
Output Load	—	LV-PECL Output	—	50	ohm
Low Level Input Voltage	VIL		—	30% Vcc	V
High Level Input Voltage	VIH		70% Vcc	—	V
Input Resistance	—		150	—	k ohm
Start-up Time	t _{str}	@Minimum operating voltage to be 0 sec.	—	10	ms
Disable Time	t _{dis}		—	200	ns
Enable Time	t _{ena}		—	2	ms
Phase Jitter	J _{Phase}	12kHz to 20MHz @622.08MHz	—	1.0	ps
Phase Noise @622.08MHz	—	- 40 (@10Hz offset) - 70 (@100Hz offset) - 95 (@1kHz offset) - 105 (@10kHz offset) - 105 (@100kHz offset) - 125 (@1MHz offset) - 135 (@10MHz offset)			dBc/ Hz

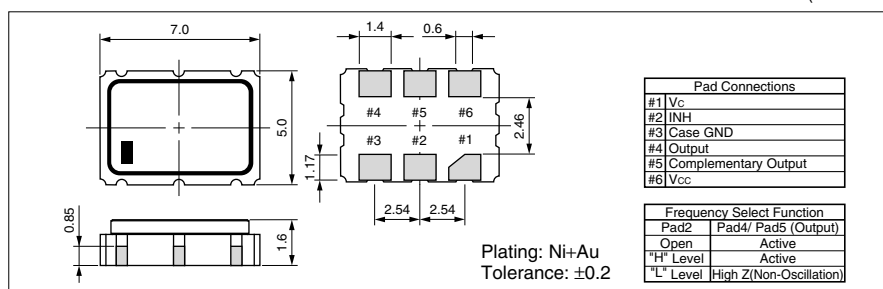
Note : All electrical characteristics are defined at the maximum load and operating temperature range.

Note1: Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Note2: DC characteristic

Dimensions

(Unit: mm)



Recommended Land Pattern

(Unit: mm)

