Vishay Dale



# 3 % DCR Tolerance, Low Profile, High Current Inductor

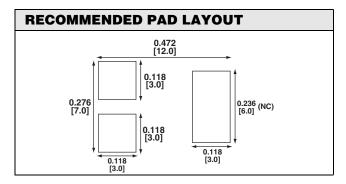


### Patents Pending

STANDARD ELECTRICAL SPECIFICATIONS					
Lo INDUCTANCE μH ± 20 % at 100 kHz, 0.25 V, 0 A	DCR mΩ ± 3 % at 25 °C	HEAT RATING CURRENT DC AMPS <sup>3</sup> TYPICAL	SATURATION CURRENT DC AMPS <sup>4</sup> TYPICAL		
0.34	0.88	32	36		
0.42	0.88	32	30		
0.50	0.88	32	25		
0.62	0.88	32	20		

#### NOTES:

- 1. All test data is referenced to 25 °C ambient
- 2. Operating Temperature Range 55 °C to + 125 °C
- 3. DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- 4. DC current (A) that will cause Lo to drop approximately 20 %
- 5. The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.



### **FEATURES**

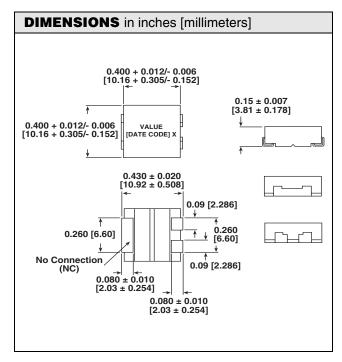
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without saturation
- 100 % lead (Pb)-free and RoHS compliant

# Pb-free

ROHS

### **APPLICATIONS**

- Notebook/Desktop/Server applications
- · High current POL converters
- Low profile, high current power supplies
- · Battery powered devices
- DC/DC converters in distributed power systems



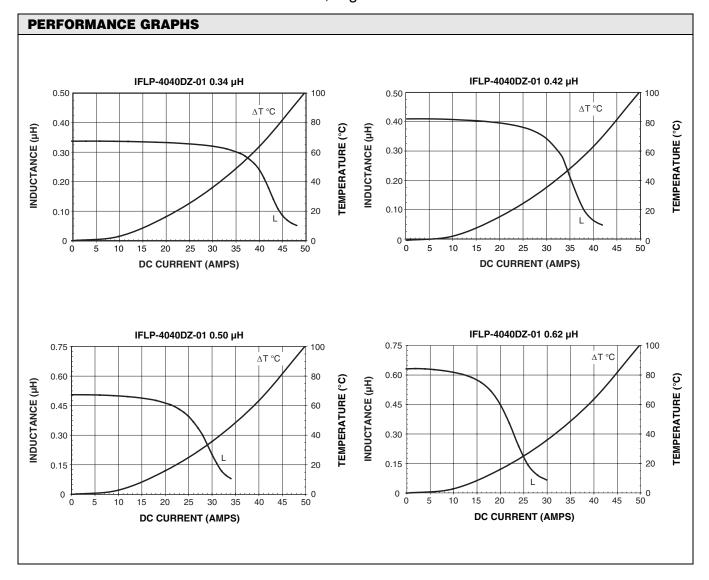
DESCRIPTION					
IFLP-4040DZ-01 MODEL	<b>0.42 μΗ</b> INDUCTANCE VALUE	± 20 % INDUCTANCE TOLERANCE	<b>ER</b> PACKAGE CODE	<b>e3</b> JEDEC LEAD (Pb)-FREE STANDARD	
GLOBAL PART NUMBER					
I F L MODEL	P 4 0 4		E R R 4  PACKAGE INDUCTAN VALUE	2 M 0 1  CE INDUCTANCE SERIES TOLERANCE	





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