

HTS8A60H/HTS8A80H

3 Quadrants Standard TRIAC

FEATURES

- ❑ Repetitive Peak Off-State Voltage : 600V/800V
- ❑ R.M.S On-State Current ($I_{T(RMS)} = 8A$)
- ❑ Gate Trigger Current : 35mA
- ❑ High commutation capability.

Applications

General purpose of AC switching, heating control, motor control, etc

General Description

Semihow's standard TRIAC product is a glass passivated device, has a high commutative performance, stable gate triggering level to temperature and high off state voltage. It is generally suitable for power and phase control in ac application

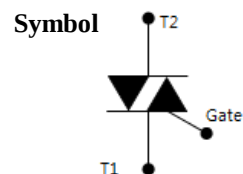
Absolute Maximum Ratings ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

$$V_{\text{DRM}} = 600\text{V}/800\text{V}$$

$$I_{\text{T(RMS)}} = 8 \text{ A}$$

$$I_{\text{TSM}} = 84 \text{ A}$$

$$I_{\text{GT}} = 35\text{mA}$$



Symbol	Parameter	Conditions	Ratings		Unit
			HTS8A60H	HTS8A80H	
V_{DRM}	Repetitive Peak Off-State Voltage	Sine wave, 50/60Hz, Gate open	600	800	V
V_{RRM}	Repetitive Peak Reverse Voltage		600	800	V
$I_{\text{T(AV)}}$	Average On-State Current	Full sine wave, $T_C = 97.6^{\circ}\text{C}$	7.2		A
$I_{\text{T(RMS)}}$	R.M.S. On-State Current		8		A
I_{TSM}	Surge On-State Current	½ cycle, 50Hz/60Hz, Sine wave, Non repetitive	80/84		A
I^2t	Fusing Current	$t = 10\text{ms}$	32		A ² S
P_{GM}	Forward Peak Gate Power Dissipation	$T_J = 125^{\circ}\text{C}$	5		W
$P_{\text{G(AV)}}$	Forward Average Gate Power Dissipation	$T_J = 125^{\circ}\text{C}$, over any 20ms	0.5		W
I_{FGM}	Forward Peak Gate Current	$T_J = 125^{\circ}\text{C}$, pulse width $\leq 20\mu\text{s}$	2		A
V_{RGM}	Reverse Peak Gate Voltage	$T_J = 125^{\circ}\text{C}$, pulse width $\leq 20\mu\text{s}$	10		V
T_J	Operating Junction Temperature		-40~+150		$^{\circ}\text{C}$
T_{STG}	Storage Temperature		-40~+150		$^{\circ}\text{C}$

Electrical Characteristics(T_J=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I _{DRM}	Repetitive Peak Off-State Current	V _D = V _{DRM}	T _J =25°C	-	-	50 uA
			T _J =125°C	-	-	5 mA
I _{RRM}	Repetitive Peak Reverse Current	V _D = V _{DRM}	T _J =25°C	-	-	50 uA
			T _J =125°C	-	-	5 mA
I _{GT}	Gate Trigger Current	V _D = 12V, R _L =330Ω	1+, 1-, 3-	-	-	35 mA
V _{GT}	Gate Trigger Voltage	V _D = 12V, R _L =330Ω	1+, 1-, 3-	-	-	1.5 V
V _{GD}	Non-Trigger Gate Voltage ¹	V _D = 12V, R _L =330Ω, T _J =125°C		0.2	-	- V
V _{TM}	Peak On-State Voltage	I _T = 11A, I _G = 20mA		-	1.2	1.5 V
dv/dt	Critical Rate of Rise of Off-State Voltage	V _D = 2/3 V _{DRM} , T _J =125°C		200	-	- V/us
I _H	Holding current	I _T = 0.2A		-	30	- mA

Notes :

1. Pulse Width ≤ 1.0ms, Duty Cycle ≤ 1%

Thermal Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
R _{θJC}	Thermal Resistance	Junction to Case			3.7	°C/W
R _{θJA}	Thermal Resistance	Junction to Ambient			58	°C/W

Typical Characteristics

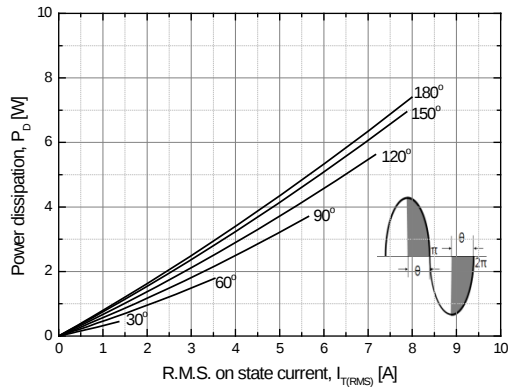


Fig 1. R.M.S. current vs. Power dissipation

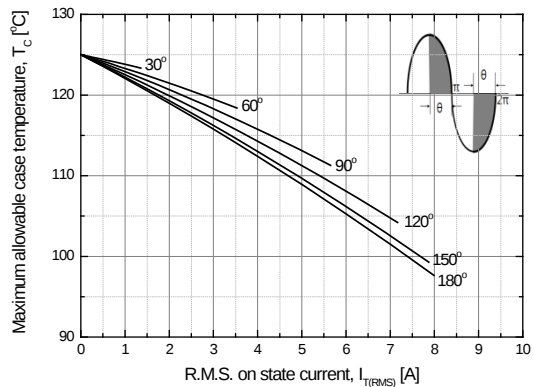


Fig 2. R.M.S. current vs. Case temperature

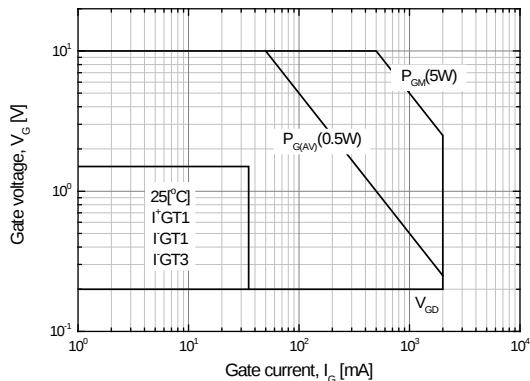


Fig 3. Gate power characteristics

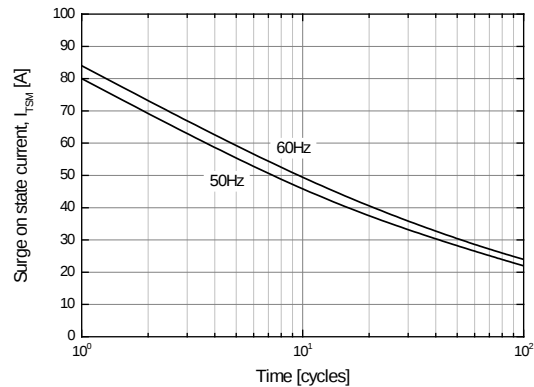


Fig 4. Surge on state current rating (Non-repetitive)

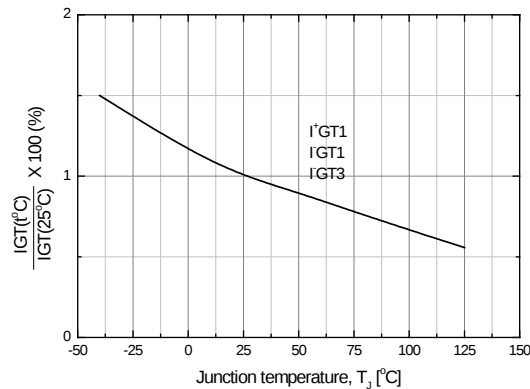


Fig 5. Gate trigger current vs. junction temperature

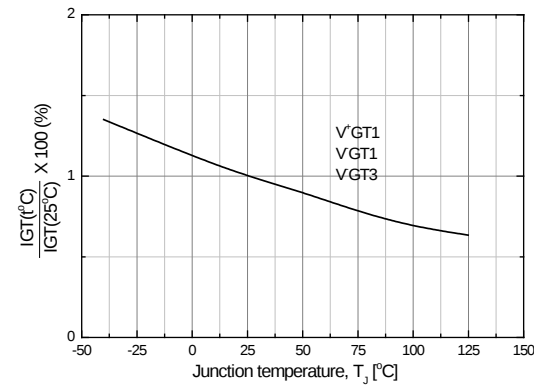


Fig 6. Gate trigger voltage vs. junction temperature

Typical Characteristics

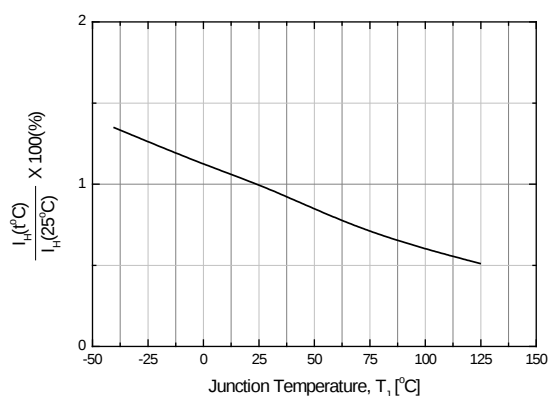


Fig 7. Holding current vs. Junction temperature

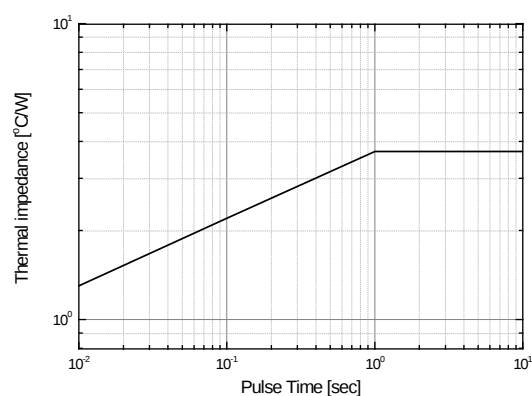


Fig 8. Thermal Impedance vs. pulse time

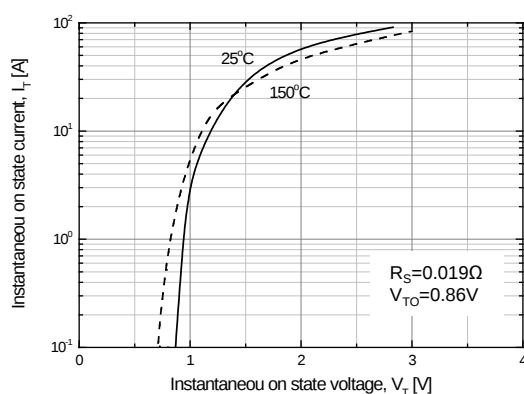
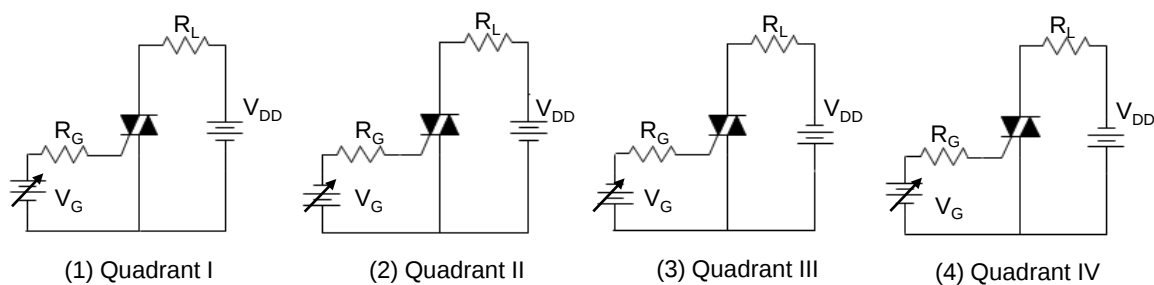


Fig 9. Instantaneous on state current vs. Instantaneous on state voltage

Measurement of gate trigger current



Note. Whole parameter and test condition can not be over absolute maximum ratings in this datasheet.

Package Dimension

TO-220F

