Digital I/O Board for PCI
DIO-6464T2-PCI

* Specifications, color and design of the products are subject to change without notice.
* The contents in this document are subject to change without notice.
* Visit the CONTEC website to check the latest details in the document.
* The information in the data sheets is as of May, 2018.

This product is a PCI bus-compliant interface board used to provide a digital signal I/O function on a PC.

DIO-6464T2-PCI features 64 unisolated TTL level inputs and 64 unisolated open-collector outputs.   You can use 16 input signals as interrupt inputs.   In addition, the digital filter function to prevent wrong recognition of input signals is provided.

Windows/Linux driver is bundled with this product.

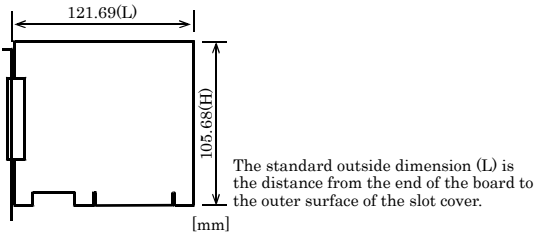Possible to be used as a data recording device for LabVIEW, with dedicated libraries.

Features

Unisolated TTL level input, unisolated open-collector output
DIO-6464T2-PCI has the 64ch of unisolated TTL level inputs and 64ch of unisolated open-collector outputs whose response speed is 200nsec.
Output rating : max 30VDC, 40mA per pin.

You can use 16 input signals as interrupt request signals.
You can use 16 input signals as interrupt request signals and also disable or enable the interrupt in bit units and select the edge of the input signals, at which to generate an interrupt.

This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering.
This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering.   All input terminals can be added a digital filter, and the setting can be performed by software.

Windows/Linux compatible driver libraries are attached.
Using the attached driver library API-PAC(W32) makes it possible to create applications of Windows/Linux.   In addition, a diagnostic program by which the operations of hardware can be checked is provided.

LabVIEW is supported by a plug-in of dedicated library VI-DAQ.
Using the dedicated library VI-DAQ makes it possible to create each application for LabVIEW.

Packing List

Board [DIO-6464T2-PCI] ...1
First step guide ... 1
Disk *1 [API-PAC(W32)] ...1
Warranty Certificate ...1
Serial Number Label...1

*1 The bundled disk contains the driver software and User's Guide.

Specification

Item		Specification
Input		
Input format		Unisolated TTL level input (Negative logic *1)
Number of input signal channels		64channels (16channels of them are available for interrupts) (1 common)
Input resistance		Pull up 10kΩ (1TTL load)
Interrupt		16 interrupt input signals are arranged into a single output of interrupt signal INTA. An interrupt is generated at the rising edge (HIGH-to-LOW transition) or falling edge (LOW-to-HIGH transition).
Response time		200nsec within
Output		
Output format		Unisolated open collector output (Negative logic *1)
Number of output signal channels		64channels (1 common)
Output rating	Output voltage	30VDC (Max.)
	Output current	40mA (per channel) (Max.)
Response time		Within 200nsec (change by pull-up registor value)
Common		
External supply capable current (Max.)		5VDC 1A *3
Allowable distance of signal extension		Approx. 1.5m (depending on wiring environment)
I/O address		Any 32-byte boundary
Interrupt Level		1 level use
Max. board count for connection		16 boards including the master board
Power consumption (Max.)		5VDC 450mA *4
Operating condition		0 - 50°C, 10 - 90%RH (No condensation)
Bus specification		PCI (32bit, 33MHz, Universal key shapes supported *2)
Dimension (mm)		121.69(L) x 105.68(H)
Connector		100 pin 0.8mm pitch connector [F (female) type] x 2 HDRA-E100W1LFDT1EC-SL+[HONDA TSUSHIN KOGYO CO., LTD.] equivalent to it
Weight		100g

*1   Data "0" and "1" correspond to the High and Low levels, respectively.
*2   This board requires power supply at +5V from an expansion slot (it does not work on a machine with a +3.3V power supply alone).
*3   Because it supplies the +5V power supply from an expansion slot, the external supply capable current depends on the PC.
*4   The power consumption does not include it in the external supply capable current.

121.69(L)

105.68(H)

The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.

[mm]

Support Software

Windows version of digital I/O driver API-DIO(WDM)/ API-DIO(98/PC)
[Stored on the bundled disk driver library API-PAC(W32)]
The API-DIO(WDM) is the Windows version driver library software that provides products in the form of Win32 API functions (DLL). Various sample programs such as Visual Basic and Visual C++, etc and diagnostic program *1useful for checking operation is rovided.

For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

Linux version of digital I/O driver API-DIO(LNX)
[Stored on the bundled disk driver library API-PAC(W32)]
The API-DIO(LNX) is the Linux version driver software which provides device drivers (modules) by shared library and kernel version.   Various sample programs of gcc are provided.

For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

LabVIEW-support data acquisition library DAQfast for LabVIEW (Available for downloading (free of charge) from the CONTEC web site.)
This is a data collection library to use in the LabVIEW by National Instruments.   With Polymorphic VI, our design enables a LabVIEW user to operate seamlessly.   Our aim is that the customers to perform easily, promptly what they wish to do.

For more details on the library and download of DAQfast for LabVIEW, please visit the CONTEC's Web site.

Data acquisition library for LabVIEW   VI-DAQ (Available for downloading (free of charge) from the CONTEC web site.)
This is a VI library to use in National Instruments LabVIEW.

VI-DAQ is created with a function form similar to that of LabVIEW's Data Acquisition VI, allowing you to use various devices without complicated settings.

For more details on the library and download of VI-DAQ, please visit the CONTEC's Web site.

Cable & Connector (Option)

Shielded Cable With Two 100pin Connector	PCB100PS-0.5 (0.5m)
	PCB100PS-1.5 (1.5m)
Connection Conversion Shield Cable (100P→96P)	PCB100/96PS-1.5(1.5m)
Flat Cable with One 100-Pin Connector	PCA100P-1.5(1.5m)
Connection Conversion Shield Cable (100P→37P D-SUB x 2)	
	PCB100WS-1.5(1.5m)

*If using both the CNA and CNB connectors, two cable sets are required.
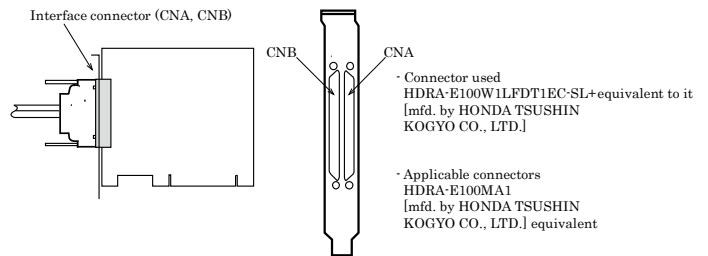
Accessories (Option)

Screw Terminal (M3 x 100P)	EPD-100A *1*4*6
Screw Terminal (M3 x 96P)	EPD-96A *2*4*6
Screw Terminal (M3.5 x 96P)	EPD-96 *2*4
Screw Terminal	DTP-64(PC) *2*4
Connector Conversion Board (96P→37P x 2)	CCB-96 *2*4
Signal Monitor for Digital I/O (64Bits)	CM-64L *2*4
Screw Terminal (M3 x 37P)	EPD-37A *3*5*6
Screw Terminal (M3.5 x 37P)	EPD-37 *3*5
General Purpose Terminal	DTP-3A *3*5
Screw Terminal	DTP-4A *3*5
Signal Monitor for Digital I/O (32Bits)	CM-32L *3*5

*1   PCB100PS-0.5, 1.5 optional cable is required separately.
*2   PCB100/96PS-1.5 optional cable is required separately.
*3   PCB100WS-1.5 optional cable is required separately.
*4   If using both the CNA and CNB connectors, two each of the terminal and cable sets are required.
*5   If using both the CNA and CNB connectors, two cable sets are required.
     You will also require sufficient terminal blocks for the number of I/O points you are using.
*6   "Spring-up" type terminal is used to prevent terminal screws from falling off.

*   Check the CONTEC's Web site for more information on these options.
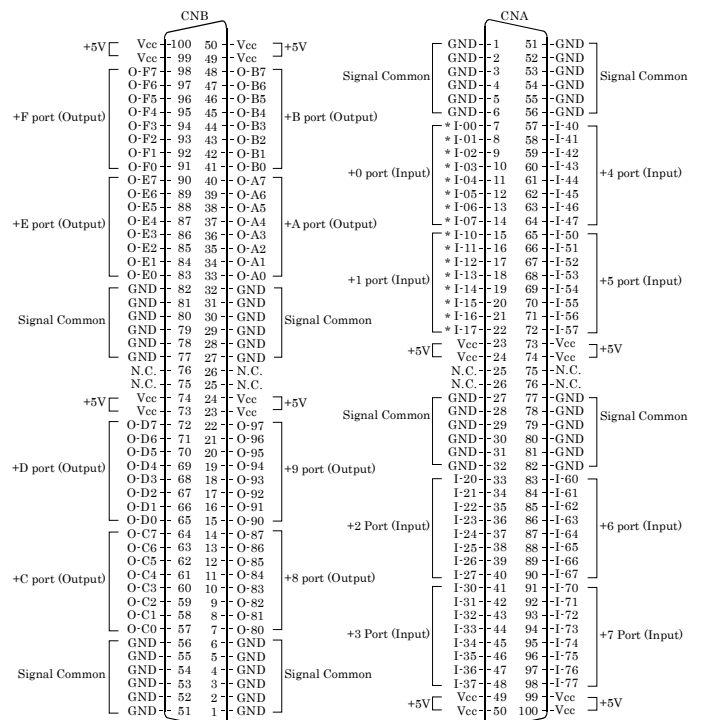
On-board connector wiring

Connector shape
The on-board interface connector (CNA, CNB) is used when connecting this product and the external devices.

Interface connector (CNA, CNB)

CNB   CNA

· Connector used
HDRA-E100W1LFDT1EC-SL+equivalent to it
[mfd. by HONDA TSUSHIN KOGYO CO., LTD.]

· Applicable connectors
HDRA-E100MA1
[mfd. by HONDA TSUSHIN KOGYO CO., LTD.] equivalent

Connector Pin Assignment
Pin Assignments of Interface Connector (CNA, CNB)

CNB

Signal	Pin	Pin	Signal	Port
Vcc	100	50	Vcc	+5V
Vcc	99	49	Vcc	
O-F7	98	48	O-B7	
O-F6	97	47	O-B6	
O-F5	96	46	O-B5	
O-F4	95	45	O-B4	+B port (Output)
O-F3	94	44	O-B3	
O-F2	93	43	O-B2	
O-F1	92	42	O-B1	
O-F0	91	41	O-B0	
O-E7	90	40	O-A7	
O-E6	89	39	O-A6	
O-E5	88	38	O-A5	
O-E4	87	37	O-A4	+A port (Output)
O-E3	86	36	O-A3	
O-E2	85	35	O-A2	
O-E1	84	34	O-A1	
O-E0	83	33	O-A0	
GND	82	32	GND	
GND	81	31	GND	
GND	80	30	GND	
GND	79	29	GND	Signal Common
GND	78	28	GND	
GND	77	27	GND	
N.C.	76	26	N.C.	
N.C.	75	25	N.C.	
Vcc	74	24	Vcc	+5V
Vcc	73	23	Vcc	
O-D7	72	22	O-97	
O-D6	71	21	O-96	
O-D5	70	20	O-95	
O-D4	69	19	O-94	+9 port (Output)
O-D3	68	18	O-93	
O-D2	67	17	O-92	
O-D1	66	16	O-91	
O-D0	65	15	O-90	
O-C7	64	14	O-87	
O-C6	63	13	O-86	
O-C5	62	12	O-85	
O-C4	61	11	O-84	+8 port (Output)
O-C3	60	10	O-83	
O-C2	59	9	O-82	
O-C1	58	8	O-81	
O-C0	57	7	O-80	
GND	56	6	GND	
GND	55	5	GND	
GND	54	4	GND	Signal Common
GND	53	3	GND	
GND	52	2	GND	
GND	51	1	GND	

Left column port labels (CNB): +F port (Output), +E port (Output), Signal Common, +5V, +D port (Output), +C port (Output), Signal Common

CNA

Signal	Pin	Pin	Signal	Port
GND	1	51	GND	
GND	2	52	GND	
GND	3	53	GND	Signal Common
GND	4	54	GND	
GND	5	55	GND	
GND	6	56	GND	
*I-00	7	57	I-40	
*I-01	8	58	I-41	
*I-02	9	59	I-42	
*I-03	10	60	I-43	+4 port (Input)
*I-04	11	61	I-44	
*I-05	12	62	I-45	
*I-06	13	63	I-46	
*I-07	14	64	I-47	
*I-10	15	65	I-50	
*I-11	16	66	I-51	
*I-12	17	67	I-52	
*I-13	18	68	I-53	+5 port (Input)
*I-14	19	69	I-54	
*I-15	20	70	I-55	
*I-16	21	71	I-56	
*I-17	22	72	I-57	
Vcc	23	73	Vcc	+5V
Vcc	24	74	Vcc	
N.C.	25	75	N.C.	
N.C.	26	76	N.C.	
GND	27	77	GND	
GND	28	78	GND	Signal Common
GND	29	79	GND	
GND	30	80	GND	
GND	31	81	GND	
GND	32	82	GND	
I-20	33	83	I-60	
I-21	34	84	I-61	
I-22	35	85	I-62	
I-23	36	86	I-63	+6 port (Input)
I-24	37	87	I-64	
I-25	38	88	I-65	
I-26	39	89	I-66	
I-27	40	90	I-67	
I-30	41	91	I-70	
I-31	42	92	I-71	
I-32	43	93	I-72	
I-33	44	94	I-73	+7 Port (Input)
I-34	45	95	I-74	
I-35	46	96	I-75	
I-36	47	97	I-76	
I-37	48	98	I-77	
Vcc	49	99	Vcc	+5V
Vcc	50	100	Vcc	

Left column port labels (CNA): Signal Common, *0 port (Input), *1 port (Input), Signal Common, +5V, Signal Common, +2 Port (Input), +3 Port (Input), +5V

* I-00 - I-17 can be used as interrupt signal.

I-00 - I-77	64 input signal pins. Connect output signals from the external device to these pins.
O-80 - O-F7	64 output signal pins. Connect these pins to the input signal pins of the external device.
Vcc	Output +5V.　The current that can be supplied is 1A(Max.). The permitted current per pin of connector is 0.3A. Connect the number of pins required to supply the total current.
GND	This pin is connected to GND in the slot. The permitted current per pin of connector is 0.3A. Connect the number of pins required to supply the total current for the 64 outputs.
N.C.	This pin is left unconnected.

Pin Assignments of Optional Connector PCB100/96PS or PCB100WS

The figure below shows the correspondence between the option cable pins and signals.

< Pin assignments for connecting a PCB100/96PS or PCB100WS to the DIO-6464T2-PCI >
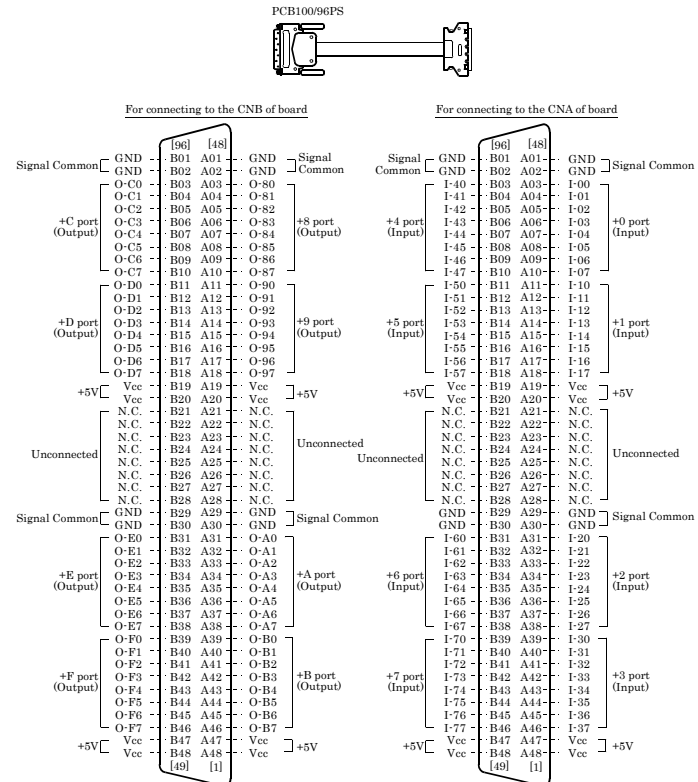
*　The numbers in square brackets [ ] are pin numbers designated by HONDA TSUSHIN KOGYO CO., LTD.
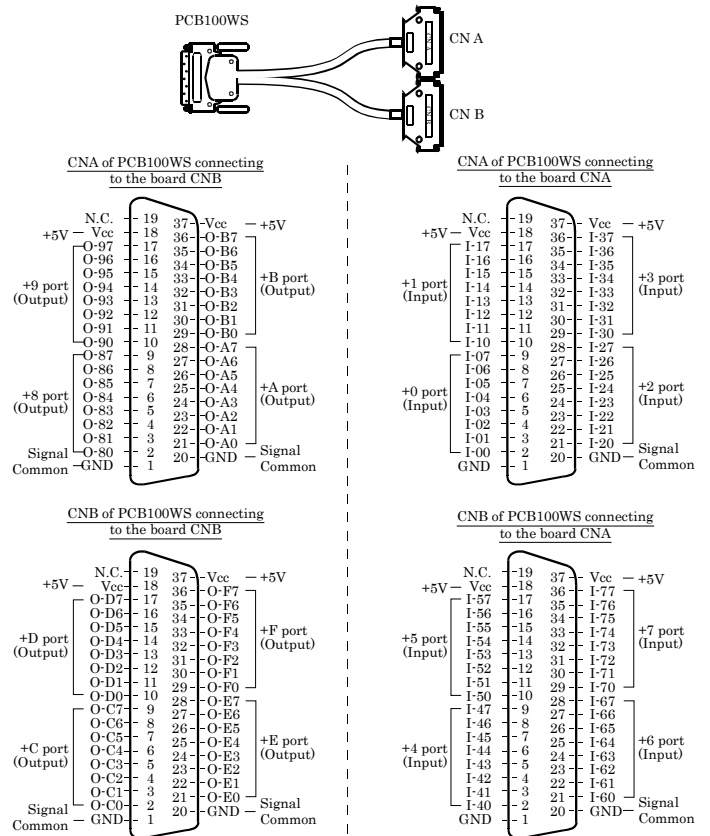
Connecting Input Signals
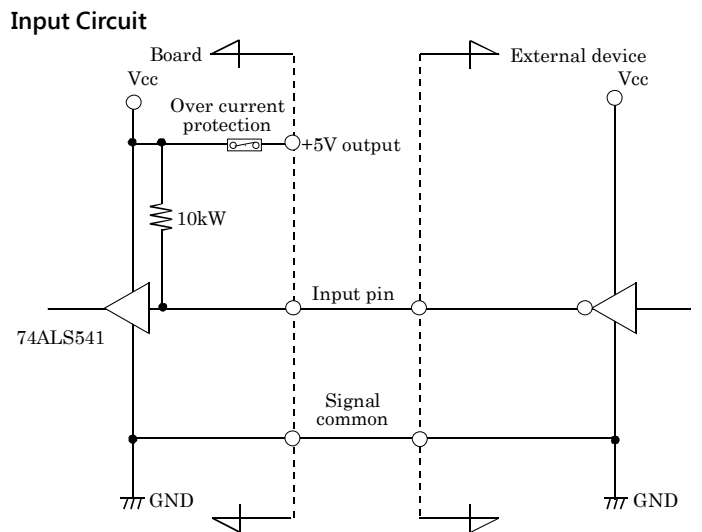
Input Circuit
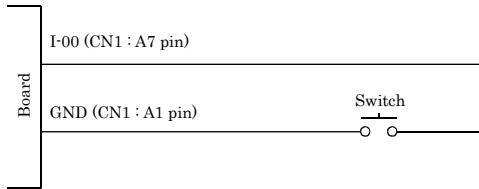

* I-xx represents an input pin.
One over current protection is connected for Vcc(+5V) terminal.

The input circuits of interface blocks of the < DIO-6464T2-PCI > is illustrated in Figure.
External digital signals given to signal inputs are TTL levels.　The individual input signals are passed to the personal computer as negative logic signals.　As each of the signal inputs is pulled up internally, the output of a relay contact or semiconductor switch can be connected directly between the signal input and the signal common pin.
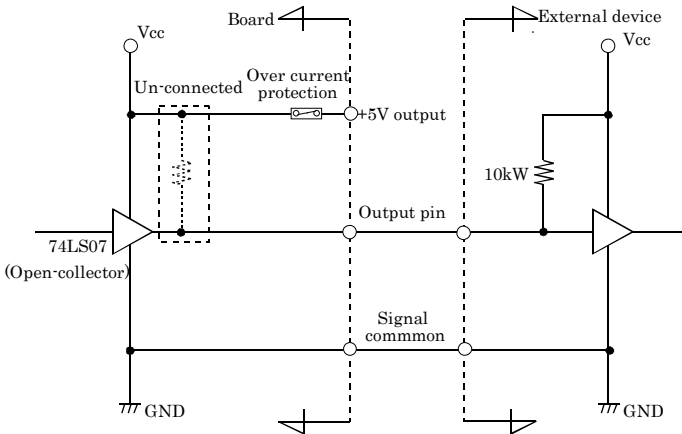
Connecting a Switch

I-00 (CN1 : A7 pin)

Board

GND (CN1 : A1 pin)   Switch

When the switch is ON, the corresponding bit contains 1.
When the switch is OFF, by contrast, the bit contains 0.

Connecting Output Signals

### Output Circuit

* O-xx represents an output pin.
One over current protection is connected for Vcc(+5V) terminal.

The output circuit of < DIO-6464T2-PCI > is illustrated in Figure.
Signal outputs are open-collector outputs; individual output
signals are sent to the external device as negative logic signals.
Note that each signal output must be pulled up at the external
device as it is not pulled up internally.

⚠ CAUTION
When the PC is turned on, all output are reset to OFF.

Connection to the LED

Vcc (CN1 : B23 pin)

Board side

2kΩ

LED

O-20 (CN1 : B7 pin)
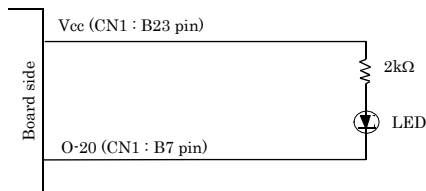
When "1" is output to a relevant bit, the corresponding LED comes on.
When "0" is output to the bit, in contrast, the LED goes out.

A Protection Function of the +5V Outputs

An overcurrent protection circuit is attached to this board.

Block Diagram