Finisar

Product Specification

OC-48 LR-1/STM L-16.1 RoHS Compliant Pluggable SFP Transceiver

FTLF1721P1xCL

PRODUCT FEATURES

- Up to 2.67Gb/s bi-directional data links
- Hot-pluggable SFP footprint
- Built-in digital diagnostic functions
- Uncooled 1310nm DFB laser transmitter
- APD Receiver
- Duplex LC connector
- RoHS Compliant
- Metal enclosure, for lower EMI
- Single 3.3V power supply
- Low power dissipation typically <700mW
- Extended operating temperature range: -10°C to 70°C



APPLICATIONS

- SONET OC-48 LR-1 / SDH STM L-16.1
- SONET OC-12 / SDH STM 4
- SONET OC-3 / SDH STM-1
- Gigabit Ethernet / 1x/2x Fibre Channel

Finisar's FTLF1721P1xCL Small Form Factor Pluggable (SFP) transceivers are compatible with the Small Form Factor Pluggable Multi-Sourcing Agreement (MSA)[1]. They comply with SONET OC-48 LR-1 (SDH STM L-16.1) standards[2], Gigabit Ethernet as specified in IEEE Std 802.3[3(*)] and Fibre Channel FC-PI 13.0[4(*)]. They are compatible with SONET OC-12 SR and SONET OC-3 standards[2]. Digital diagnostics functions are available via the 2-wire serial bus specified in the SFP MSA.

PRODUCT SELECTION

FTLF1721P1xCL

x	W	Wide Extraction Bail
	B	Narrow Extraction Bail

I. Pin Descriptions

Pin	Symbol	Name/Description	Ref.
1	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1
2	T_{FAULT}	Transmitter Fault.	
3	T_{DIS}	Transmitter Disable. Laser output disabled on high or open.	2
4	MOD_DEF(2)	Module Definition 2. Data line for Serial ID.	3
5	MOD_DEF(1)	Module Definition 1. Clock line for Serial ID.	3
6	MOD_DEF(0)	Module Definition 0. Grounded within the module.	3
7	Rate Select	No connection required	4
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	5
9	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
10	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
11	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
15	V_{CCR}	Receiver Power Supply	
16	V_{CCT}	Transmitter Power Supply	
17	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1

Notes:
1. Circuit ground is internally isolated from chassis ground.
2. Laser output disabled on T_{DIS} >2.0V or open, enabled on T_{DIS} <0.8V.
3. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. MOD_DEF(0) pulls line low to indicate module is plugged in.
4. Finisar FTLFxx21xxxxx transceivers operate between OC-3 and OC-48, 1x and 2x Fibre Channel, and Gigabit Ethernet data rates and respective protocols without active control. Finisar FTRJxx19xxxxx transceivers operate at 1x and 2x Fibre Channel, and Gigabit Ethernet data rates and respective protocols without active control.
5. LOS is open collector output. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

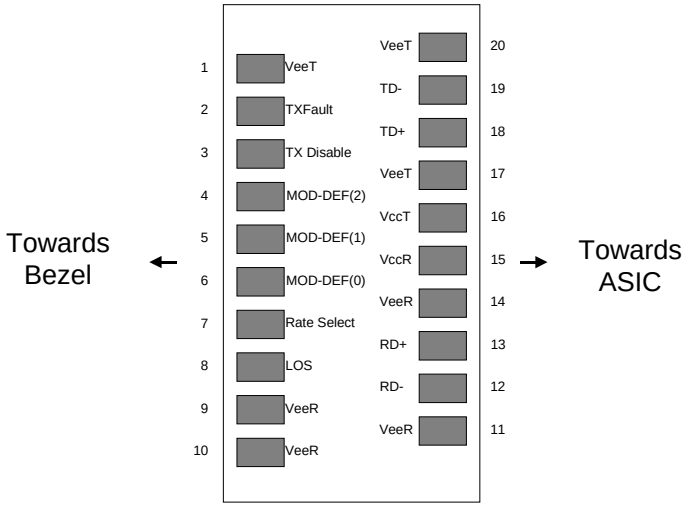


Diagram of Connector Block Pinout on the Host Board

II. Absolute Maximum Ratings

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Maximum Supply Voltage	Vcc	-0.5		4.5	V	
Storage Temperature	T_S	-40		85	°C	
Case Operating Temperature	T_{OP}	-10		70	°C	

III. Electrical Characteristics (T_{OP} = -10 to 70 °C, V_{CC} = 3.00 to 3.60 Volts)

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Supply Voltage	Vcc	3.00		3.60	V	
Supply Current	Icc		230	300	mA	
Transmitter						
Input differential impedance	R_{in}		100		Ω	1
Single ended data input swing	Vin,pp	250		1200	mV	
Transmit Disable Voltage	V_D	Vcc – 1.3		Vcc	V	
Transmit Enable Voltage	V_{EN}	Vee		Vee+ 0.8	V	2
Transmit Disable Assert Time				10	us	
Receiver						
Single ended data output swing	Vout,pp	300	400	800	mV	3
Data output rise time	t_r		100	175	ps	4
Data output fall time	t_f		100	175	ps	4
LOS Fault	$V_{LOS\ fault}$	Vcc – 0.5		Vcc_{HOST}	V	5
LOS Normal	$V_{LOS\ norm}$	Vee		Vee+0.5	V	5
Power Supply Rejection	PSR	100			mVpp	6
Total Generated Reciever Jitter (peak to peak)	J_{RX}p-p			0.07	UI	
Total Generated Reciever Jitter (rms)	J_{RX}rms			0.007	UI	

Notes:
1. AC coupled
2. Or open circuit.
3. Into 100 ohms differential termination.
4. 10 – 90 %
5. Loss Of Signal is LVTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
6. All transceiver specifications are compliant with a power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the power supply filtering network shown on page 23 of the Small Form-factor Pluggable (SFP) Transceiver MultiSource Agreement (MSA), September 14, 2000.

IV. Optical Characteristics (T_{OP} = -10 to 70°C, V_{CC} = 3.00 to 3.60 Volts)

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Transmitter						
Output Opt. Pwr: 9/125 SMF	P_{OUT}	-2		+3	dBm	1
Optical Wavelength	λ	1280		1335	nm	2
Spectral Width (-20dB)	σ			1	nm	
Optical Extinction Ratio	ER	9			dB	
Sidemode Supression ratio	SSR_{min}	30			dB	
Optical Rise/Fall Time	t_r/ t_f			160	ps	3
Total Generated Transmitter Jitter (peak to peak)	J_{TX}p-p			0.07	UI	
Total Generated Transmitter Jitter (rms)	J_{TX}rms			0.007	UI	
Receiver						
Average Rx Sensitivity @ 2.67Gb/s	R_{SENS1}			-27	dBm	3
Average Rx Sensitivity @ OC-48	R_{SENS1}			-27	dBm	3
Average Rx Sensitivity @ 2X Fibre Channel	R_{SENS2}			-28	dBm	4
Average Rx Sensitivity @ Gigabit Ethernet	R_{SENS3}			-30	dBm	4
Average Rx Sensitivity @ OC-12	R_{SENS4}			-29	dBm	5
Average Rx Sensitivity @ OC-3	R_{SENS5}	-15		-30	dBm	5
Maximum Input Power	P_{MAX}	-9			dBm	
Optical Center Wavelength	λ_C	1270		1600	nm	
LOS De-Assert	LOS_D			-28	dBm	
LOS Assert	LOS_A	-41			dBm	
LOS Hysteresis		0.5			dB	

Notes:
1. Class 1 Laser Safety per FDA/CDRH and IEC-825-1 regulations.
2. Unfiltered
3. With worst-case extinction ratio. Measured with a PRBS 2^{31}-1 test pattern interspersed with 72 consecutive ones and 72 consecutive zeros.
4. With worst-case extinction ratio. Measured with a PRBS 2^{7}-1 test pattern.
5. With worst-case extinction ratio. Measured with a PRBS 2^{23}-1 test pattern.

V. General Specifications

Parameter	Symbol	Min	Typ	Max	Units	Ref.
Data Rate	BR	155		2667	Mb/sec	1
Bit Error Rate	BER			10^{-12}		2
Max. Supported Link Length on 9/125µm SMF @ OC-48	L_{MAX1}		45		km	3
Max. Supported Link Length on 9/125µm SMF @ 2X Fibre Channel	L_{MAX2}		47		km	3
Max. Supported Link Length on 9/125µm SMF @ Gigabit Ethernet	L_{MAX3}		50		km	3
Max. Supported Link Length on 9/125µm SMF @ OC-12	L_{MAX4}		50		km	3
Max. Supported Link Length on 9/125µm SMF @ OC-3	L_{MAX5}		50		km	3

Notes:
1. SONET OC-48 LR-1/SDH STM L-16.1, Gigabit Ethernet and 1x/2x Fibre Channel compliant. Compatible with SONET OC-12 and OC-3. Supports FEC at 2.67Gb/s.
2. Tested with a PRBS 2^{31}-1 test pattern interspersed with 72 consecutive ones and 72 consecutive zeros.
3. Attenuation of 0.55 dB/km is used for the link length calculations (per GR-253 CORE). Distances are indicative only. Please refer to the Optical Specifications in Table IV to calculate a more accurate link budget based on specific conditions in your application.

VI. Environmental Specifications

Finisar SFP transceivers have an extended operating temperature range from -10°C to +70°C case temperature.

Parameter	Symbol	Min	Typ	Max	Units	Ref.
Case Operating Temperature	T_{op}	-10		70	°C	
Storage Temperature	T_{sto}	-40		85	°C	

VII. Regulatory Compliance

Finisar transceivers are Class 1 Laser Products and comply with US FDA regulations. These products are certified by TÜV and CSA to meet the Class 1 eye safety requirements of EN (IEC) 60825 and the electrical safety requirements of EN (IEC) 60950. Copies of certificates are available at Finisar Corporation upon request.

VIII. Digital Diagnostic Functions

Finisar FTLF1721P1xCL SFP transceivers support the 2-wire serial communication protocol as defined in the SFP MSA[1]. It is very closely related to the E^2PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, Finisar SFP transceivers provide a unique enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

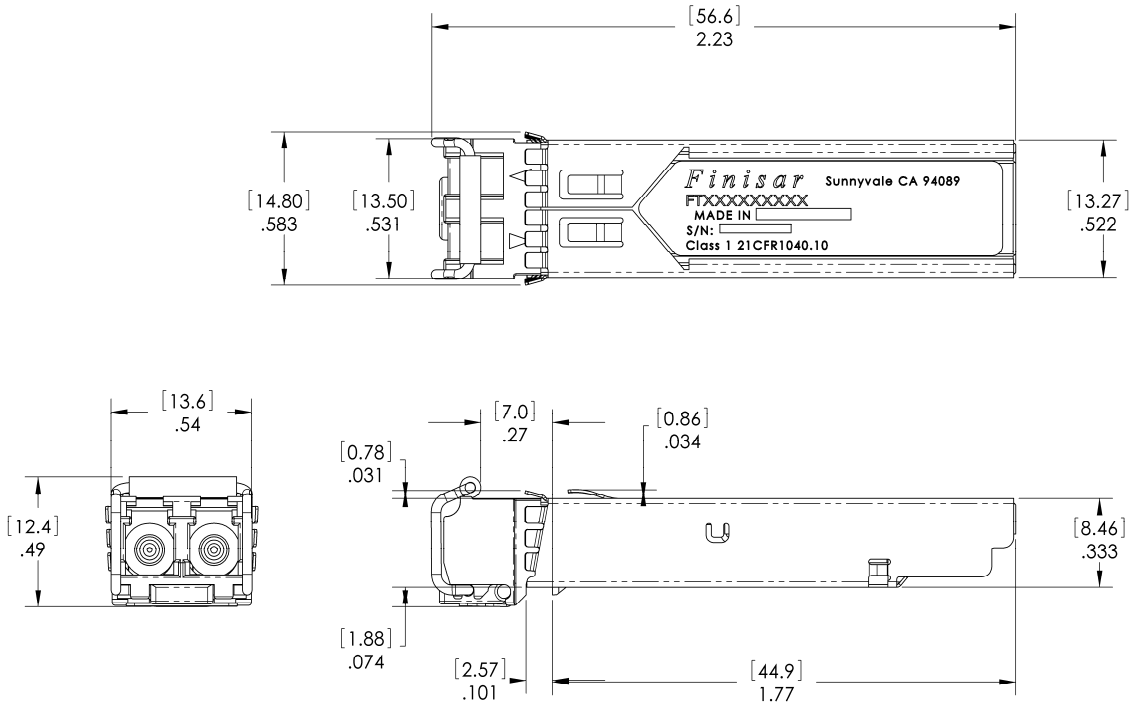
The SFP MSA defines a 256-byte memory map in E^2PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement. The complete interface is described in Finisar Application Note AN-2030: "Digital Diagnostics Monitoring Interface for SFP Optical Transceivers".

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E^2PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

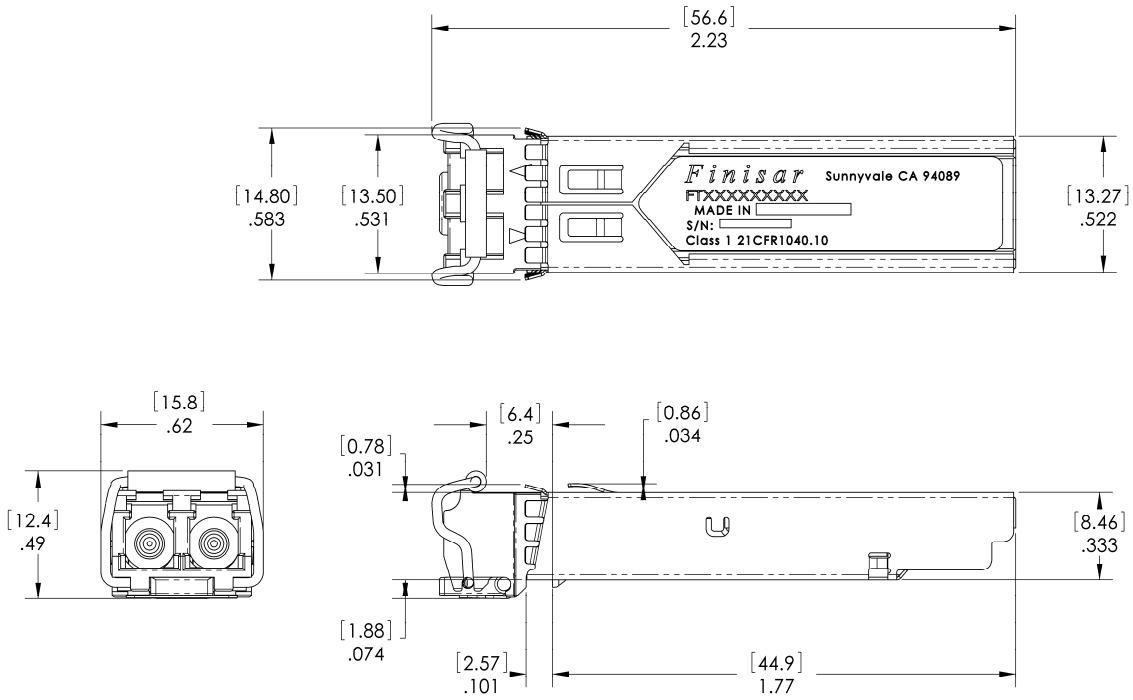
For more information, please see the SFP MSA documentation[1] or Finisar Application Note AN-2030.

IX. Mechanical Specifications

Finisar's Small Form Factor Pluggable (SFP) transceivers are compatible with the dimensions defined by the SFP Multi-Sourcing Agreement (MSA)[3].

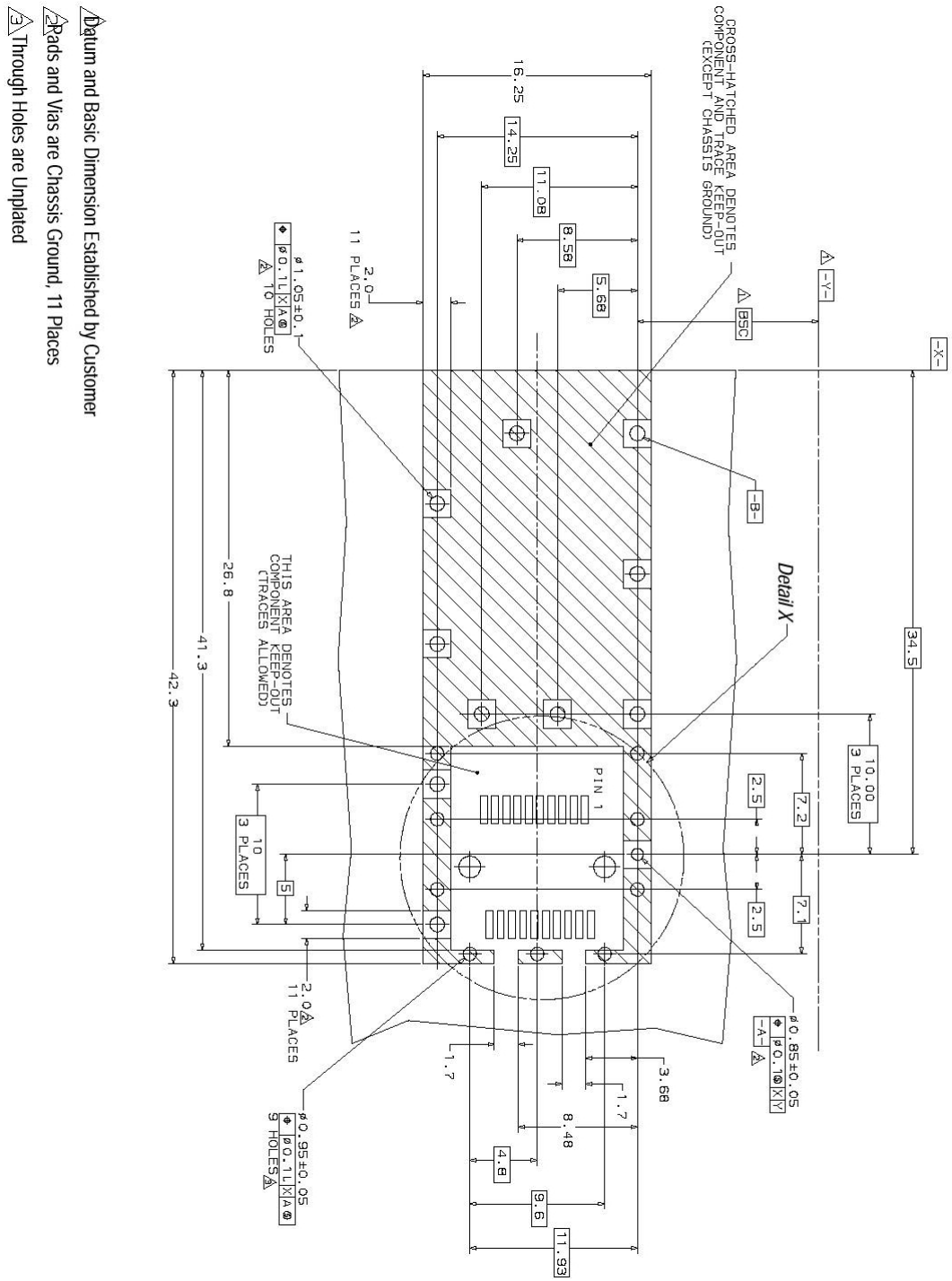


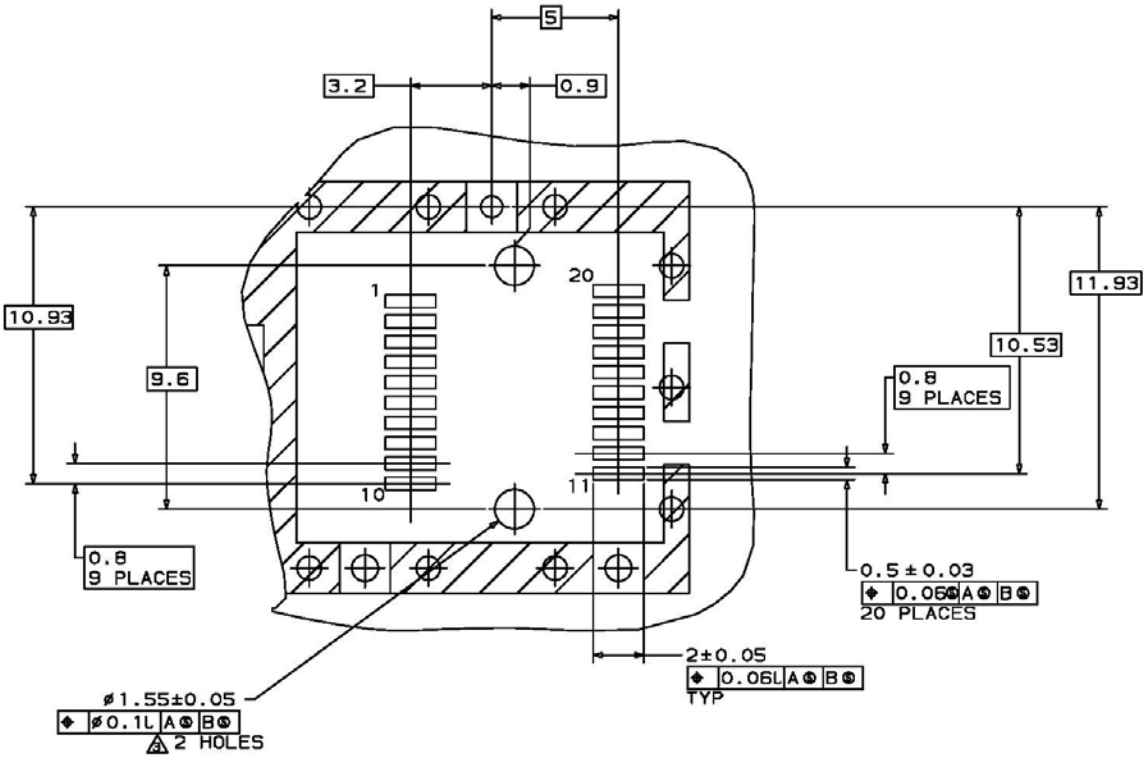
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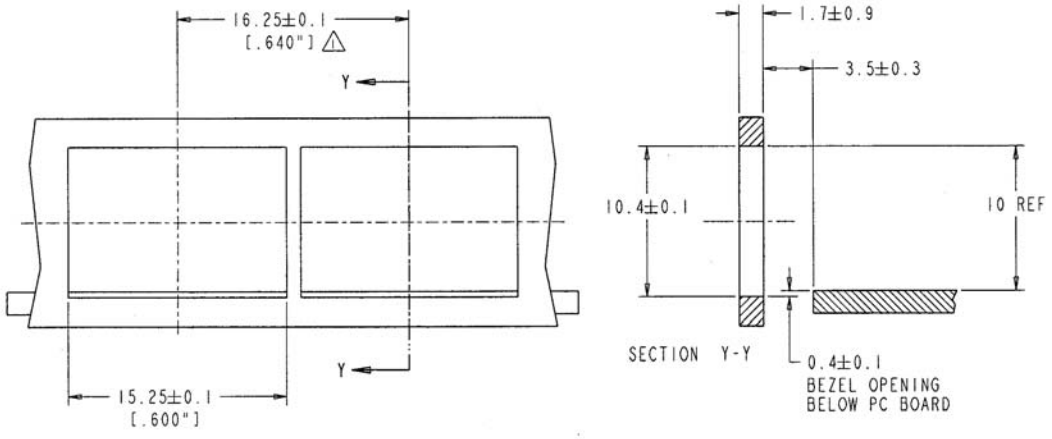


FTLF1721P1WCL

X. PCB Layout and Bezel Recommendations







NOTES:

1. MINIMUM PITCH ILLUSTRATED, ENGLISH DIMENSIONS ARE FOR REFERENCE ONLY
2. NOT RECOMMENDED FOR PCI EXPANSION CARD APPLICATIONS

XI. References

1. Small Form Factor Pluggable (SFP) Transceiver Multi-Source Agreement (MSA), September 2000. Documentation is currently available at Finisar upon request.

2. Bellcore GR-253 and ITU-T G.957 Specifications (Transmitter Optical Output Power complies with SONET OC-48 requirements only).

3. IEEE Std 802.3, 2002 Edition, Clause 38, PMD Type 1000BASE-LX. IEEE Standards Department, 2002. (*)

4. "Fibre Channel Draft Physical Interface Specification (FC-PI-2 Rev. 7.0)". American National Standard for Information Systems. (*)

5. Directive 2002/95/EC of the European Council Parliament and of the Council, "on the restriction of the use of certain hazardous substances in electrical and electronic equipment." January 27, 2003.

(*) Neither IEEE 802.3 nor FC-PI-2 Rev. 7.0 specifies a 1310nm DFB single mode interface. The FTLF1721P2xCL complies with these specifications except for transmitter wavelength, receiver sensitivity, and transmit output power. See Section IV for details.

XII. For More Information

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