

Model : HK2015

Open loop hall circuit, Sub-plate installation,
Detect DC, AC and pulse current.

Technical indicators:

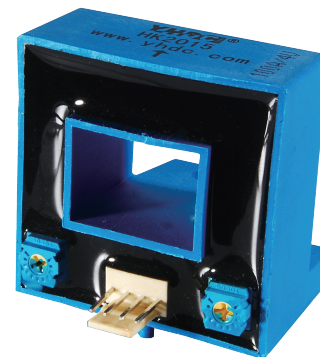
Flame resistance: UL94-V0

Working temperature: $-25^{\circ}\text{C} \sim +70^{\circ}\text{C}$

Storage temperature: $-40^{\circ}\text{C} \sim +95^{\circ}\text{C}$

Bandwidth: DC $\sim$ 50KHz

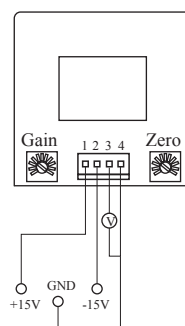
Dielectric strength: 3KV 50Hz 1min



Electrical parameters:

I_p	Rated input	± 50	A DC
I_{PM}	Input measured range	± 150	A DC
V_{OUT}	Rated output	± 4	V DC
X	Accuracy	1	%
ϵ_L	Linearity	< 0.1	%
V_C	Supply voltage($\pm 5\%$)	± 15	V
I_C	Current consumption	± 25	mA
R_L	Load impedance	> 3	K Ω
V_{OE}	Zero offset, $T_A = 25^{\circ}\text{C}$	$\leq \pm 20$	mV
T_R	Response time	< 3	μs
N.W	Weight	76	g

Connection Diagram:



Terminal definition

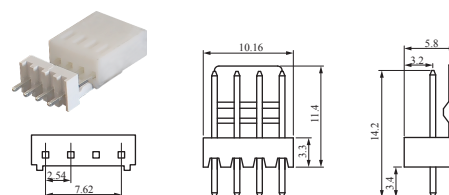
1: +15V

2: -15V

3: Vout

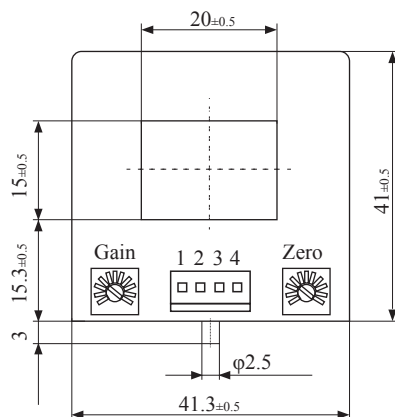
4: 0V

connector schematic diagram:

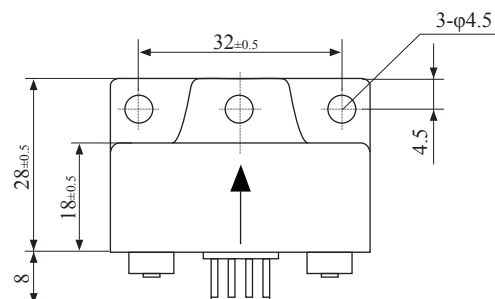


Cold-press terminal plug /socket, Distance 2.54, Stitch size 0.5 $\times$ 0.5

Dimensions (in mm) :



Front view



Top view