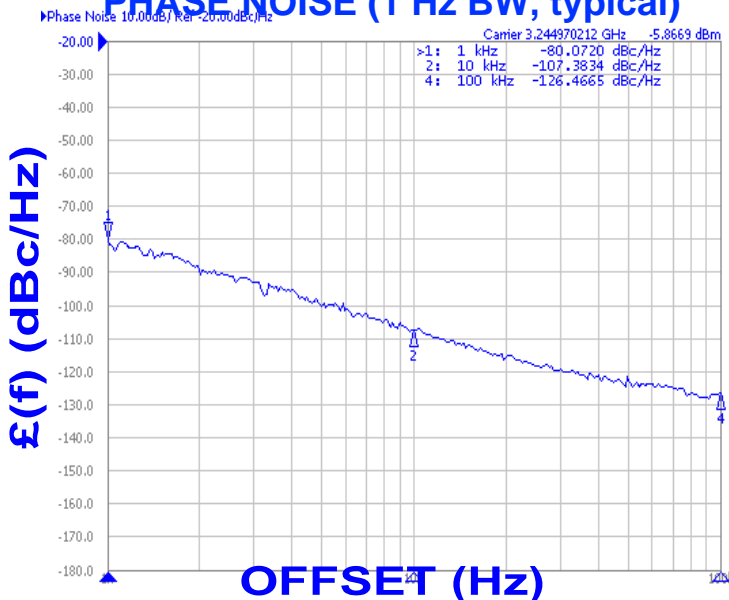




PHASE NOISE (1 Hz BW, typical)



FEATURES

- Frequency Range: 3330 - 3360 MHz
- Step Size: 125 KHz
- PLL-24H - Style Package

APPLICATIONS

- Telecommunications
- Satellite
- Telemetry

PERFORMANCE SPECIFICATIONS

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	3330 - 3360	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-105	dBc/Hz
Harmonic Suppression (2nd, typ.)	-12	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	0±3	dBm
Load Impedance	50	Ω
Step Size	125	kHz
Charge Pump Output Current	5000	μA
Switching Speed (typ., adjacent channel)	3	mSec
Startup Lock Time (typ.)	3	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	PLL-24H	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	30	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-202 : PLL Functional Description

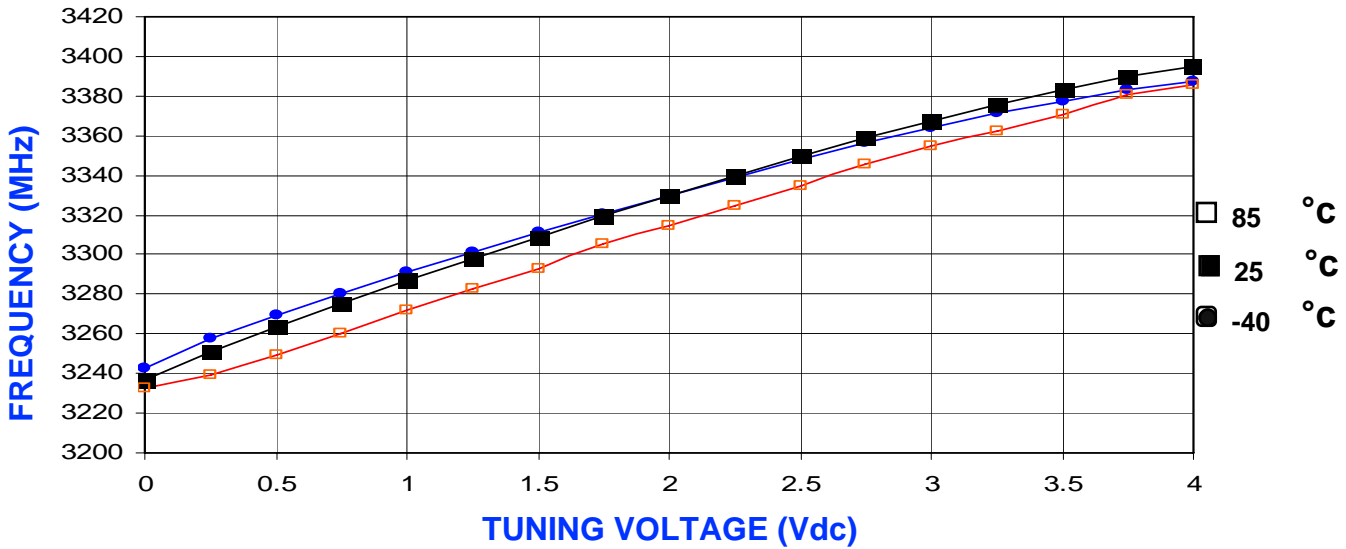
NOTES:

Reference Oscillator Signal: 5 MHz f_{osc} <math>< 100</math> MHz
 Frequency Synthesizer: Analog Devices - ADF4106

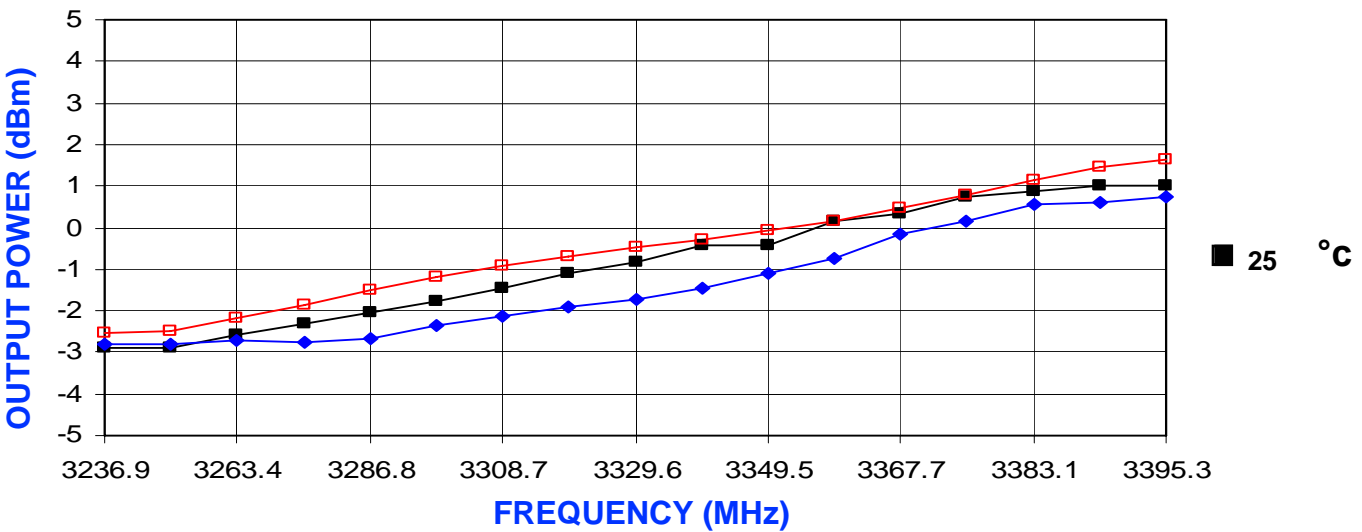
Prescaler: 32

PHASE LOCKED LOOP

VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

