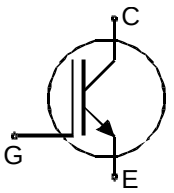
SIGC54T60R3

IGBT^3 Chip

FEATURES:
- 600V Trench & Field Stop technology
- low $V_{CE(sat)}$
- low turn-off losses
- short tail current
- positive temperature coefficient
- easy paralleling

This chip is used for:
- power module

Applications:
- drives



Chip Type	V_{CE}	I_{Cn}	Die Size	Package	Ordering Code
SIGC54T60R3	600V	100A	5.97 x 8.97 mm^2	sawn on foil	Q67050-A4341-A101

MECHANICAL PARAMETER:

Raster size	5.97 x 8.97	mm^2
Emitter pad size	(2.489 x 1.767) x 4 (2.789 x 1.995) x 4	
Gate pad size	1.615 x 0.817	
Area total / active	53.6 / 40	mm^2
Thickness	70	µm
Wafer size	150	mm
Flat position	90	deg
Max. possible chips per wafer	245 pcs	
Passivation frontside	Photoimide	
Emitter metallization	3200 nm AlSiCu	
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding	
Die bond	electrically conductive glue or solder	
Wire bond	Al, <500µm	
Reject ink dot size	∅ 0.65mm ; max 1.2mm	
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C	

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SIGC54T60R3

MAXIMUM RATINGS:

Parameter		Symbol	Value	Unit
Collector-emitter voltage, T_j= 25 °C		V_{CE}	600	V
DC collector current, limited by T_{jmax}		I_C	[1]	A
Pulsed collector current, t_p limited by T_{jmax}		I_{cpuls}	300	A
Gate emitter voltage		V_{GE}	±20	V
Operating junction and storage temperature		T_j, T_{stg}	-40 … +175	°C
SC data, V_{GE} = 15V, V_{CC} = 360V	Tvj = 150°C	*tp*	6	µs
	Tvj = 25°C		8	

[1] depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_j=25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-emitter breakdown voltage	$V_{(BR)CES}$	V_{GE}=0V , I_C= 4mA	600			V
Collector-emitter saturation voltage	$V_{CE(sat)}$	V_{GE}=15V, I_C=100A	1.05	1.45	1.85	
Gate-emitter threshold voltage	$V_{GE(th)}$	I_C=1600µA , V_{GE}=V_{CE}	5.0	5.8	6.5	
Zero gate voltage collector current	I_{CES}	V_{CE}=600V , V_{GE}=0V			5.1	µA
Gate-emitter leakage current	I_{GES}	V_{CE}=0V , V_{GE}=20V			600	nA
Integrated gate resistor	R_{Gint}			2		Ω

ELECTRICAL CHARACTERISTICS (verified by design/characterization):

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Input capacitance	C_{iss}	V_{CE}=25V, V_{GE}=0V, *f*=1MHz		6160		pF
Output capacitance	C_{oss}			384		
Reverse transfer capacitance	C_{rss}			183		

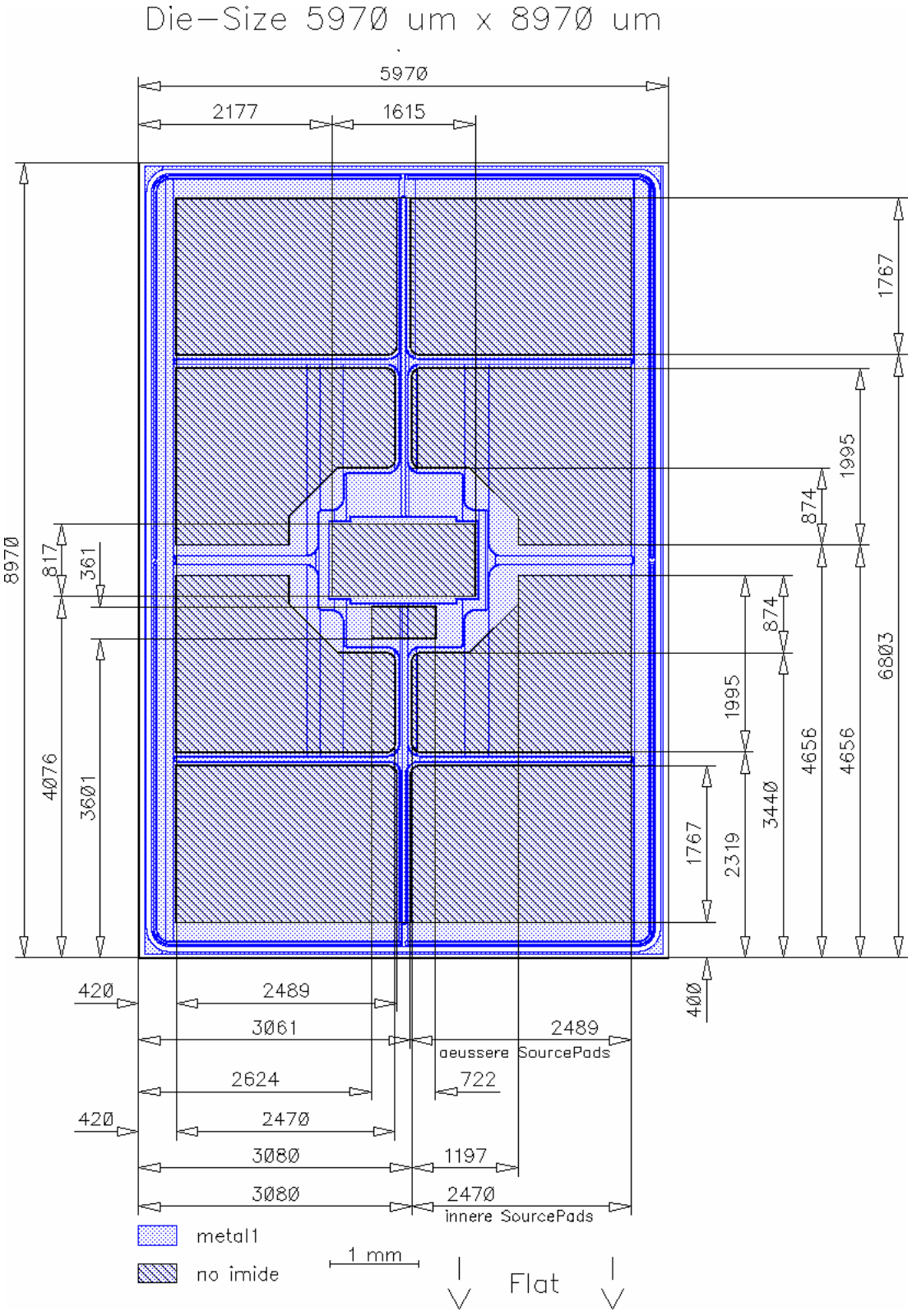
SWITCHING CHARACTERISTICS (verified by design/characterization), inductive load

Parameter	Symbol	Conditions	Value [2]			Unit
			min.	typ.	max.	
Turn-on delay time	$t_{d(on)}$	T_j=125°C, V_{CC}=300V, I_C=100A, V_{GE}=-15/15V, R_G= 3.3Ω		70		ns
Rise time	t_r			25		
Turn-off delay time	$t_{d(off)}$			260		
Fall time	t_f			60		

[2] values also influenced by parasitic L- and C- in measurement and package.

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CHIP DRAWING:



SIGC54T60R3

FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet		

DESCRIPTION:

AQL 0,65 for visual inspection according to failure catalog
Electrostatic Discharge Sensitive Device according to MIL-STD 883
Test-Normen Villach/Prüffeld

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