



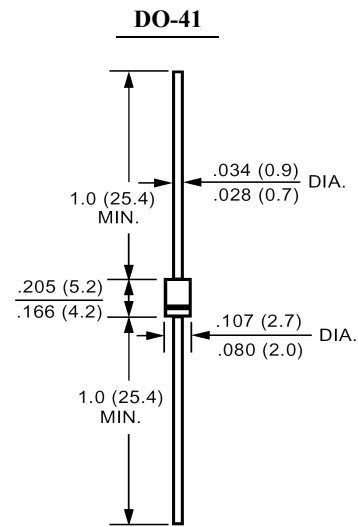
IN4001G THRU IN4007G SILICON RECTIFIER

FEATURES

- Glass Passivated chip junction
- Low forward voltage drop
- High current capability
- High capability
- High surge current capability
- Exceeds environmental standards of MIL-S-19500/228

MECHANICAL DATA

Case: Molded plastic, DO-41
 Epoxy: UL 94V-O rate flame retardant
 Lead: Axial leads, solderable per MIL-STD-202, method 208 guaranteed
 Polarity: Color band denotes cathode end
 Mounting position: Any
 Weight: 0.012ounce, 0.33gram



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.
 Single phase, half wave, 60Hz, resistive or inductive load.
 For capacitive load, derate current by 20%.

	Symbols	IN4001G	IN4002G	IN4003G	IN4004G	IN4005G	IN4006G	IN4007G	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current .375"(9.5mm) Lead Length at $T_A=75$	$I_{(AV)}$	1.0							Amp
Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method)	I_{FSM}	30							Amp
Maximum Forward Voltage at 1.0A DC and 25	V_F	1.1							Volts
Maximum Full Load Reverse Current Full Cycle Average at 75 °C Ambient		30							uAmp
Maximum Reverse Current at $T_A=25$ at Rated DC Blocking Voltage $T_A=100$	I_R	5.0 500							uAmp
Typical Junction Capacitance (Note 1)	C_J	15							pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$	50							/W
Operating Junction Temperature Range	T_J	-55 to +150							
Storage Temperature Range	T_{stg}	-55 to +150							

NOTES:

- 1- Measured at 1 MHz and applied reverse voltage of 4.0 VDC.
- 2- Thermal Resistance Junction to Ambient 0.375"(9.5mm) lead length P.C.B. Mounted.



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RATINGS AND CHARACTERISTIC CURVES

FIG.1- MAXIMUM FORWARD CURRENT DERATING CURVE

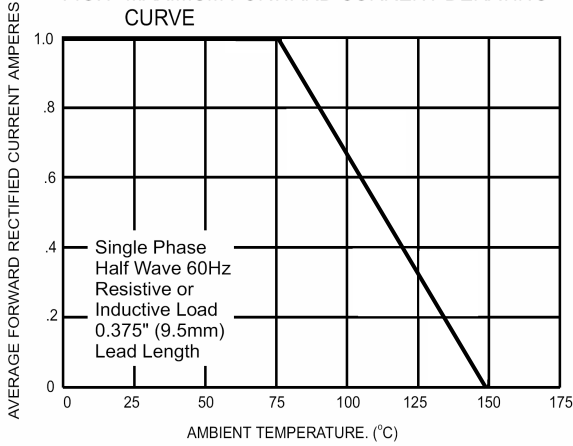


FIG.2- TYPICAL FORWARD CHARACTERISTICS

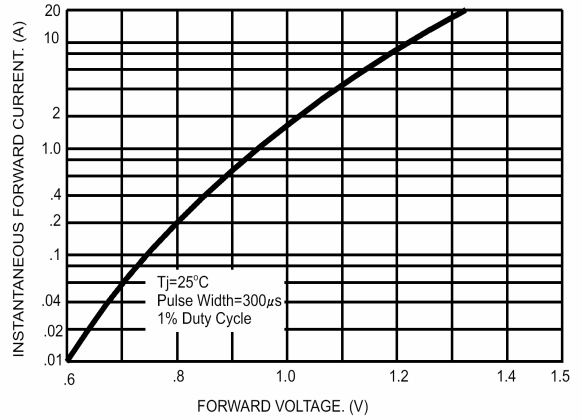


FIG.3- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

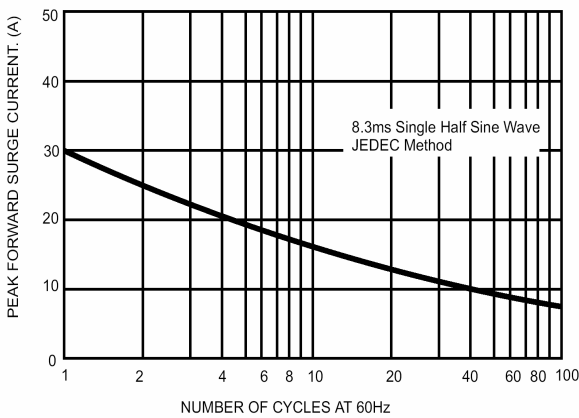


FIG.4- TYPICAL JUNCTION CAPACITANCE

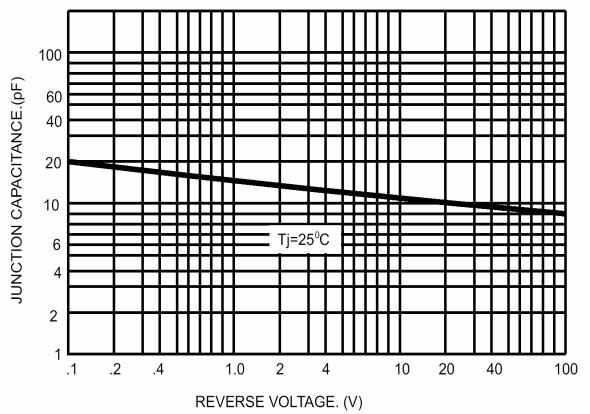


FIG.5- TYPICAL REVERSE CHARACTERISTICS

