

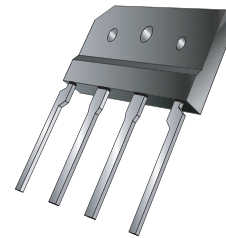
VOLTAGE RANGE: 100 - 1000V
CURRENT: 6 A

Features

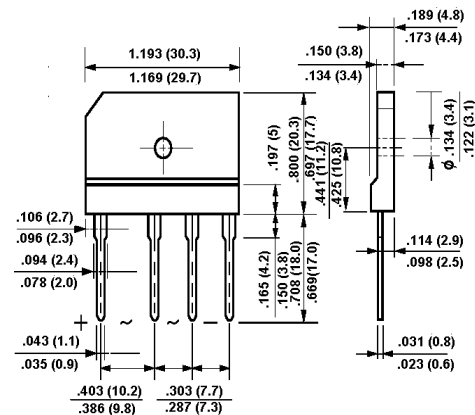
- Glass passivated chip junctions
- Plastic Package has Underwriters
- Laboratory Flammability Classification 94V-0
- High current capacity with small package
- Superior thermal conductivity

Mechanical Data

- Terminal: Plated leads solderable per MIL-STD 202E, method 208C
- Case: UL-94 Class V-O recognized flame retardant epoxy
- Polarity: Polarity symbol marked on body
- Mounting position: Any



D5-SB



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		Symbol	D5SB10	D5SB20	D5SB40	D5SB60	D5SB80	D5SB100	Unit
Maximum repetitive voltage		V _{RM}	100	200	400	600	800	1000	V
Maximum DC reverse current at @T _A =25°C		I _R	10						μA
rated DC blocking voltage @T _A =125°C			500						
Average rectified forward current 60Hz Sine wave @T _C =100°C		I _O	6 ⁽¹⁾						A
Resistance load @T _A =25°C			2.8 ⁽²⁾						
Peak Forward Surge Current 10ms single half sine-wave superimposed on rated load		I _{FSM}	150						A
Maximum Instantaneous Forward Voltage @ 3.0A		V _F	1.1						V
Dielectric strength terminals to case, AC 1 minute Current 1mA		V _{dia}	2.5						KV
Maximum thermal Resistance per leg	on P.C.B without heat-sink	R _{θJA}	22 ⁽²⁾						°C / W
	on Al plate heat-sink	R _{θJC}	3.4 ⁽¹⁾						
Operating and Storage Temperature Range		T _J , T _{STG}	150 , -40 ~ 150						°C
Mounting torque		Tor	Rating Torque: 0.8N.m						N.m

Notes: (1) Unit case mounted on Al plate heat-sink

(2) Unites mounted on P.C.B. without heat-sink

(3) Recommended mounting position is to bolt down on heatsink with silicone thermal compound for maximum heat transfer with #6 screw {heat-sink size : 10.5 * 8.2 * 0.3cm}

Fig.1 derating Curve

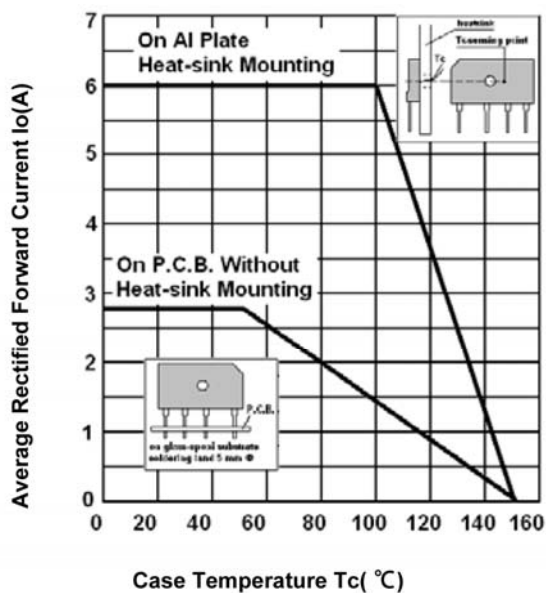


Fig.2 Typical Reverse Characteristics

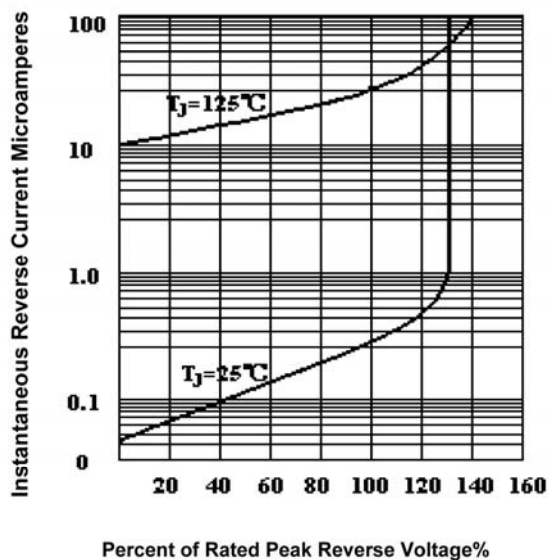


Fig.3 Peak Surge Forward capability

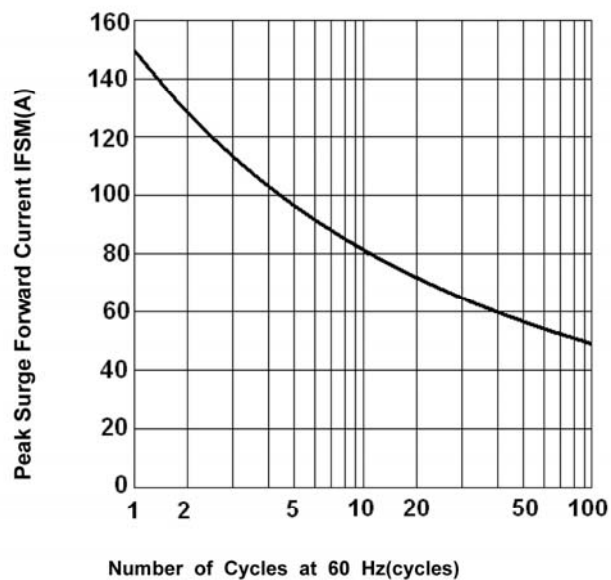


Fig.4 Forward Voltage

