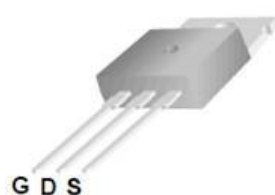


P5010AT

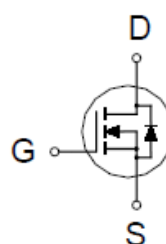
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	$50m\Omega$ @ $V_{GS} = 10V$	30A



TO-220



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	30	A
	$T_C = 100\text{ }^{\circ}\text{C}$		18	
Pulsed Drain Current ¹		I_{DM}	70	
Avalanche Current		I_{AS}	37	
Avalanche Energy	$L = 0.1mH$	E_{AS}	70	mJ
Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	P_D	96	W
	$T_C = 100\text{ }^{\circ}\text{C}$		38	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.3	$^{\circ}\text{C} / \text{W}$

¹Pulse width limited by maximum junction temperature.

P5010AT

N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

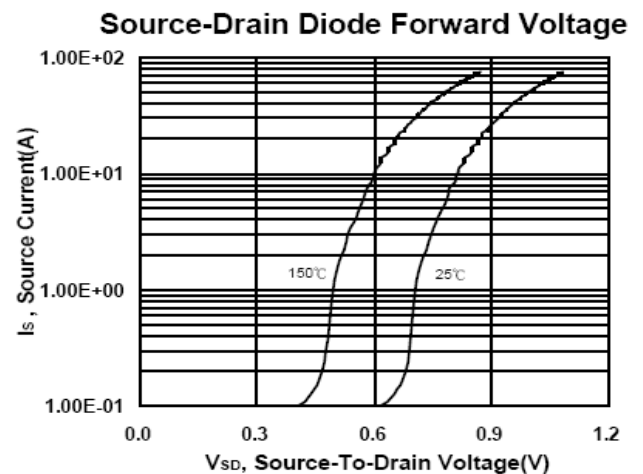
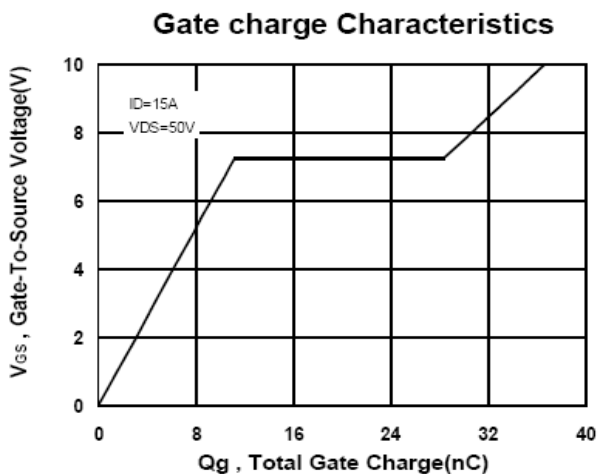
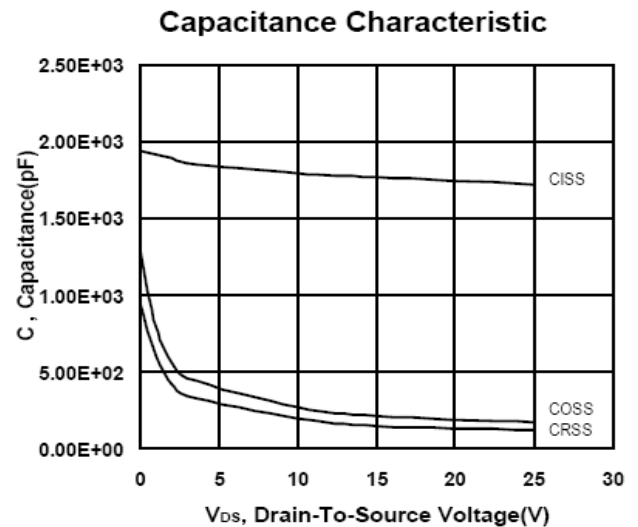
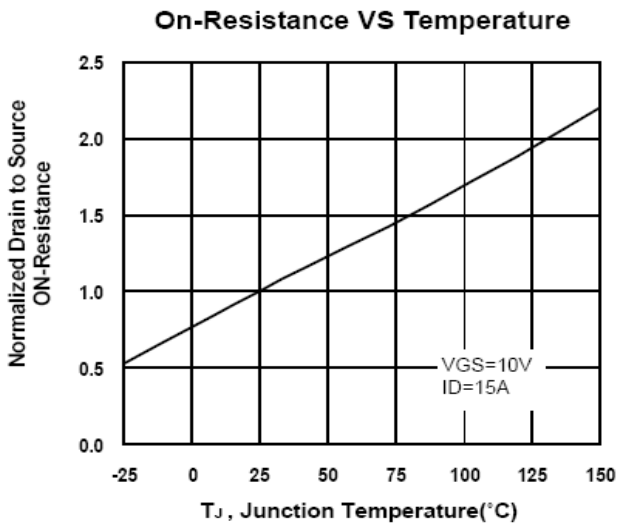
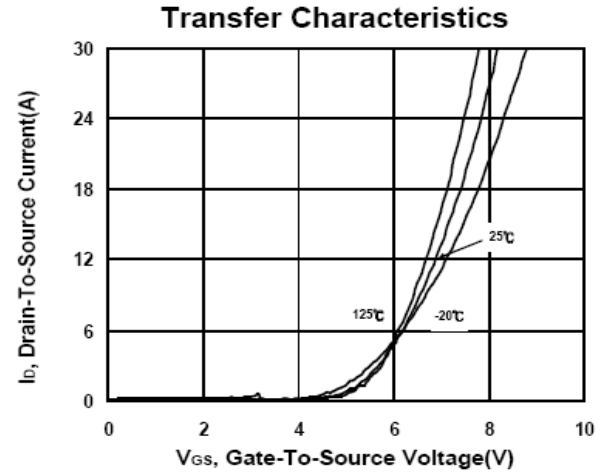
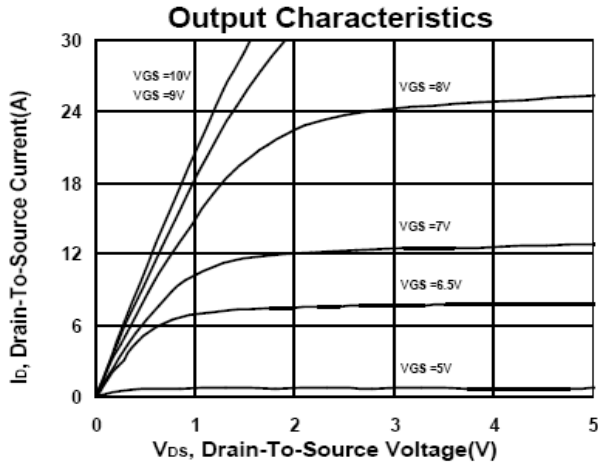
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	100			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2	3	4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 80V, V _{GS} = 0V			1	μA
		V _{DS} = 80V, V _{GS} = 0V , T _J = 125 °C			10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 10V, V _{GS} = 10V	70			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 10V, I _D = 15A		39	50	mΩ
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 15A		10		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		1723		pF
Output Capacitance	C _{oss}			173		
Reverse Transfer Capacitance	C _{rss}			120		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		2.2		Ω
Total Gate Charge ²	Q _g	V _{DS} = 50V, V _{GS} = 10V, I _D = 15A		38		nC
Gate-Source Charge ²	Q _{gs}			12		
Gate-Drain Charge ²	Q _{gd}			18		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 50V, I _D ≅ 15A, V _{GS} = 10V, R _{GEN} = 6Ω		15		nS
Rise Time ²	t _r			130		
Turn-Off Delay Time ²	t _{d(off)}			30		
Fall Time ²	t _f			90		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current	I _S				30	A
Forward Voltage ¹	V _{SD}	I _F = 15A, V _{GS} = 0V			1.2	V
Reverse Recovery Time	t _{rr}	I _F = 15A, dI _F /dt = 100A / μS		72		nS
Reverse Recovery Charge	Q _{rr}			206		nC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

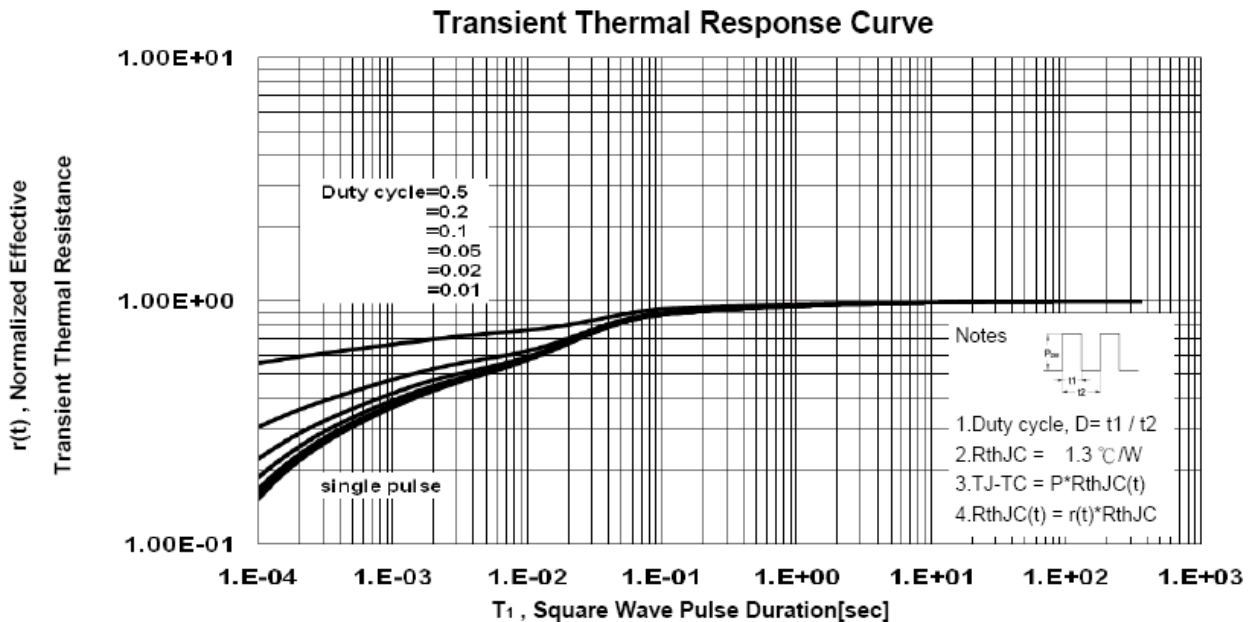
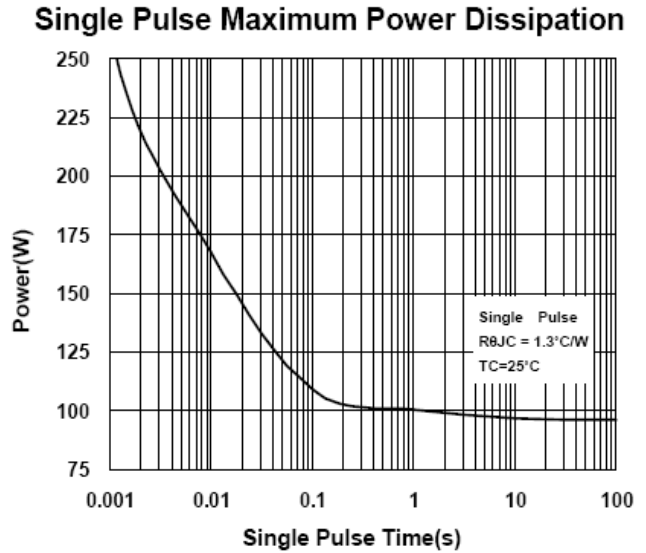
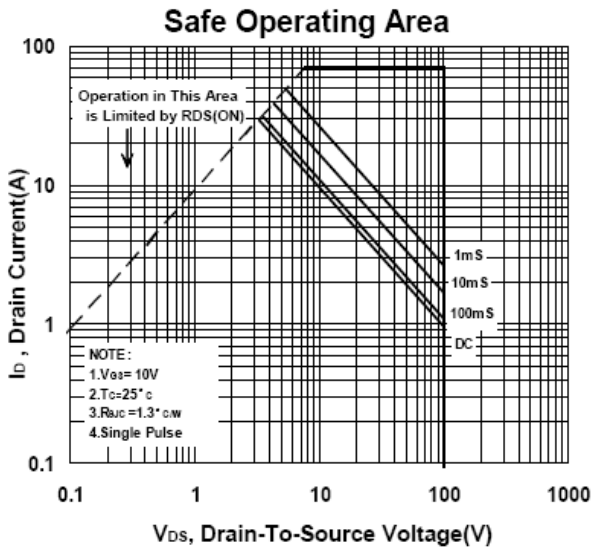
P5010AT

N-Channel Enhancement Mode MOSFET



P5010AT

N-Channel Enhancement Mode MOSFET



P5010AT

N-Channel Enhancement Mode MOSFET

Package Dimension

TO-220 (3-Lead) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	9.652	10.16	11.5	H	2.04	2.54	3.04
B	2.54	2.79	3.048	I	1.15	1.52	1.778
C	17.3		22.86	J	3.556	4.57	4.826
D	26.924	29.03	31.242	K	0.508	1.3	1.45
E	14.224	15.45	16.510	L	1.89	2.69	3.09
F	8.382	9.20	9.40	M	0.34	0.5	0.6
G	0.381	0.81	1.016	N			

