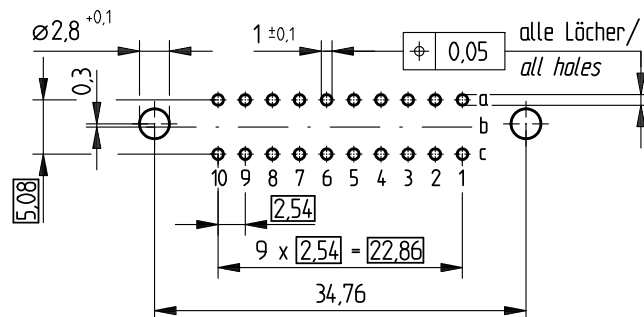


Leiterplatten-Lochbild / *PCB drillhole pattern*



Missing information and dimensions per IEC 60 603-2

Consider protection memo from DIN 34

Information:				Tolerances		 All Dimensions in mm		Scale		2:1		
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