Synchronous, Fixed Frequency Step-Up DC/DC Converter

FEATURES

- **Output Disconnected from Input During Shutdown**
- **Output Voltage Remains Regulated When $V_{IN} > V_{OUT}$**
- **Controlled Input Current During Start-Up**
- 300kHz Current Mode PWM Operation
- Can Be Externally Synchronized
- Internal 2A Switches
- Operates with V_{IN} as Low as 1.8V
- Automatic Burst Mode Operation at Light Loads
- Quiescent Current: 160µA
- Shutdown Current: 9µA Typ

APPLICATIONS

- Satellite Phones
- Portable Instruments
- Personal Digital Assistants
- Palmtop Computers

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Burst Mode is a trademark of Linear Technology Corporation.

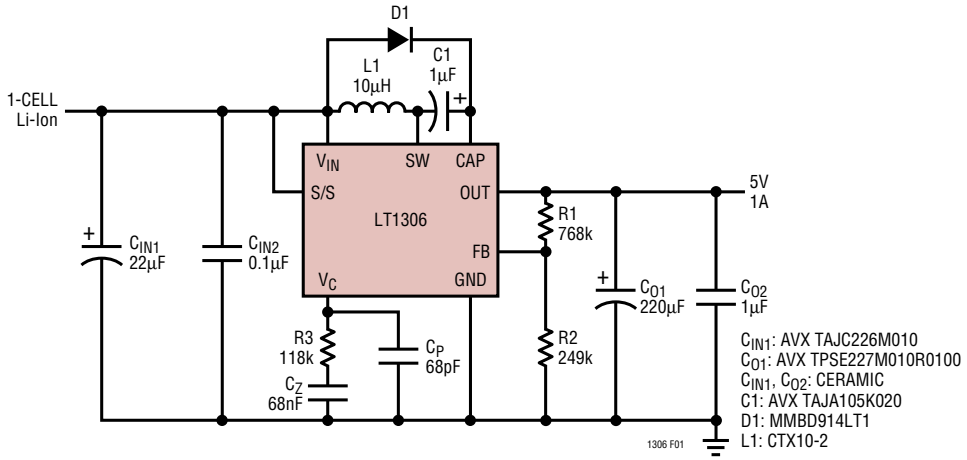
DESCRIPTION

The LT®1306 is a fully integrated, fixed frequency synchronous boost converter capable of generating 5V at 1A from a Li-Ion cell. The device contains both the main power switch and synchronous rectifier on chip and automatically disconnects the output from the input in shutdown, eliminating the need for external load disconnect circuitry. Additionally, the output remains regulated when V_{IN} exceeds V_{OUT}, allowing difficult step-up/step-down converter functions to be easily realized using a single inductor.

The internal 300kHz oscillator of the LT1306 can be easily synchronized to an external clock from 425kHz to 500kHz. This allows switching harmonics to be tightly controlled and eliminates any beat frequencies that may result from a multifrequency system. The LT1306 automatically shifts into power saving Burst Mode™ operation at light loads. At heavy loads the LT1306 operates in fixed frequency current mode. No-load quiescent current is 160µA and reduces to 9µA in shutdown mode.

The LT1306 is available in an SO-8 package.

TYPICAL APPLICATION



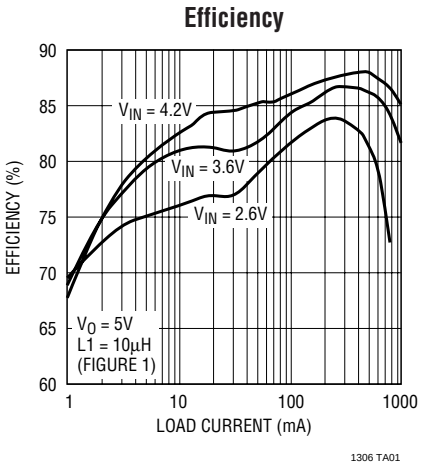


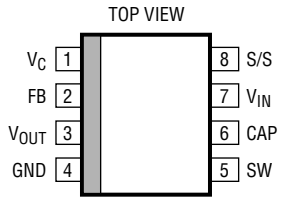
Figure 1. Single Li-Ion Cell to 5V Converter

ABSOLUTE MAXIMUM RATINGS

(Note 1)

V_{IN} Voltage 10V
S/S Voltage 7V
FB Voltage 10V
V_{OUT} Voltage 5.5V
Junction Temperature ..... 125°C
Operating Temperature Range (Note 2) .. –40°C to 85°C
Storage Temperature Range ..... –65°C to 150°C
Lead Temperature (Soldering, 10 sec) ..... 300°C

PACKAGE/ORDER INFORMATION



S8 PACKAGE
8-LEAD PLASTIC SO

T_{JMAX} = 125°C, θ_{JA} = 90°C/W

ORDER PART NUMBER
LT1306ES8
S8 PART MARKING
1306

Consult factory for Industrial and Military grade parts.

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_{IN} = 2.5V, $V_{S/S}$ = V_{IN}, V_C open unless otherwise noted.

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Reference Voltage	Measured at the FB Pin	●	1.22	1.24	1.26	V
Reference Line Regulation	1.8V ≤ V_{IN} ≤ 7V			0.002	0.1	%/V
FB Input Bias Current	V_{FB} = V_{REF}	●		10	25	nA
Error Amplifier Transconductance	ΔI = ±0.2μA		80	150	220	$\mu\Omega^{-1}$
Error Amplifier Output Source Current	V_{FB} = 1V, V_C = 0.8V		5	7.5	11	μA
Error Amplifier Output Sink Current	V_{FB} = 1.5V, V_C = 0.8V		5	7.5	11	μA
Error Amplifier Output Clamp Voltage	V_{FB} = 1V		1.18	1.28	1.38	V
V_{IN} Undervoltage Lockout Threshold			1.55		1.8	V
Idle Mode Output Leakage Current	V_{FB} = 1.5V, V_{OUT} = 5.5V, V_{SW} = 1.7V	●		6	15	μA
Output Source Current in Shutdown	V_{OUT} = 0V, V_{IN} = V_{SW} = 7V, V_{CAP} = 7.2V, $V_{S/S}$ = 0V	●			–3	μA
Switching Frequency	1.8V ≤ V_{IN} ≤ 7V, 0°C ≤ T_A ≤ 85°C 1.8V ≤ V_{IN} ≤ 7V, T_A = –40°C	●	260 225	310 305	415 390	kHz kHz
Maximum Duty Cycle	V_{FB} = 1V, 0°C ≤ T_A ≤ 85°C V_{FB} = 1V, T_A = –40°C		80 65	90 80		% %
Switch Current Limit	Duty Cycle = 0.1 (Note 3) Duty Cycle = 0.8 (Note 3)		2.3 2.0			A A
Burst Mode Operation Switch Current Limit				250		mA
Switch V_{CESAT}	I_{SW} = 2A			0.45	0.575	V
Rectifier V_{CESAT}	I_{SW} = 2A			0.49	0.675	V
Stepdown Mode Rectifier Voltage	V_{OUT} = 0V, I_{SW} = 1A V_{OUT} = 2.2V, I_{SW} = 1A		0.3 + V_{IN} 1.3		0.7 + V_{IN} 1.8	V V
Switch and Rectifier Leakage Current	V_{OUT} = 0V, V_{IN} = V_{SW} = 7V, V_{CAP} = 7.2V, $V_{S/S}$ = 0V	●		0.1	20	μA

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_{IN} = 2.5V, $V_{S/S}$ = V_{IN}, V_C open unless otherwise noted.

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
S/S Pin Current	$V_{S/S}$ = V_{IN} $V_{S/S}$ = 0V				6 –3	µA µA
Shutdown Pin Input High Voltage Shutdown Pin Input Low Voltage			1.2		 0.45	V V
Shutdown Delay			12	20	50	µs
Synchronization Frequency Range			425		500	kHz
Operating Supply Current				4.5	8	mA
Quiescent Supply Current	$V_{S/S}$ = V_{IN}, V_{FB} = 1.5V	●		160	250	µA
Shutdown Supply Current	$V_{S/S}$ = 0V			9	16	µA
CAP Pin Leakage Current	V_{IN} = V_{CAP} = 7V, $V_{S/S}$ = 2.5V, I_{SW} = 0	●			10	µA
Output Boost-to-Stepdown Threshold				V_{IN}		V
Output Stepdown-to-Boost Threshold				V_{IN} – 0.1		V

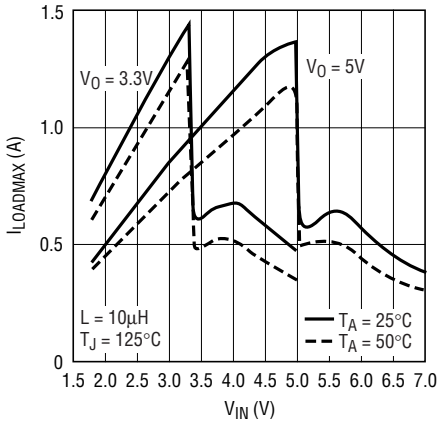
Note 1: Absolute Maximum Ratings are those values beyond which the life to the device may be impaired.

Note 2: The LT1306E is guaranteed to meet performance specifications from 0°C to 70°C. Specifications over the –40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

Note 3: Switch current limit guaranteed by design/correlation to static tests.

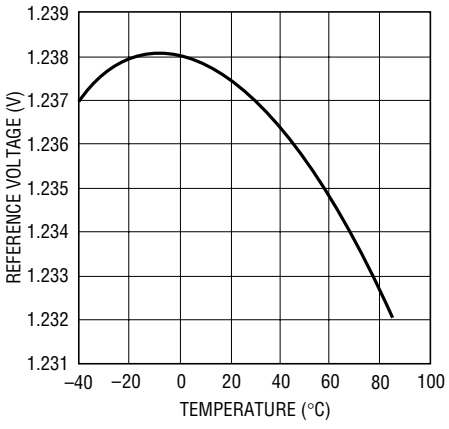
TYPICAL PERFORMANCE CHARACTERISTICS

Maximum Load Current vs Input Voltage



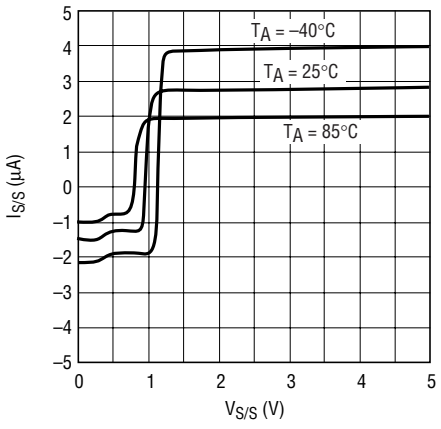
1306 • G01

Reference Voltage vs Temperature



1306 • G02

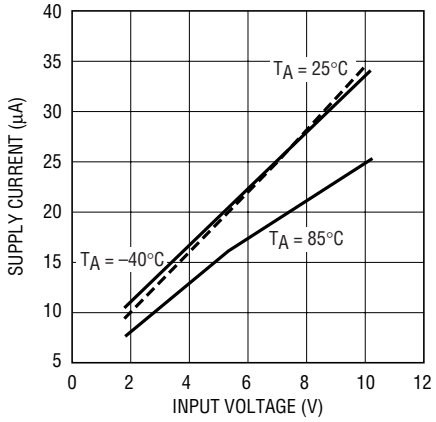
S/S Pin Current vs S/S Pin Voltage



1306 • G03

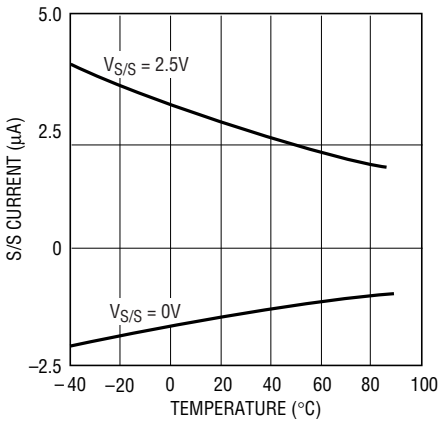
TYPICAL PERFORMANCE CHARACTERISTICS

Shutdown Supply Current vs Input Voltage



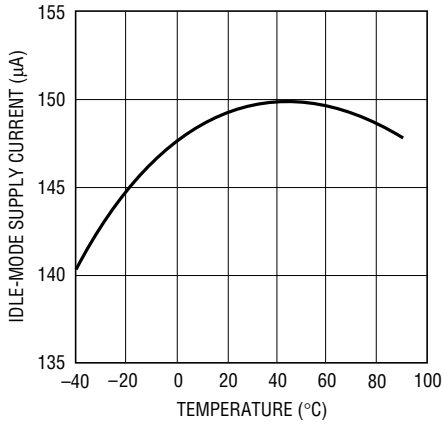
1306 • G04

S/S Pin Current vs Temperature



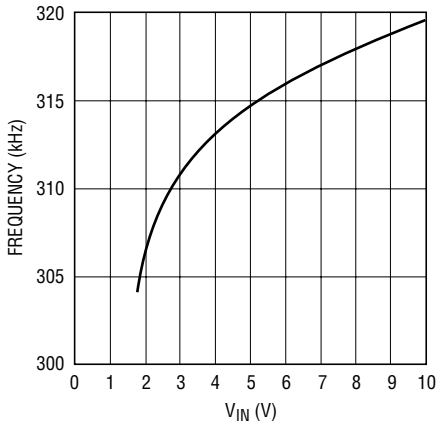
1306 • G05

Idle-Mode Supply Current vs Temperature



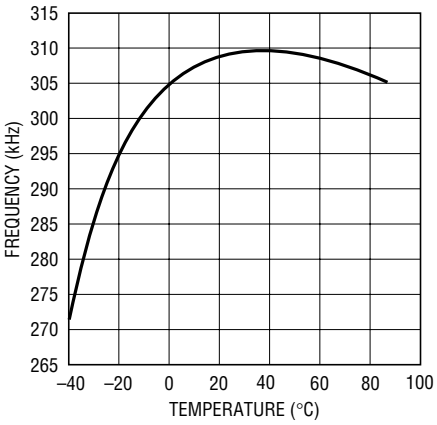
1306 • G06

Oscillator Frequency Line Regulation



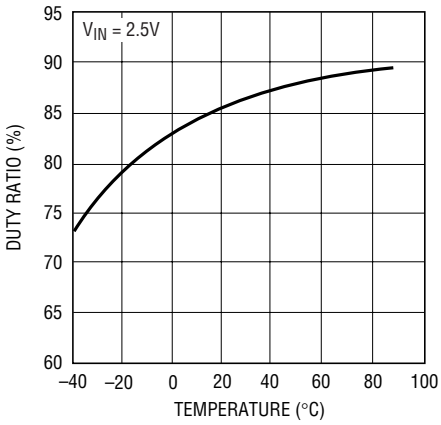
1306 • G07

Frequency vs Temperature



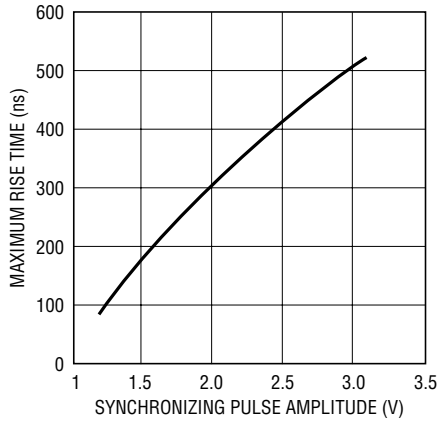
1306 • G08

Maximum Duty Ratio



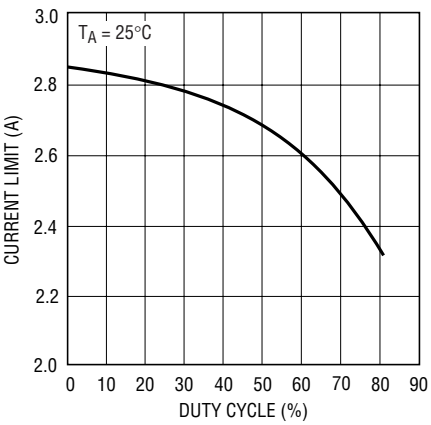
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Maximum Allowable Rise Time of Synchronizing Pulse



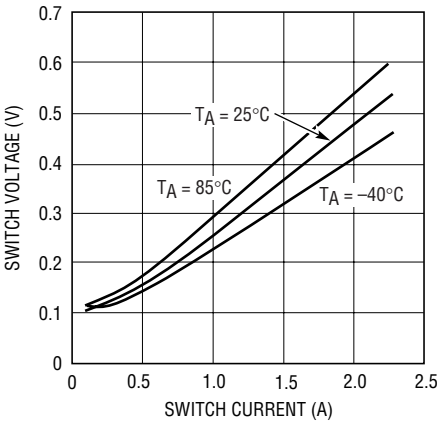
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Current Limit vs Duty Cycle



1306 • G11

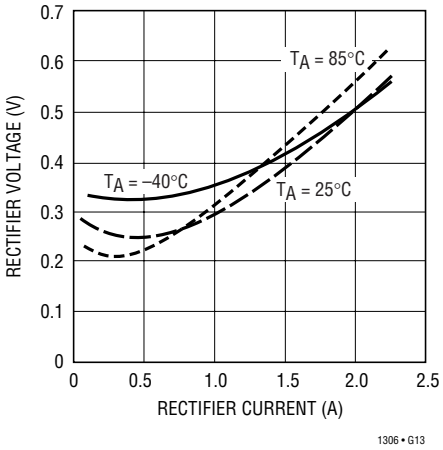
Switch Saturation Voltage vs Current



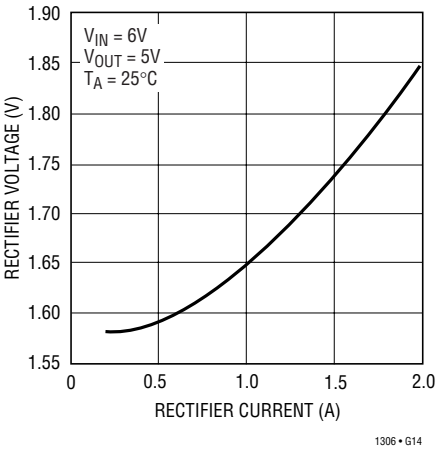
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TYPICAL PERFORMANCE CHARACTERISTICS

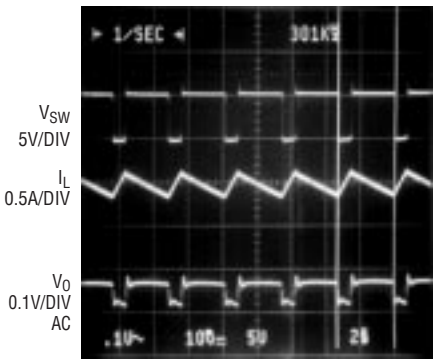
Rectifier Saturation Voltage vs Current



Stepdown-Mode Rectifier Voltage vs Current

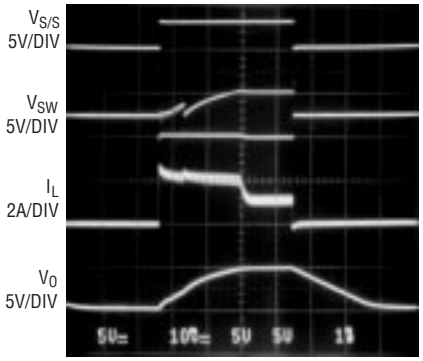


Continuous-Conduction Mode Switching Waveforms in Boost Operation



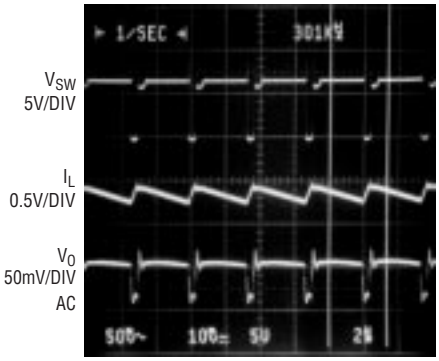
V_{IN} = 4.2V 2µs/DIV
V_O = 5V

Start-Up to Shutdown Transient Response*



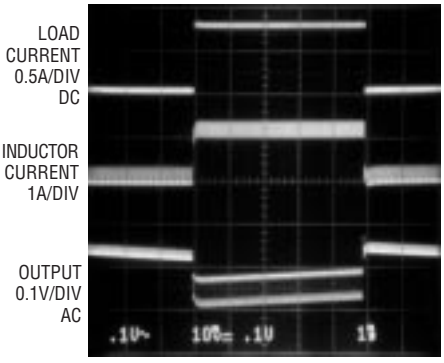
V_{IN} = 2.5V 1ms/DIV

Continuous-Conduction Mode Switching Waveforms in Stepdown Mode



V_{IN} = 6V 2µs/DIV
V_O = 5V

Transient Response of the Converter in Figure 1 with a 50mA to 800mA Load Step



V_{IN} = 3.6V 1ms/DIV
V_O = 5V

*Notice that the Input Start-Up Current is well Controlled and the Output Voltage Falls to Zero in Shutdown.

PIN FUNCTIONS

V_C (Pin 1): Compensation Pin for Error Amplifier. V_C is the output of the transconductance error amplifier. Loop frequency compensation is done by connecting an RC network from the V_C pin to ground.

FB (Pin 2): Inverting Input of the Error Amplifier. Connect the resistor divider tap here. Set output voltage according to V_{OUT} = 1.24V (1 + R1/R2).

V_{OUT} (Pin 3): Output of the Switching Regulator and Emitter of the Synchronous Rectifier. Connect appropriate output capacitor from here to ground. V_{OUT} must be kept below 5.5V.

GND (Pin 4): Ground. Connect to local ground plane.

SW (Pin 5): Switch Pin. The collectors of the grounded power switch and the synchronous rectifier. Keep the SW trace as short as possible to minimize EMI.

CAP (Pin 6): Power Supply to the Synchronous Rectifier Driver. The bootstrap capacitor and the blocking diode are tied to this pin. The CAP voltage switches between a low level of $V_{IN} - V_D$ to a high level determined by the V_{SW} high level.

V_{IN} (Pin 7): Supply or Battery Input Pin. Must be closely bypassed to ground plane.

S/S (Pin 8): Shutdown and Synchronization Pin. Shutdown is active low with a typical threshold of 0.9V. For normal operation, the S/S pin is tied to V_{IN}. To externally synchronize the switching regulator, drive the S/S pin with a pulse train.

BLOCK DIAGRAM

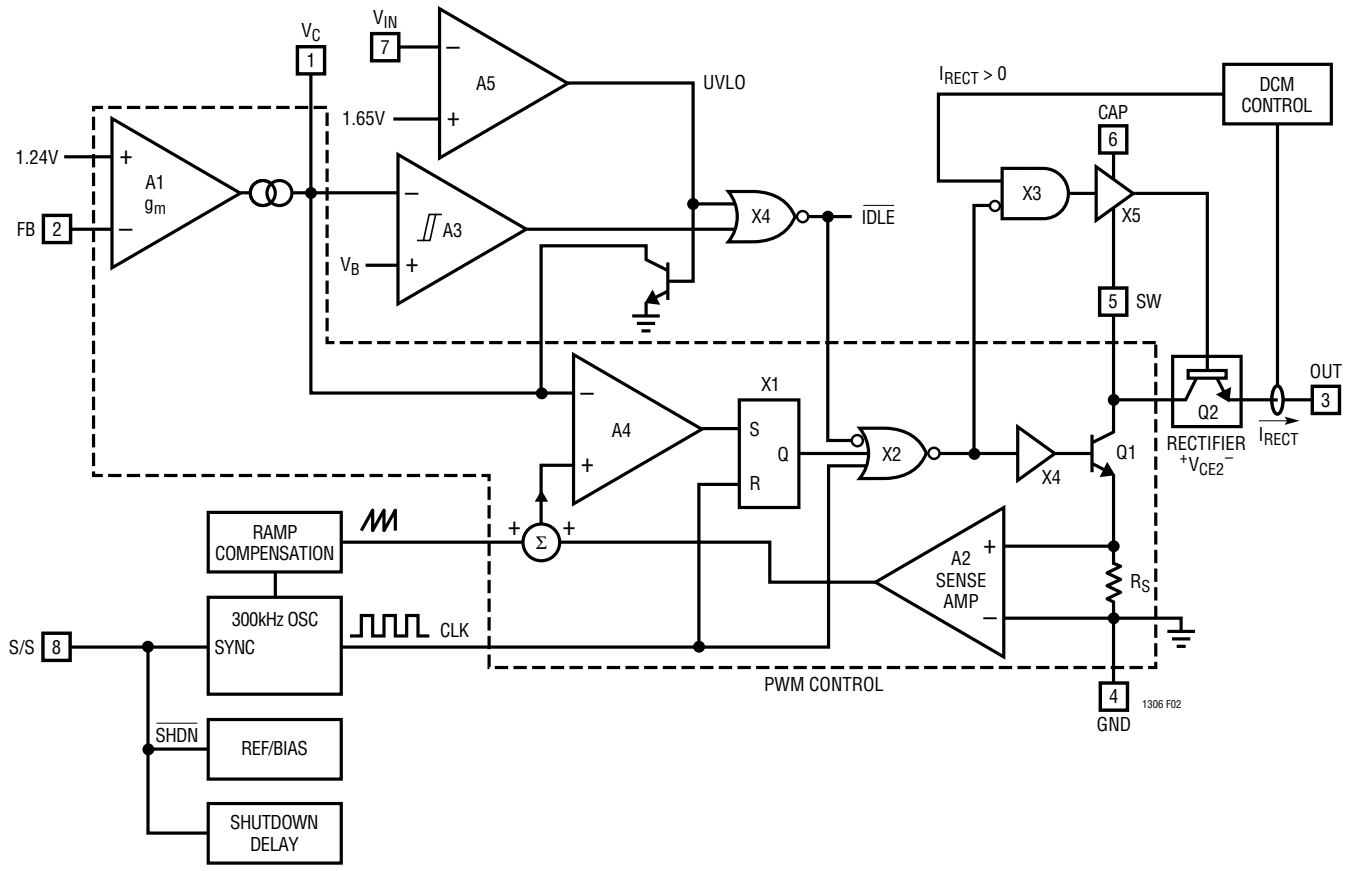


Figure 2. LT1306 Block Diagram

OPERATION

The LT1306 is a fixed frequency current mode PWM regulator with integrated power transistor Q1 and synchronous rectifier Q2.

In the Block Diagram, Figure 2, the PWM control circuit is enclosed within the dashed line. It consists of the current sense amplifier (A2), the oscillator, the compensating ramp generator, the PWM comparator (A4), the logic (X1 and X2), the power transistor driver (X4) and the main power switch (Q1). Notice that the clock (CLK) "blanks" Q1 conduction. The internal oscillator frequency is 300kHz.

The pulse width of the clock determines the maximum on duty ratio of Q1. In the LT1306 this is set to 88%. Q1 turns on at the trailing edge of the clock pulse. To prevent subharmonic oscillation above 50% duty ratio, a compensating ramp (generated from the oscillator sawtooth) is added to the sensed Q1 current. Q1 is turned off when this sum exceeds the error amplifier A1 output, V_C. Q1's absolute current limit is reached when V_C's upward excursion is clamped internally at 1.28V.

The error amplifier output, V_C, determines the peak switch current required to regulate the output voltage. V_C is a measure of the output power. At heavy loads, the average and the peak inductor currents are both high. V_C moves to the upper end of its operating range and the LT1306 operates in continuous conduction mode (CCM).

As load decreases, the average inductor current decreases. In CCM, the peak-to-peak inductor current ripple to the first order depends only on the inductance, the input and the output voltages. When the average inductor current falls below 1/2 of the peak-to-peak inductor current ripple, the converter enters discontinuous conduction mode (DCM). The switching frequency remains constant except that the inductor current always returns to zero within each switching cycle.

In both CCM and DCM, the output voltage is regulated with negative feedback. A1 amplifies the error voltage between the internally generated 1.24V reference and the attenuated output voltage. The RC network from the V_C pin to ground provides the loop compensation.

Further reduction in the load moves V_C towards the lower end of its operating range. Both the peak inductor current and switch Q1's on-time decrease. Hysteretic comparator A3 determines if V_C is too low for the LT1306 to operate efficiently. As V_C falls below the trip voltage V_B, the output of A3 goes high. All circuits except the error amplifier, comparators A3 and A5, and the rectifier driver control X5, are turned off. After the remaining energy stored in the inductor is delivered to the output through the synchronous rectifier Q2, the LT1306 stops switching. In this idle state, the LT1306 draws only 160μA from the input. With switching stopped and the load being powered by the output filter capacitor, the output voltage decreases. V_C then starts to increase. Q1 does not start to switch until V_C rises above the upper trip point of A3. The LT1306 again delivers power to the output as a current mode PWM converter except that the switch current limit is only about 250mA due to the low value of V_C. If the load is still light, the output voltage will rise and V_C will fall, causing the converter to idle again. Power delivery therefore occurs in bursts. The on-off cycle frequency, or burst frequency, depends on the operating conditions, the inductance and the output filter capacitance. The output voltage ripple in Burst Mode operation is usually higher than either CCM or DCM operation. Burst Mode operation increases light load efficiency because it delivers more energy to the output during each clock cycle than is possible with DCM operation's extremely low peak switch current. This allows fewer switching cycles per unit time to maintain a given output. Chip supply current therefore becomes a small fraction of the total input current.

The synchronous rectifier is represented as NPN transistor, Q2, in the Block Diagram (Figure 2). A rectifier drive circuit, X5, supplies variable base drive to Q2 and controls the voltage across the rectifier. The supply voltage, V_{CAP}, for the driver is generated locally with the bootstrap circuit, D1 and C1 (Figure 1). When Q1 is on, the bootstrap capacitor C1 is charged from the input to the voltage $V_{IN} - V_{D1(ON)} - V_{CESAT1}$. The charging current flows from the input through D1, C1 and Q1 to ground. After Q1 is switched off, the node SW goes above V_O by the rectifier drop V_{CESAT2}. D1 becomes back-biased and the CAP voltage is pushed up to $V_O + V_{CESAT2} + V_{IN} - V_{D1(ON)} - V_{CESAT1}$. C1 supplies the base drive to Q2. The consumed charge is replenished during the Q1 on interval.

OPERATION

In boost operation, X5 drives the rectifier Q2 into saturation. The voltage across the rectifier is V_{CESAT}. As the inductor current decreases, Q2's base drive also decreases. X5 ceases supplying base current to Q2 when the inductor current falls to zero.

If $V_{IN} > V_O$, Q2 will no longer be driven into saturation. Instead the voltage across Q2 is allowed to increase so that the inductor voltage reverses polarity as Q1 switches. Since the inductor voltage is bipolar, volt-second balance can be maintained regardless of the input voltage. The LT1306 is therefore capable of operating as a step-down regulator with the basic boost topology. Input start-up current is also well controlled since the inductor current cannot increase during Q1's off-time with negative inductor voltage.

The rectifier voltage drop depends on both the input and the output voltages. Efficiency in the step-down mode is less than that of a linear regulator. For sustained step-down operation, the maximum output current will be limited by the package thermal characteristics.

A hysteretic comparator in driver X5 controls the mode of operation. DC transfer characteristics of the comparator are shown in Figure 3 and Figure 4.

A logic low at the S/S pin (Pin 8) initiates shutdown. First, all circuit blocks in the LT1306 are switched off. The synchronous rectifier Q2 and its driver are kept on to allow stored inductive energy to flow to the output. As V_O drops below V_{IN}, the voltage across the rectifier Q2 increases so that the inductor voltage reverses. Inductor current continues to fall to zero. Driver X5 then turns off and the rectifier, Q2, becomes an open circuit. The LT1306 dissipates only 9μA in shutdown.

The LT1306 is guaranteed to start with a minimum V_{IN} of 1.8V. Comparator A5 senses the input voltage and generates an undervoltage lockout (UVLO) signal if V_{IN} falls below this minimum. In UVLO, V_C is pulled low and Q1 stops switching. The LT1306 draws 160μA from the input.

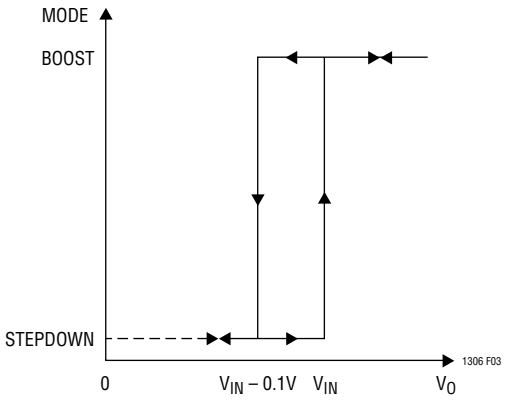


Figure 3. DC Transfer Characteristics of the Mode Control Comparator Plotted with V_O as an Independent Variable. V_{IN} is Considered Fixed.

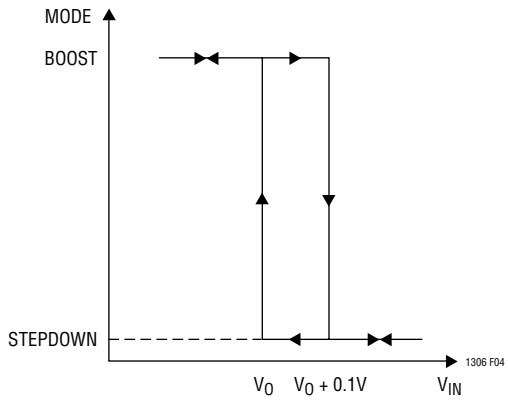


Figure 4. DC Transfer Characteristics of the Mode Control Comparator Plotted with V_{IN} as an Independent Variable. V_O is Considered Fixed.

APPLICATIONS INFORMATION

Output Voltage Setting

The output voltage of the LT1306 is set with a resistive divider, R1 and R2 (Figure 1 and Figure 5), from the output to ground. The divider tap is tied to the FB pin. Current through R2 should be significantly higher than the FB pin input bias current (≤25nA). With R2 = 249k, the input bias current of the error amplifier is 0.5% of the current in R1.

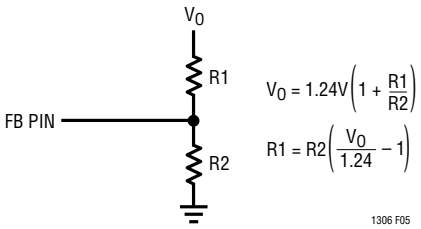


Figure 5. Feedback Resistive Divider

Synchronization and Shutdown

The S/S pin (Pin 8) can be used to synchronize the oscillator or disconnect the load from the input. The S/S pin is tied to the input (V_{IN} > 1.8V) for normal operation. The oscillator in the LT1306 can be externally synchronized by driving the S/S pin with a pulse train (See the graph "Maximum Allowable Rise Time of Synchronizing Pulse" in the Typical Performance Characteristics). The synchronization is positive edge triggered. The recommended frequency of the external clock ranges from 425kHz to 500kHz. If synchronization results in switching jitter, reducing the rising edge dv/dt of the external clock pulse usually cures the problem.

Shutdown will be activated if the S/S pin voltage stays below the shutdown threshold (0.45V) for more than 50µs. This shutdown delay is reset whenever the S/S pin goes above the shutdown threshold.

Inductor

The value of the energy storage inductor L1 (Figure 1) is usually selected so that the peak-to-peak ripple current is less than 40% of the average inductor current. For 1- or 2-cell alkaline or single Li-Ion to 5V applications, 10µH to 20µH is recommended for the LT1306 running at 300kHz. A 5µH to 10µH inductor can be used if the LT1306 is externally synchronized at 500kHz.

The inductor should be able to handle the full load peak inductor current without saturation. The peak inductor current can be as high as 2A. This places a lower limit on the core size of the inductor. Powder iron cores have unacceptable core losses and are not suitable for high efficiency applications. Most ferrite core materials have manageable core losses and are recommended. Inductor DC winding resistance (DCR) also needs to be considered for efficiency. Usually there are trade-offs between core loss, DCR, saturation current, cost and size.

For EMI sensitive applications, one may want to use magnetically shielded or toroidal inductors to contain field radiation. Table 1 lists a number of inductors suitable for LT1306 applications.

Table 1. Inductors Suitable for Use with the LT1306

VENDOR	PART NO.	VALUE (µH)	MAX DCR (Ω)	CORE TYPE	HEIGHT (mm)
BH Electronics	511-0033	5.0	0.023	Toroid	4.8
Coilcraft	DO3308-103	10	0.09	Open	3.0
	DO3316-472	4.7	0.018	Open	5.2
	DO3316-103	10	0.029	Open	5.2
	DO3316-153	15	0.046	Open	5.2
Coiltronics	CTX5-2	5	0.021	Toroid	6.0
	CTX10-2	10	0.032	Toroid	6.0
Murata	LQN6C4R7	4.7	0.034	Open	5.0
Sumida	CDRH73-100	10	0.072	Magnetic Shielding	3.4
	CD43-4R7	4.7	0.109	Open	3.2

Capacitors

The output filter capacitor is usually chosen based on its equivalent series resistance (ESR) and the acceptable change in output voltage as a result of load transients. The output voltage ripple at the switching frequency can be estimated by considering the peak inductor current and the capacitor ESR.

$$I_{PEAK} \approx I_{IN} \approx \frac{(I_O)(V_O)}{V_{IN}}$$

$$\text{output ripple} \cong (ESR)(I_{PEAK}) = \frac{(ESR)(I_O)(V_O)}{V_{IN}}$$

APPLICATIONS INFORMATION

Since a boost converter produces high output current ripple, one also needs to consider the maximum ripple current rating of the output capacitor. Capacitor reliability will be affected if the ripple current exceeds the maximum allowable ratings. This maximum rating is usually specified as the RMS ripple current. In the LT1306 the RMS output capacitor ripple current is:

$$I_O \sqrt{\frac{V_O - V_{IN}}{V_{IN}}}$$

For 2-cell to 5V applications, 220µF low ESR solid tantalum capacitors (AVX TPS series or Sprague 593D series) work well. To reduce output voltage ripple due to heavy load transients or Burst Mode operation, higher capacitance may be used. For through-hole applications, Sanyo OS-CON capacitors are also good choices.

In a boost regulator, the input capacitor ripple current is much lower. Maximum ripple current rating and input voltage ripples are not usually of concern. A 22µF tantalum capacitor soldered near the input pin is generally an adequate bypass.

Bootstrap Supply

Diode D1 and capacitor C1 generate a pulsating supply voltage, V_{CAP}, which is higher than the output. The rectifier drive circuit runs off this supply. During rectifier on-time, the rectifier base current drains C1. Q2 base current and the maximum allowable V_{CAP} ripple voltage determine the size of C1. A 1µF capacitor is sufficient to keep V_{CAP} ripple below 0.3V. For a 2-cell input (V_{IN} > 1.8V) over an extended temperature range, a BAT54 Schottky diode may be used for D1. The use of a Schottky diode increases the bootstrap voltage and the operating headroom for the rectifier driver, X5. Diodes like a 1N4148 or 1N914 work well for 2-cell inputs over the 0°C to 70°C commercial temperature range.

The charge drawn from C1 during the rectifier on-time has to be replenished during the switch on-interval. As duty cycle decreases, the amplitude of the C1 charging current can increase dramatically especially when delivering high power to the load. This charging current flows through the switch and can cause the current limit comparator to trip erratically. For boost applications where V_{IN} is a few tenths of a volt below V_O, a 1µF or 2.2µF tantalum capacitor (such as AVX TAJ series) can be used for C1. The ESR of the tantalum capacitor limits the charging current. A low value resistor (2Ω to 5Ω) can also be added in series with C1 for further limiting the charging current although this tends to lower the converter efficiency slightly.

Frequency Compensation

Current mode switching regulators have two feedback loops. The inner current feedback loop controls the inductor current in response to the outer loop. The outer or overall feedback loop tightly regulates the output voltage. The high frequency gain asymptote of the inner current loop rolls off at –20dB/decade and crosses the unity gain axis at a frequency ω_C between 1/6 to 2/3 of the switching frequency. The current loop is stable and is wideband compared to the overall voltage feedback loop. The low frequency current loop gain is not high (usually between unity and 10) but it increases the low frequency impedance of the inductor as seen by the output filter capacitor. (In a boost regulator, the inductor is connected to the output during the switch off-time.) Current mode control introduces an effective series resistance (>>DCR) to the inductor that damps the LC tank response. The complex high-Q poles of the LC filter are now separated, resulting in a dominant pole determined by the filter capacitance and the load resistance and a second high frequency pole.

For a boost regulator the control to output transfer function can be shown to have a dominant pole at the load corner frequency

$$\omega_P = \frac{1}{\left(\frac{R_L}{2}\right)(C_O)}$$

and a moving right-half plane (RHP) zero with a minimum value of

$$\omega_Z = \frac{R_L(1-D_{MAX})^2}{L}$$

APPLICATIONS INFORMATION

where

$$R_L = \text{Maximum Load} = \frac{\text{Output Voltage}}{\text{Maximum DC Load Current}}$$

$$D_{MAX} = \text{Maximum Converter Duty Cycle}$$

$$= \frac{V_O - V_{IN(MIN)} + 0.5}{V_O + 0.1}$$

There is also a second pole at the current loop crossover frequency ω_C (Figure 6). ω_Z is much lower in frequency than ω_C. The loop is compensated by adjusting the midband gain with resistor R3 (Figure 7) so that the overall loop gain crosses 0dB before the minimum frequency RHP zero (i.e., corresponding to the highest duty ratio). The value of R3 can be estimated with the fromula:

$$R3 = \frac{390 V_O (1 - D_{MAX}) C_O R_L}{L}$$

Due to the low transconductance of the error amplifier, the gain setting resistor R3 is AC-coupled with capacitor C_Z. This prevents R3 from inducing an offset to the input of the error amplifier. It also creates a pole at DC and a low frequency zero.

The amplitude response of the error amplifier with the compensation network shown is:

$$\frac{\hat{V}_C}{\hat{V}_O} = g_m \left(\frac{R2}{R1 + R2} \right) \frac{1 + \left(S \bullet R3 \bullet C_Z \right)}{S \bullet C_Z \left[1 + \left(S \bullet R3 \bullet C_P \right) \right]}$$

$$C_Z >> C_P$$

The low frequency zero $1/R3C_Z$ of the compensation network is placed at $\omega_P/2$.

$$C_Z = \frac{2}{R3\omega_P}$$

The capacitor C_P ensures adequate gain margin beyond the RHP zero. The high frequency pole $1/R3C_P$ of the amplifier frequency response is placed beyond ω_Z.

$$C_P = \frac{1}{3\omega_Z R3}$$

Higher output filter capacitance rolls off the gain response from a lower corner frequency so higher midband gain is required in the compensation network to make the overall loop gain cross 0dB just below ω_Z.

Layout Consideration

To minimize EMI and high frequency resonances, it is essential to keep the SW and the CAP trace leads as short as possible. The input and the output bypass capacitors C_{IN} and C_{OUT} should be placed close to the IC package and soldered to the ground plane. A ground plane under the switching regulator is highly recommended. Figure 8 shows a suggested component placement and PC board layout.

APPLICATIONS INFORMATION

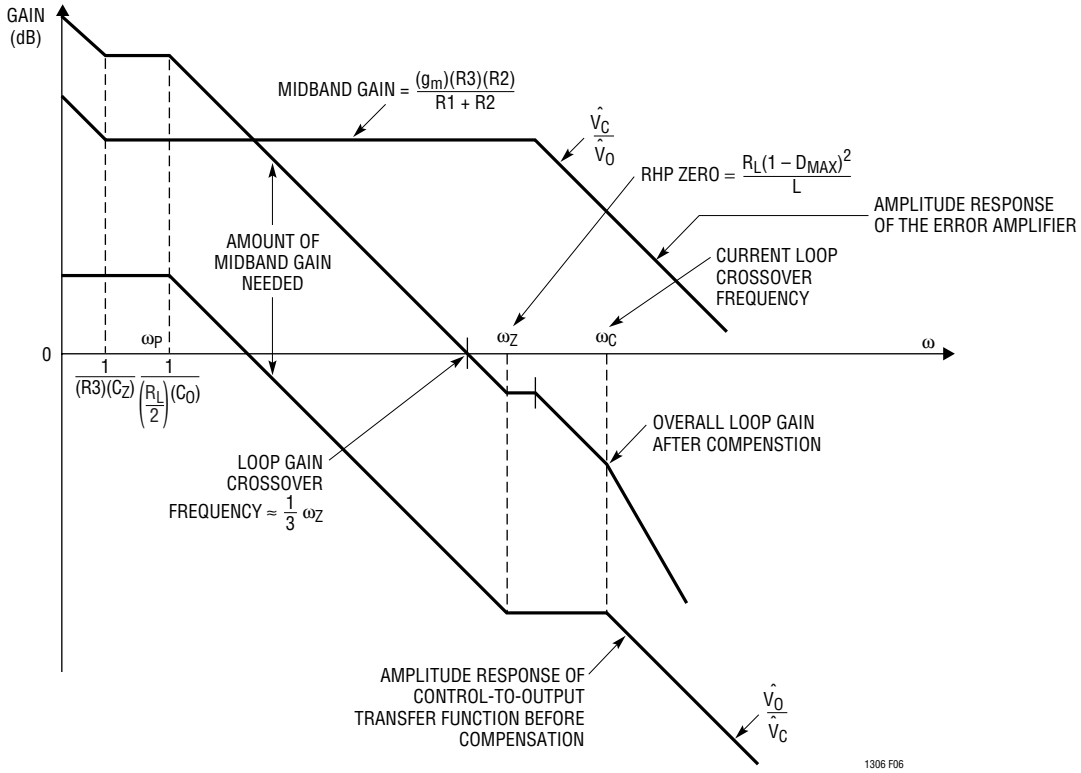


Figure 6. Gain Asymptotes of the Control-to-Output $\left(\frac{\hat{V}_O}{\hat{V}_C}\right)$ and Error Amplifier $\left(\frac{\hat{V}_C}{\hat{V}_O}\right)$ Transfer Function

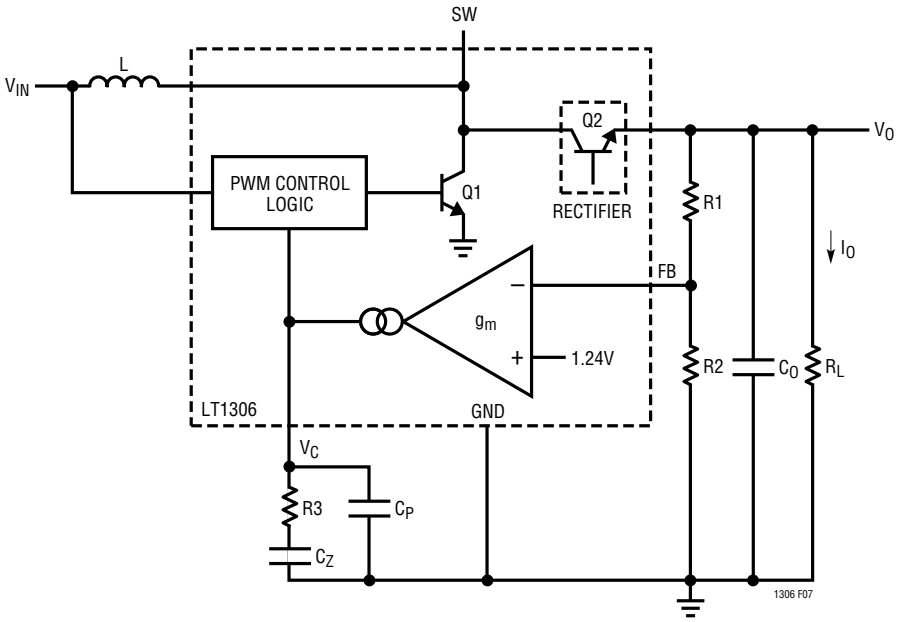


Figure 7. Current Mode Boost Converter Overall-Loop Compensation

APPLICATIONS INFORMATION

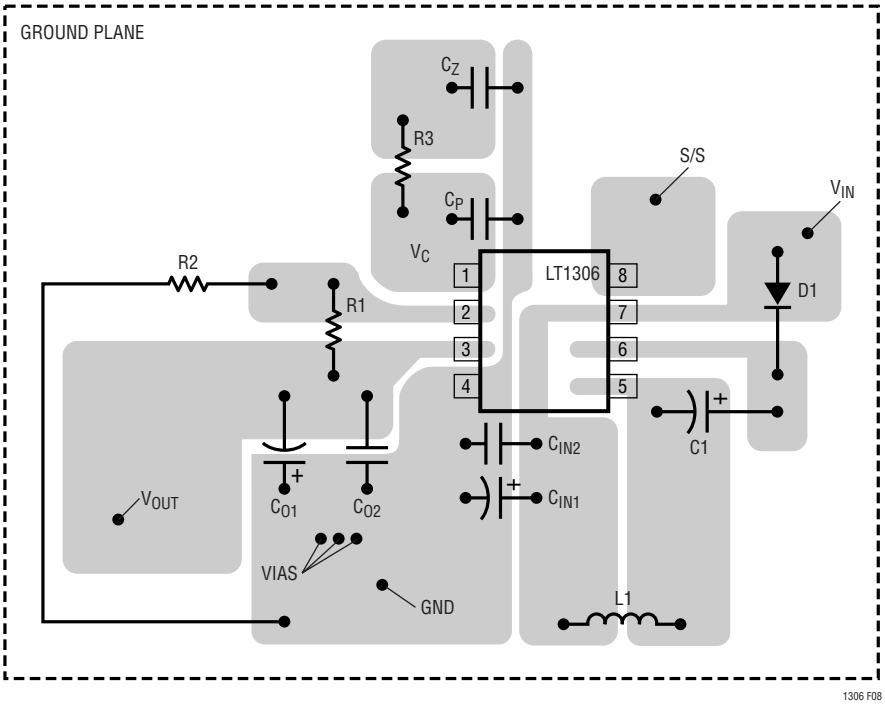
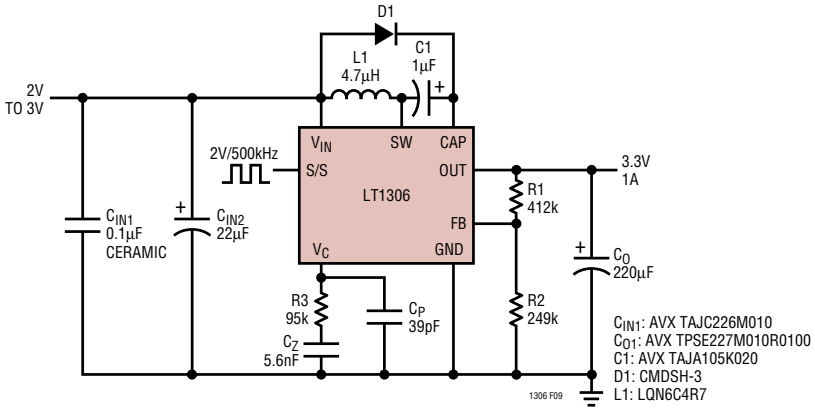


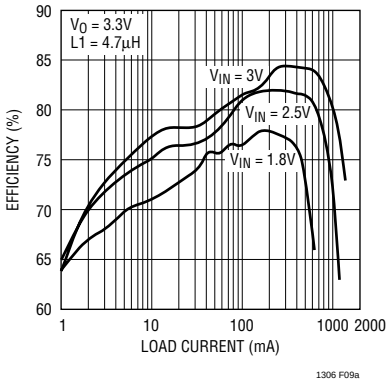
Figure 8. Recommended Component Placement for LT1306. Notice That the Input and the Output Capacitors Are Grounded at the Same Point. A Ground Plane Under the DC/DC Converter Is Highly Recommended. Use Multiple Vias to Tie Pin 4 Copper to the Ground Plane

TYPICAL APPLICATIONS

2-Cell NiMH to 3.3V Output



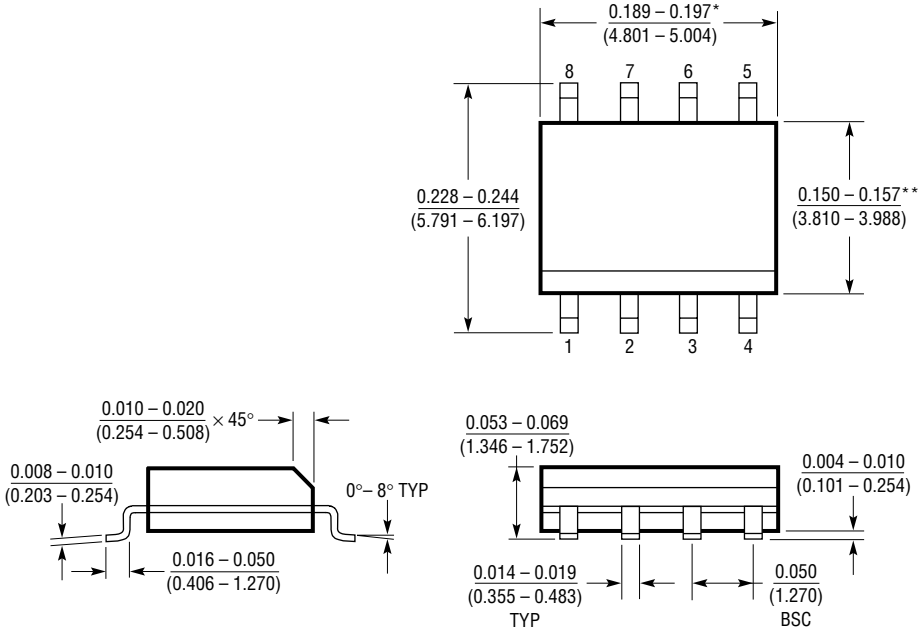
Efficiency



PACKAGE DESCRIPTION

Dimensions in inches (millimeters) unless otherwise noted.

S8 Package
8-Lead Plastic Small Outline (Narrow 0.150)
(LTC DWG # 05-08-1610)



*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

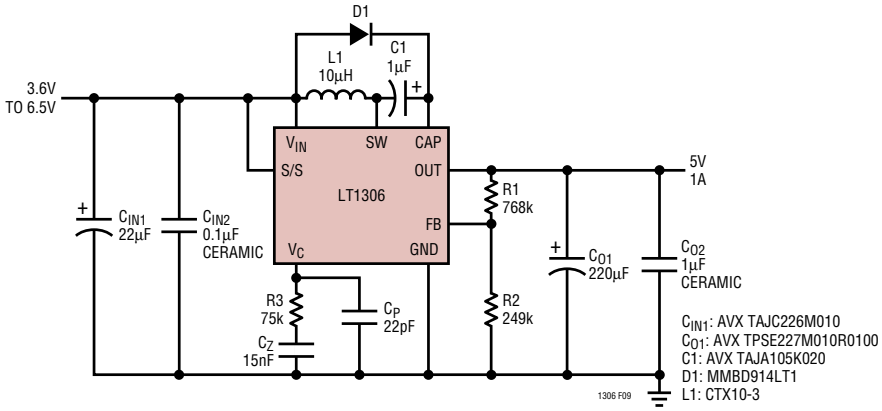
**DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

SO8 1298

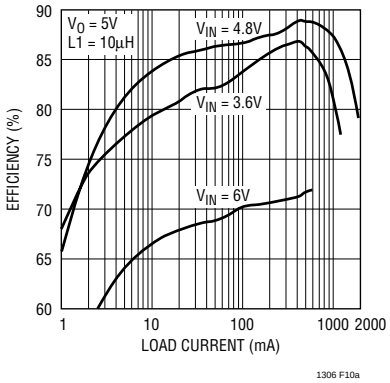
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TYPICAL APPLICATIONS

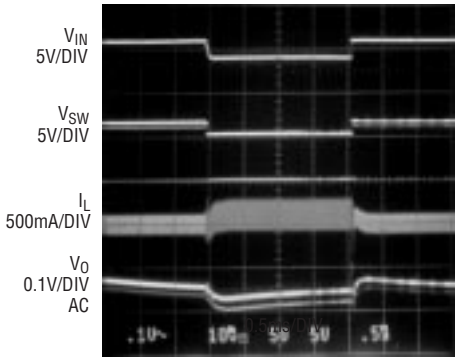
4-Cell NiMH to 5V Output



Efficiency



Transient Response with Step Input (4V to 6V)



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1302	High Output Current Micropower DC/DC Converter	5V/600mA from 2V, 2A Internal Switch, 200µA I_Q
LT1304	2-Cell Micropower DC/DC Converter	5V/200mA, Low-Battery Detector Active in Shutdown
LT1307/LT1307B	Single Cell, Micropower, 600kHz PWM DC/DC Converters	3.3V at 75mA from One Cell, MSOP Package
LT1308A/LT1308B	High Output Current Micropower DC/DC Converter	5V at 1A from Single Li-Ion Cell
LT1316	Burst Mode Operation DC/DC with Programmable Current Limit	1.5V Minimum, Precise Control of Peak Current Limit
LT1317/LT1317B	Micropower, 600kHz PWM DC/DC Converters	100µA I_Q, Operate with V_{IN} as Low as 1.5V
LT1610	Single-Cell Micropower DC/DC Converter	3V at 30mA from 1V, 1.7MHz Fixed Frequency
LT1613	1.4MHz Switching Regulator in 5-Lead SOT-23	5V at 200mA from 4.4V Input, Tiny SOT-23 package
LT1615	Micropower Step-Up DC/DC in 5-Lead SOT-23	20µA I_Q, 36V/350mA Internal Switch, V_{IN} as Low as 1.2V
LTC1624	High Efficiency N-channel Switching Regulator Controller	V_{OUT} = 1.19V to 30V in Stepdown; V_{IN} = 3.5V to 36V SO-8 Package
LT1949	600kHz, 1A Switch PWM DC/DC Converter	1.1A, 0.5Ω/30V Internal Switch, V_{IN} as Low as 1.5V

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