

Vishay Dale

Full Size Clock Oscillators TTL/HCMOS Compatible



The XO-543 series is with 3.3 V power supply. The metal package with pin 7 case ground acts as shielding to minimize EMI radiation.

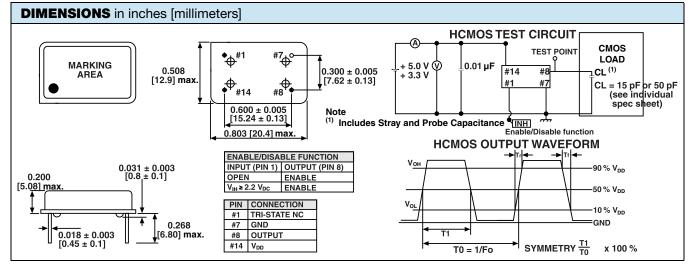
FEATURES

- Size: 14 pin full size
- Industry standard
- Wide frequency range
- Low cost
- Tri-state enable/disable
- Resistance weld package
- 3.3 V
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition

STANDARD ELECTRICAL SPECIFICATIONS			
PARAMETER	SYMBOL	CONDITION	VALUE
Frequency range	Fo	-	1.000 MHz to 100.000 MHz
Frequency stability ⁽¹⁾		all conditions	± 25 ppm, ± 50 ppm, ± 100 ppm
Operating temperature range	т	-	0 °C to 70 °C
	T _{OPR}		- 40 °C to + 85 °C (option)
Storage temperature range	T _{STG}	-	- 55 °C to + 125 °C
Power supply voltage	V _{DD}	-	3.3 V ± 10 %
Aging (first year)		25 °C ± 3 °C	± 5 ppm
Supply current		1.000 MHz to 23.999 MHz	15 mA max.
		24.000 MHz to 49.999 MHz	20 mA max.
	I _{DD}	50.000 MHz to 69.999 MHz	30 mA max.
		70.000 MHz to 100.000 MHz	45 mA max.
Output symmetry	Sym	at ½ V _{DD}	40 %/60 % (45 %/55 % option)
Rise time	t _r	10 % V _{DD} to 90 % V _{DD}	8 ns max.
Fall time	t _f	90 % V _{DD} to 10 % V _{DD}	8 ns max.
Output voltage	V _{OH}	-	90 % V _{DD} min.
	V _{OL}	-	10 % V _{DD} max.
Output load	TTL load	-	1 TTL to 5 TTL
	HCMOS load	-	to 50M: 30 pF
		-	to 125M: 15 pF
Start-up time	t _s	-	10 ms max.
Pin 1, tri-state function			pin 1 = H or open (output active at pin 3)
		-	pin $1 = L$ (high impedance at pin 3)

Note

(1) Include: 25 °C tolerance, operating temperature range, input voltage change, aging, load change, shock and vibration





COMPLIANT

HALOGEN

FREE

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ORDERING INFORMATION XO-543 в R Е 40M e2 MODEL OTR FREQUENCY STABILITY ENABLE/DISABLE FREQUENCY/MHz JEDEC LEAD (Pb)-FREE AA = 0.0025 % (25 ppm)blank = 0 °C to + 70 °C blank = pin 1 open standard R = - 40 °C to + 85 °C A = 0.005 % (50 ppm) E = disable to tri-state B = 0.01 % (100 ppm) **GLOBAL PART NUMBER** С т D Х 0 3 4 Е Ν Α 4 0 М MODEL FREQUENCY OTR ENABLE/ PACKAGE OPTIONS FREQUENCY STABILITY DISABLE CODE **GLOBAL PART NUMBERING** Х 0 5 С Е Ν 0 Μ 2 Т L А 4 OPERATING FREQUENCY ENABLE/ PACKAGE MODEL NUMBER TEMPERATURE OPTION FREQUENCY STABILITY DISABLE CODE (OTR) Tape and reel NA = no XO53 = XO-53 C = 0.01 % T = 0 °C toF = pin 14M = 4 MHz+ 70 °C open H = RF7additional XO54 = XO-54(100 ppm) 40M = 40 MHzE = disableXO34 = XO-543D = 0.005 % R = - 40 °C to options 100M = + 85 °C to tristate Bulk 60 = 45/55 100 MHz XO52 = XO-52(50 ppm) A = B04symmetry 12M288 = XO32 = XO-523 E = 0.0025 % (XO63, XO62, XO5M = XOSM-52 12.288 MHz (25 ppm) Contact XO61) XO63 = XOSM-533 factory for M is used as C = D06XO62 = XOSM-532 all other decimal place (XO57, XO37, XO61 = XOSM-531 options holder in XO27, XO17) XO57 = XOSM-57 frequency D = D07 XO37 = XOSM-573 (XO53, XO54, XO27 = XOSM-572 XO34, XO55, XO17 = XOSM-571 XO35) XO55 = XOSM-55 L = D08 XO35 = XOSM-553 (XO52, XO32, XO5M) Example: XO52CTELNA40M

PART MARKING	
Line 1:	M28_XXXXX (part number)
Line 2:	XX.XXXXM (frequency)
Line 3:	yywwvv (date/factory code)



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