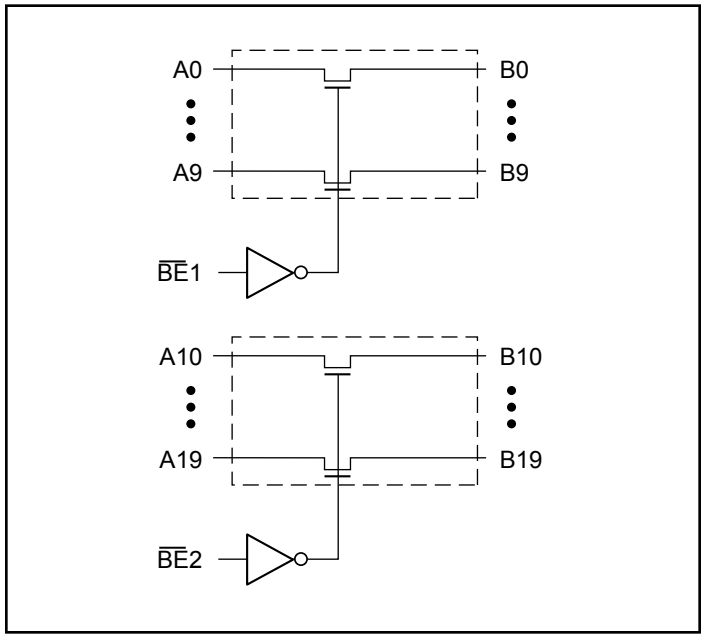
20-Bit, 2-Port Bus Switch

Features

- Near-Zero propagation delay
- 5Ω or 25Ω switches connect inputs to outputs
- Direct bus connection when switches are ON
- 32X384 function with flow through pinout make board layout easier
- Ultra-low quiescent power (0.2 µA typical) – ideally suited for notebook applications
- Industrial operating temperature: –40°C to +85°C
- Packaging (Pb-free & Green available):
  – 48-pin 240-mil wide plastic TSSOP (A)
  – 48-pin 300-mil wide plastic SSOP (V)
  – 48-pin 150-mil wide plastic BQSOP (B)

Block Diagram



Truth Table[1]

Function	$\overline{BE}$x	A19-A0
Disconnect	H	Hi-Z
Connect	L	B19-B0

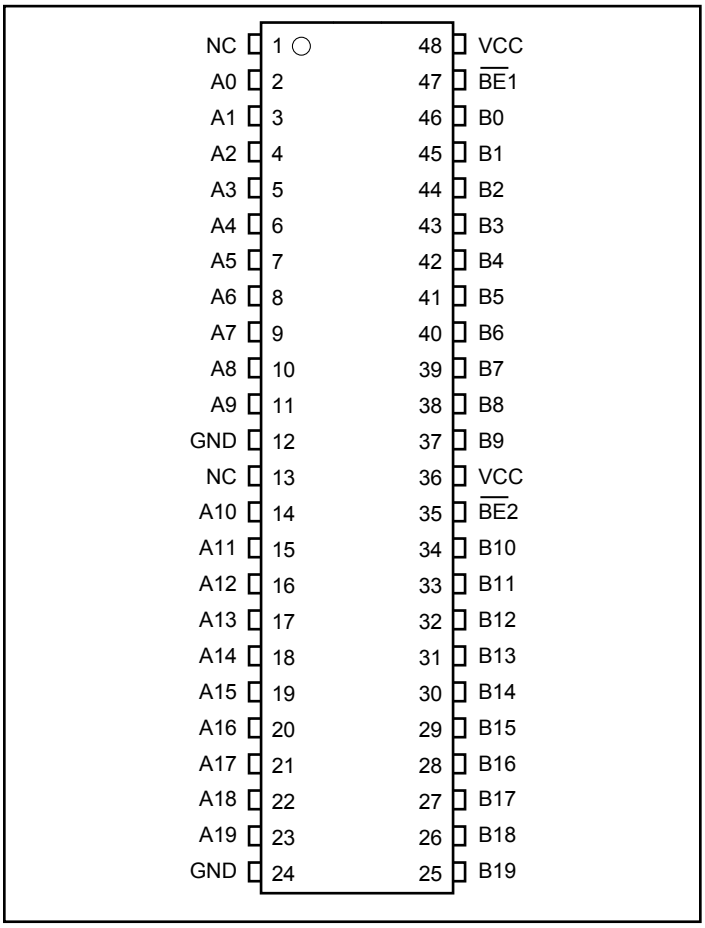
Note:
1. H = High Voltage Level, L = Low Voltage Level, Hi-Z = High Impedance

Description

Pericom Semiconductor's PI5C16861 is configured as a 20-bit, 2-port bus switch designed with a low On-Resistance (5Ω) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable ($\overline{BE}$x) input signal.

The PI5C162861 device has a built-in 25Ω series resistor to reduce noise resulting from reflections, thus eliminating the need for an external terminating resistor.

Pin Configuration



Pin Description

Pin Name	Description
$\overline{BE}$x	Bus Enable Inputs (Active LOW)
A19-A0	Bus A
B19-B0	Bus B

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V_{CC} Only)	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current	120mA
Power Dissipation	0.5W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±10%)

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level		–0.5	—	0.8	
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}		—	—	±1	µA
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND		—	—	±1	
I_{OZH}	High Impedance Output Current	0 ≤ A, B ≤ V_{CC}		—	—	±1	
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = –18mA		—	–0.7	–1.2	V
I_{ON}	Short Circuit Current[3]	A (B) = 0V, B (A) = V_{CC}		100	—	—	mA
V_H	Input Hysteresis at Control Pins			—	150	—	mV
R_{ON}	Switch On-Resistance[4]	V_{CC} = Min., V_{IN} = 0.0V,	16861	—	5	7	Ω
		I_{ON} = 48mA	162861	20	28	40	
		V_{CC} = Min., V_{IN} = 2.4V,	16861	—	10	15	
		I_{ON} = 15mA	162861	20	35	48	

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters[5]	Description	Test Conditions	Typ	Units
C_{IN}	Input Capacitance	V_{IN} = 0V	6	pF
C_{OFF}	A/B Capacitance, Switch Off		6	
C_{ON}	A/B Capacitance, Switch On		8	

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A,B) pins.
5. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions(1)		Min.	Typ(2)	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max.	V_{IN} = GND or V_{CC}		0.1	10	µA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V_{CC} = Max.	V_{IN} = 3.4V(3)			2.5	mA
I_{CCD}	Supply Current per Input per MHz(4)	V_{CC} = Max., A and B Pins Open $\overline{BE}x$ = GND, Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 5.0V, +25°C ambient.
3. Per TTL driven input (V_{IN}= 3.4V, control inputs only); A and B pins do not contribute to I_{CC}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

PI5C16861 Switching Characteristics over Operating Range

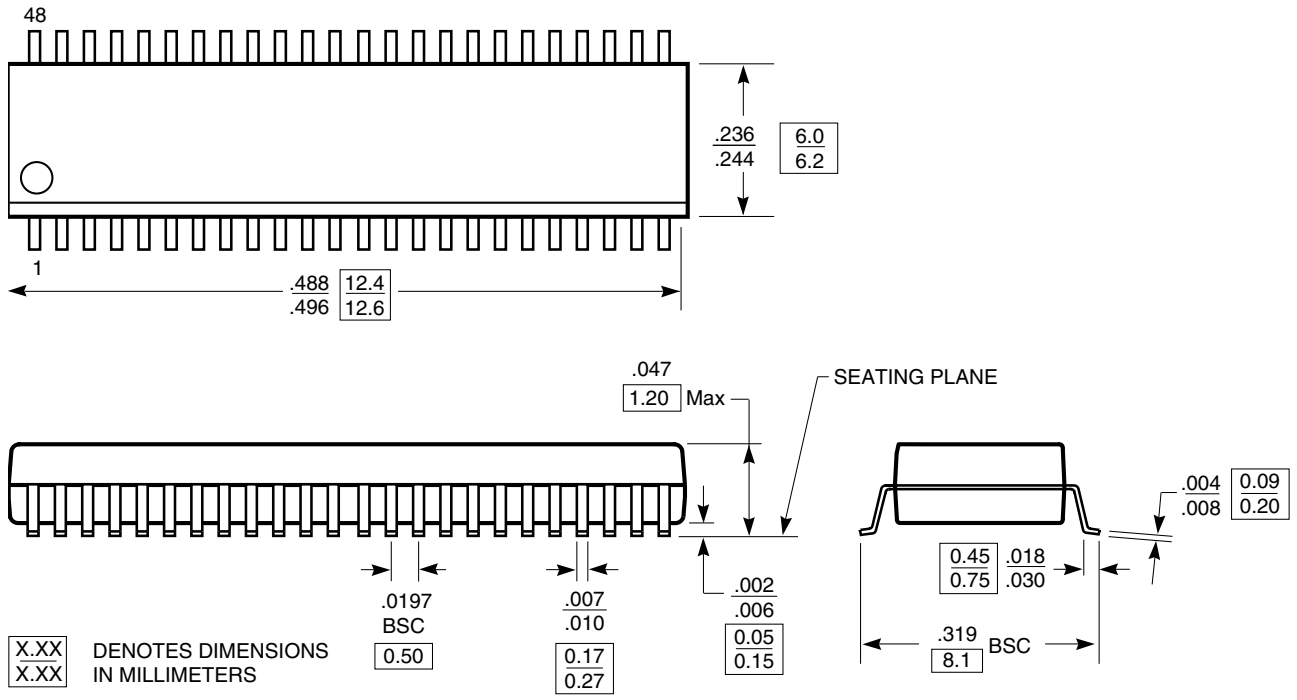
Parameters	Description	Conditions(1)	PI5C16861		Unit
			Com.		
			Min	Max	
t_{PLH} t_{PHL}	Propagation Delay(2,3) Ax to Bx, Bx to Ax	C_L = 50pF R_L = 500Ω		1.25	ns
t_{PZH} t_{PZL}	Bus Enable Time $\overline{BE}$x to Ax or Bx		1.5	6.5	
t_{PHZ} t_{PLZ}	Bus Disable Time $\overline{BE}$x to Ax or Bx		1.5	5.5	

PI5C162861 Switching Characteristics over Operating Range

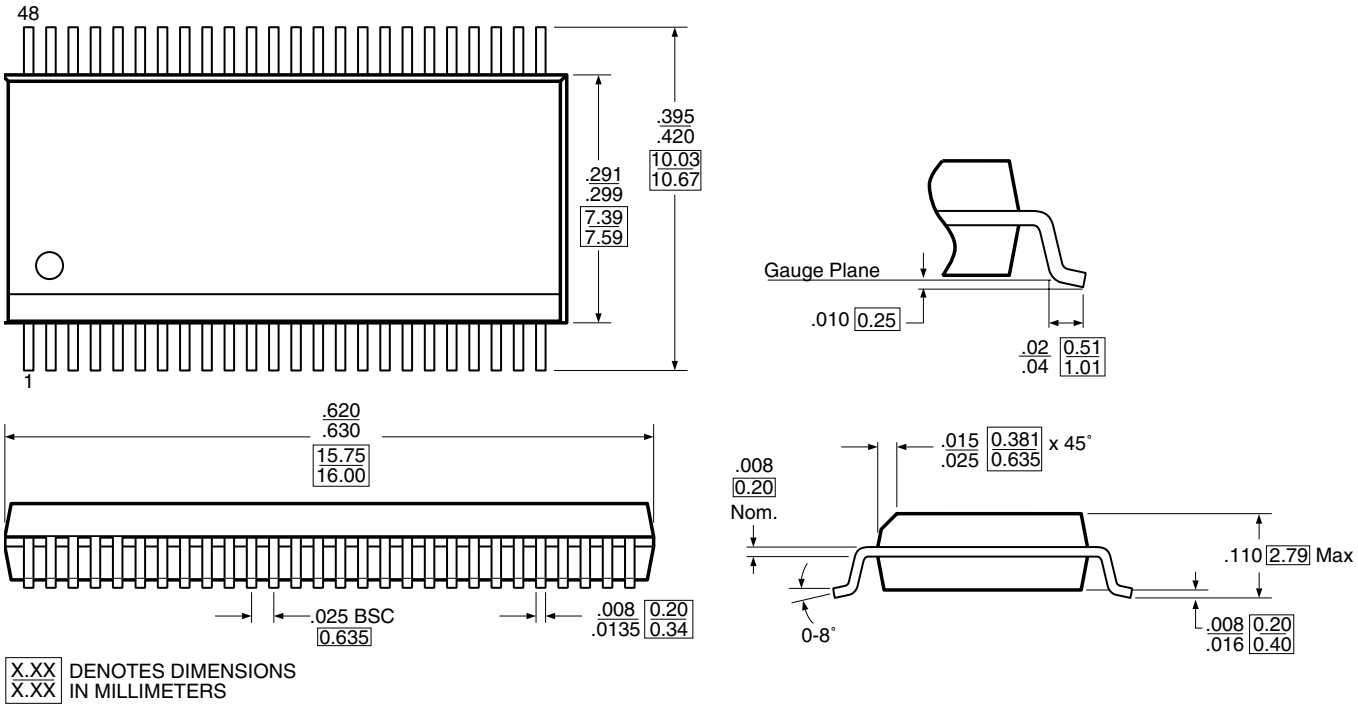
Parameters	Description	Conditions(1)	PI5C162861		Unit
			Com.		
			Min	Max	
t_{PLH} t_{PHL}	Propagation Delay(2,3) Ax to Bx, Bx to Ax	C_L = 50pF R_L = 500Ω		1.25	ns
t_{PZH} t_{PZL}	Bus Enable Time $\overline{BE}$x to Ax or Bx		1.5	6.5	
t_{PHZ} t_{PLZ}	Bus Disable Time $\overline{BE}$x to Ax or Bx		1.5	5.5	

Notes:
1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

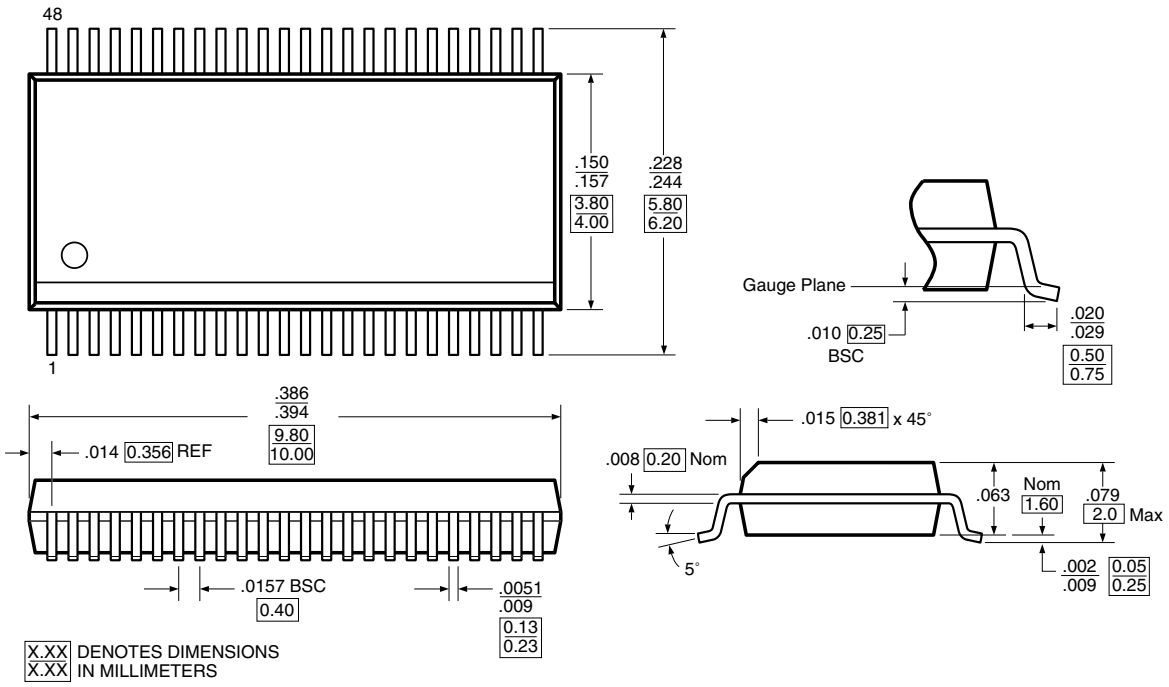
Packaging Mechanical: 48-pin TSSOP (A)



Packaging Mechanical: 48-pin SSOP (V)



Packaging Mechanical: 48-pin BSQOP (B)



Ordering Information

Ordering Code	Package Type	Package Description
PI5C16861A	A	48-Pin, TSSOP
PI5C16861AE	A	Pb-free & Green, 48-Pin, TSSOP
PI5C16861V	V	48-Pin, SSOP
PI5C16861VE	V	Pb-free & Green, 48-Pin, SSOP
PI5C16861B	B	48-Pin, BQSOP
PI5C16861BE	B	Pb-free & Green, 48-Pin, BQSOP
PI5C162861A	A	48-Pin, TSSOP
PI5C162861AE	A	Pb-free & Green, 48-Pin, TSSOP
PI5C162861V	V	48-Pin, SSOP
PI5C162861VE	V	Pb-free & Green, 48-Pin, SSOP
PI5C162861B	B	48-Pin, BQSOP
PI5C162861BE	B	Pb-free & Green, 48-Pin, BQSOP

Notes:

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
2. X = Tape and reel