Process Change Notice #1206151

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PCN Date: 6/15/2012	**Effective Date: 6/15/2012**	
Title: Si5040 Datasheet Revision from 1.2 to 1.3		
Originator: Jason RockJason Rock	**Phone:** 512-532-5997	**Dept:** Timing Marketing
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PCN Type:

☒ **Datasheet**	☐ **Foundry**	☐ **Packing**
☐ **Product Revision**	☐ **Assembly**	☐ **Labeling**
☐ **Discontinuance**	☐ **Test**	☐ **Other**

Last Order Date: N/A

PCN Details

Description of Change:
Silicon Labs is pleased to announce rev 1.3 for the Si5040-D-GM device. Please refer to Appendix A for a list of document changes. The Si5040-D-GM updated datasheet is available at www.silabs.com.

Reason for Change:
Introduction of new features and to ensure the datasheet is consistent with the functionality of the Si5040 device.

Impact on Form, Fit, Function, Quality, Reliability:
There is no impact on form, fit, function, quality or reliability.

Product Identification:
Si5040

Last Date of Unchanged Product: N/A

Qualification Samples:

N/A

Customer Early Acceptance Sign Off:

Customers may approve early PCN acceptance by completing the information below:

Early Acceptance: **Date:** _____

Name: _____

Company: _____

Email your early Acceptance approval to: katherine.haggar@silabs.com

Qualification Data:
N/A

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Appendix A:

Si5040 datasheet revision 1.2 to 1.3 changes:

- Clarified RX LOL feature in sections “5.8.1.1. Dynamic Register Control” and “5.8.2. Frequency LOL”
- Added section “5.8.3. Acquisition Time Enhancement”
- Clarified TX LOL feature in sections “6.4.1.1. Dynamic Register Control” and “6.4.2. Frequency LOL”
- Added section “6.4.3. Acquisition Time Enhancement”
- Changed TX and RX path data rate minimum parameters from 9.9 to 9.8 Gbps to allow for 9.8304 Gbps CPRI data rate

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