

# SN54F153, SN74F153 DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS

SDFS052A – D2932, MARCH 1987 – REVISED OCTOBER 1993

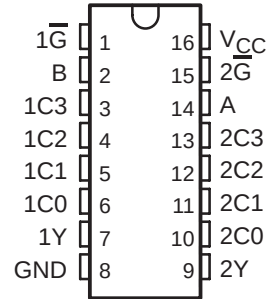
- Permits Multiplexing From N Lines to One Line
- Performs Parallel-to-Serial Conversion
- Strobe (Enable) Line Provided for Cascading (N Lines to N Lines)
- Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

## description

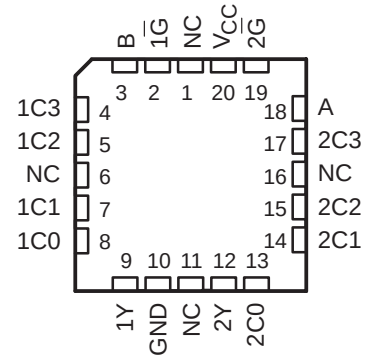
These data selectors/multiplexers contain inverters and drivers to supply full binary decoding data selection to the AND-OR gates. Separate strobe ( $\overline{G}$ ) inputs are provided for each of the two 4-line sections.

The SN54F153 is characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN74F153 is characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

SN54F153 . . . J PACKAGE  
SN74F153 . . . D OR N PACKAGE  
(TOP VIEW)



SN54F153 . . . FK PACKAGE  
(TOP VIEW)



NC – No internal connection

FUNCTION TABLE

| INPUTS |   |      |    |    |    | STROBE<br>$\overline{G}$ | OUTPUT<br>Y |
|--------|---|------|----|----|----|--------------------------|-------------|
| SELECT |   | DATA |    |    |    |                          |             |
| B      | A | C0   | C1 | C2 | C3 |                          |             |
| X      | X | X    | X  | X  | X  | H                        | L           |
| L      | L | L    | X  | X  | X  | L                        | L           |
| L      | L | H    | X  | X  | X  | L                        | H           |
| L      | H | X    | L  | X  | X  | L                        | L           |
| L      | H | X    | H  | X  | X  | L                        | H           |
| H      | L | X    | X  | L  | X  | L                        | L           |
| H      | L | X    | X  | H  | X  | L                        | H           |
| H      | H | X    | X  | X  | L  | L                        | L           |
| H      | H | X    | X  | X  | H  | L                        | H           |

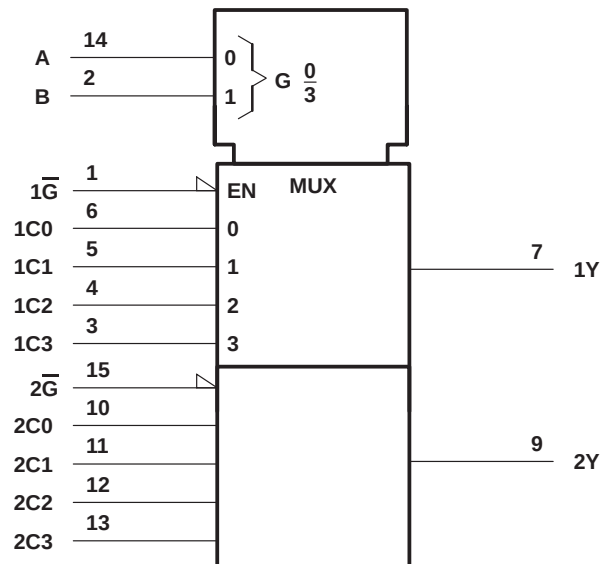
Select inputs A and B are common to both sections.

# SN54F153, SN74F153

## DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS

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logic symbol†



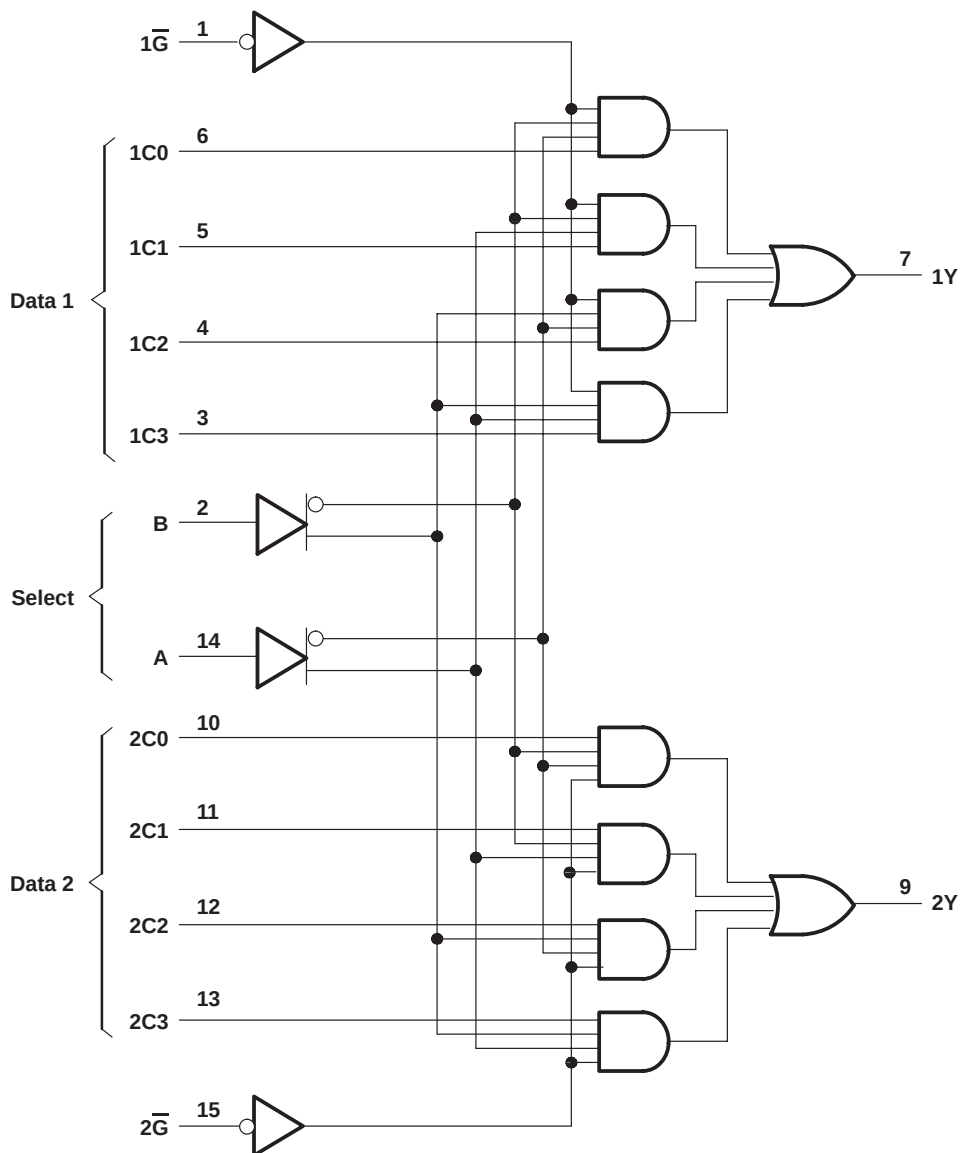
† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.  
Pin numbers shown are for the D, J, and N packages.

# SN54F153, SN74F153

## DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS

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logic diagram (positive logic)



Pin numbers shown are for the D, J, and N packages.



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265  
POST OFFICE BOX 1443 • HOUSTON, TEXAS 77251-1443

# SN54F153, SN74F153

## DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS

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### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

|                                                       |                    |
|-------------------------------------------------------|--------------------|
| Supply voltage range, $V_{CC}$                        | –0.5 V to 7 V      |
| Input voltage range (see Note 1)                      | –1.2 V to 7 V      |
| Input current range                                   | –30 mA to 5 mA     |
| Voltage range applied to any output in the high state | –0.5 V to $V_{CC}$ |
| Current into any output in the low state              | 40 mA              |
| Operating free-air temperature range: SN54F153        | –55°C to 125°C     |
| SN74F153                                              | 0°C to 70°C        |
| Storage temperature range                             | –65°C to 150°C     |

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input voltage rating may be exceeded provided that the input current rating is observed.

### recommended operating conditions

|          |                                | SN54F153 |     |     | SN74F153 |     |     | UNIT |
|----------|--------------------------------|----------|-----|-----|----------|-----|-----|------|
|          |                                | MIN      | NOM | MAX | MIN      | NOM | MAX |      |
| $V_{CC}$ | Supply voltage                 | 4.5      | 5   | 5.5 | 4.5      | 5   | 5.5 | V    |
| $V_{IH}$ | High-level input voltage       | 2        |     |     | 2        |     |     | V    |
| $V_{IL}$ | Low-level input voltage        |          |     | 0.8 |          |     | 0.8 | V    |
| $I_{IK}$ | Input clamp current            |          |     | –18 |          |     | –18 | mA   |
| $I_{OH}$ | High-level output current      |          |     | –1  |          |     | –1  | mA   |
| $I_{OL}$ | Low-level output current       |          |     | 20  |          |     | 20  | mA   |
| $T_A$    | Operating free-air temperature | –55      |     | 125 | 0        |     | 70  | °C   |

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER     | TEST CONDITIONS                     | SN54F153 |                  |      | SN74F153 |                  |      | UNIT |
|---------------|-------------------------------------|----------|------------------|------|----------|------------------|------|------|
|               |                                     | MIN      | TYP <sup>‡</sup> | MAX  | MIN      | TYP <sup>‡</sup> | MAX  |      |
| $V_{IK}$      | $V_{CC} = 4.5$ V, $I_I = -18$ mA    |          |                  | –1.2 |          |                  | –1.2 | V    |
| $V_{OH}$      | $V_{CC} = 4.5$ V, $I_{OH} = -1$ mA  | 2.5      | 3.4              |      | 2.5      | 3.4              |      | V    |
|               | $V_{CC} = 4.75$ V, $I_{OH} = -1$ mA |          |                  |      | 2.7      |                  |      |      |
| $V_{OL}$      | $V_{CC} = 4.5$ V, $I_{OL} = 20$ mA  |          | 0.3              | 0.5  |          | 0.3              | 0.5  | V    |
| $I_I$         | $V_{CC} = 5.5$ V, $V_I = 7$ V       |          |                  | 0.1  |          |                  | 0.1  | mA   |
| $I_{IH}$      | $V_{CC} = 5.5$ V, $V_I = 2.7$ V     |          |                  | 20   |          |                  | 20   | μA   |
| $I_{IL}$      | $V_{CC} = 5.5$ V, $V_I = 0.5$ V     |          |                  | –0.6 |          |                  | –0.6 | mA   |
| $I_{OS}^{\S}$ | $V_{CC} = 5.5$ V, $V_O = 0$         | –60      |                  | –150 | –60      |                  | –150 | mA   |
| $I_{CC}$      | $V_{CC} = 5.5$ V, $V_I = 0$         |          | 12               | 20   |          | 12               | 20   | mA   |

<sup>‡</sup> All typical values are at  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$ .

<sup>\S</sup> Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

# SN54F153, SN74F153

## DUAL 1-OF-4 DATA SELECTORS/MULTIPLEXERS

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### switching characteristics (see Note 2)

| PARAMETER        | FROM<br>(INPUT) | TO<br>(OUTPUT) | V <sub>CC</sub> = 5 V,<br>C <sub>L</sub> = 50 pF,<br>R <sub>L</sub> = 500 Ω,<br>T <sub>A</sub> = 25°C |     |      | V <sub>CC</sub> = 4.5 V to 5.5 V,<br>C <sub>L</sub> = 50 pF,<br>R <sub>L</sub> = 500 Ω,<br>T <sub>A</sub> = MIN to MAX† |      |          |      | UNIT |
|------------------|-----------------|----------------|-------------------------------------------------------------------------------------------------------|-----|------|-------------------------------------------------------------------------------------------------------------------------|------|----------|------|------|
|                  |                 |                | 'F153                                                                                                 |     |      | SN54F153                                                                                                                |      | SN74F153 |      |      |
|                  |                 |                | MIN                                                                                                   | TYP | MAX  | MIN                                                                                                                     | MAX  | MIN      | MAX  |      |
| t <sub>PLH</sub> | A or B          | Y              | 3.7                                                                                                   | 7.7 | 10.5 | 3.7                                                                                                                     | 14   | 3.7      | 12   | ns   |
| t <sub>PHL</sub> |                 |                | 2.7                                                                                                   | 6.6 | 9    | 2.7                                                                                                                     | 11   | 2.7      | 10.5 |      |
| t <sub>PLH</sub> | $\overline{G}$  | Y              | 3.7                                                                                                   | 6.7 | 9    | 3.7                                                                                                                     | 11.5 | 3.7      | 10.5 | ns   |
| t <sub>PHL</sub> |                 |                | 2.2                                                                                                   | 5.3 | 7    | 1.7                                                                                                                     | 9    | 1.7      | 8    |      |
| t <sub>PLH</sub> | C               | Y              | 2.2                                                                                                   | 4.9 | 7    | 1.7                                                                                                                     | 9    | 2.2      | 8    | ns   |
| t <sub>PHL</sub> |                 |                | 2.2                                                                                                   | 4.7 | 6.5  | 1.7                                                                                                                     | 8    | 1.7      | 7.5  |      |

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.





**PACKAGING INFORMATION**

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5)                | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|----------------------------------------|-------------------------|
| 5962-9758301Q2A  | ACTIVE        | LCCC         | FK                 | 20   | 1              | TBD                        | POST-PLATE              | N / A for Pkg Type   | -55 to 125   | 5962-<br>9758301Q2A<br>SNJ54F<br>153FK | <a href="#">Samples</a> |
| 5962-9758301QEA  | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | 5962-9758301QE<br>A<br>SNJ54F153J      | <a href="#">Samples</a> |
| 5962-9758301QFA  | ACTIVE        | CFP          | W                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | 5962-9758301QF<br>A<br>SNJ54F153W      | <a href="#">Samples</a> |
| JM38510/33902B2A | ACTIVE        | LCCC         | FK                 | 20   | 1              | TBD                        | POST-PLATE              | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33902B2A                   | <a href="#">Samples</a> |
| JM38510/33902BEA | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33902BEA                   | <a href="#">Samples</a> |
| JM38510/33902BFA | ACTIVE        | CFP          | W                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33902BFA                   | <a href="#">Samples</a> |
| M38510/33902B2A  | ACTIVE        | LCCC         | FK                 | 20   | 1              | TBD                        | POST-PLATE              | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33902B2A                   | <a href="#">Samples</a> |
| M38510/33902BEA  | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33902BEA                   | <a href="#">Samples</a> |
| M38510/33902BFA  | ACTIVE        | CFP          | W                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | JM38510/<br>33902BFA                   | <a href="#">Samples</a> |
| SN54F153J        | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | SN54F153J                              | <a href="#">Samples</a> |
| SN74F153D        | ACTIVE        | SOIC         | D                  | 16   | 40             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | 0 to 70      | F153                                   | <a href="#">Samples</a> |
| SN74F153DG4      | ACTIVE        | SOIC         | D                  | 16   | 40             | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | 0 to 70      | F153                                   | <a href="#">Samples</a> |
| SN74F153DR       | ACTIVE        | SOIC         | D                  | 16   | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | 0 to 70      | F153                                   | <a href="#">Samples</a> |
| SN74F153DRG4     | ACTIVE        | SOIC         | D                  | 16   | 2500           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | 0 to 70      | F153                                   | <a href="#">Samples</a> |
| SN74F153N        | ACTIVE        | PDIP         | N                  | 16   | 25             | Pb-Free<br>(RoHS)          | CU NIPDAU               | N / A for Pkg Type   | 0 to 70      | SN74F153N                              | <a href="#">Samples</a> |

| Orderable Device | Status<br>(1) | Package Type | Package<br>Drawing | Pins | Package<br>Qty | Eco Plan<br>(2)            | Lead/Ball Finish<br>(6) | MSL Peak Temp<br>(3) | Op Temp (°C) | Device Marking<br>(4/5)                | Samples                 |
|------------------|---------------|--------------|--------------------|------|----------------|----------------------------|-------------------------|----------------------|--------------|----------------------------------------|-------------------------|
| SN74F153NSR      | ACTIVE        | SO           | NS                 | 16   | 2000           | Green (RoHS<br>& no Sb/Br) | CU NIPDAU               | Level-1-260C-UNLIM   | 0 to 70      | 74F153                                 | <a href="#">Samples</a> |
| SNJ54F153FK      | ACTIVE        | LCCC         | FK                 | 20   | 1              | TBD                        | POST-PLATE              | N / A for Pkg Type   | -55 to 125   | 5962-<br>9758301Q2A<br>SNJ54F<br>153FK | <a href="#">Samples</a> |
| SNJ54F153J       | ACTIVE        | CDIP         | J                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | 5962-9758301QE<br>A<br>SNJ54F153J      | <a href="#">Samples</a> |
| SNJ54F153W       | ACTIVE        | CFP          | W                  | 16   | 1              | TBD                        | A42                     | N / A for Pkg Type   | -55 to 125   | 5962-9758301QF<br>A<br>SNJ54F153W      | <a href="#">Samples</a> |

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBsolete:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

<sup>(6)</sup> Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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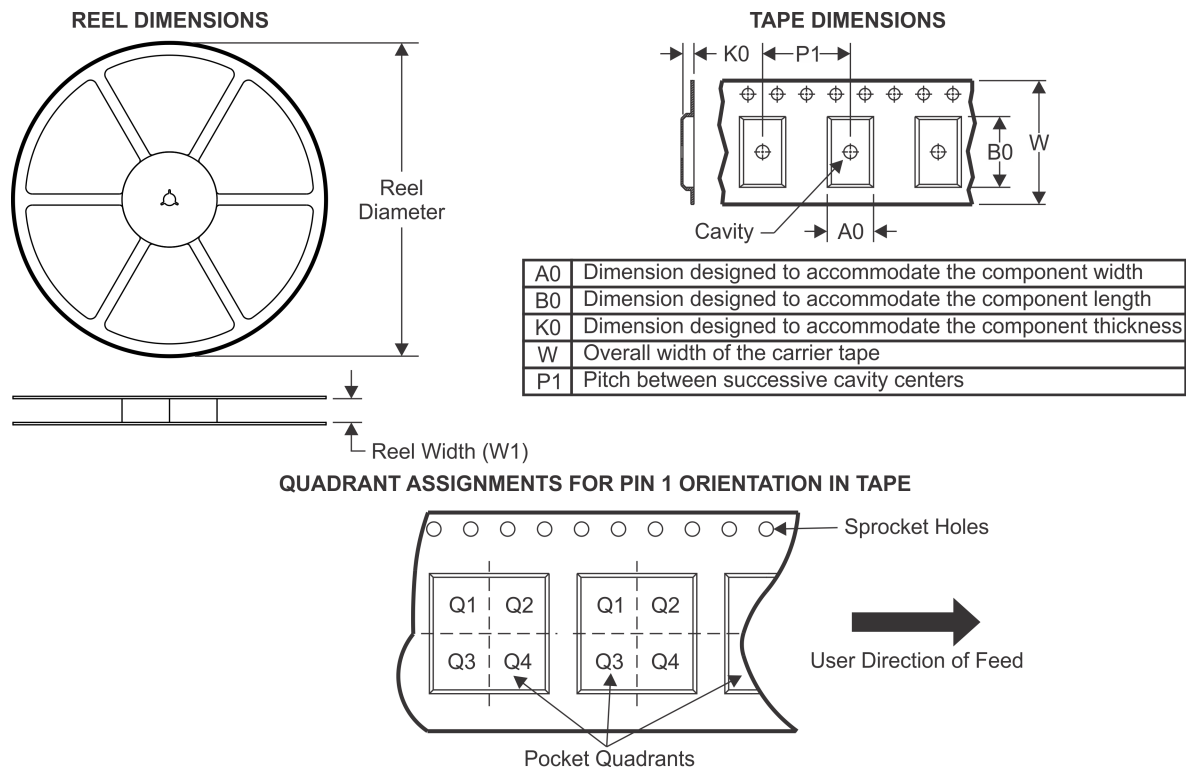
In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

**OTHER QUALIFIED VERSIONS OF SN54F153, SN74F153 :**

- Catalog: [SN74F153](#)
- Military: [SN54F153](#)

NOTE: Qualified Version Definitions:

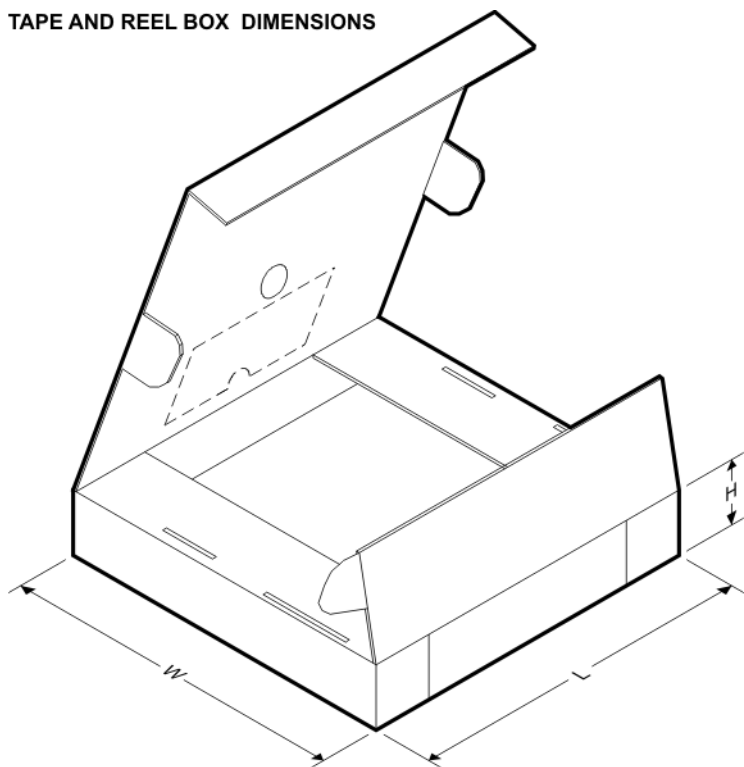
- Catalog - TI's standard catalog product
- Military - QML certified for Military and Defense Applications

**TAPE AND REEL INFORMATION**


\*All dimensions are nominal

| Device      | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74F153DR  | SOIC         | D               | 16   | 2500 | 330.0              | 16.4               | 6.5     | 10.3    | 2.1     | 8.0     | 16.0   | Q1            |
| SN74F153NSR | SO           | NS              | 16   | 2000 | 330.0              | 16.4               | 8.2     | 10.5    | 2.5     | 12.0    | 16.0   | Q1            |

## TAPE AND REEL BOX DIMENSIONS



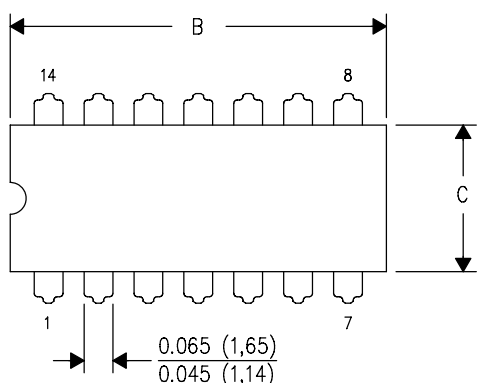
\*All dimensions are nominal

| Device      | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|-------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN74F153DR  | SOIC         | D               | 16   | 2500 | 333.2       | 345.9      | 28.6        |
| SN74F153NSR | SO           | NS              | 16   | 2000 | 367.0       | 367.0      | 38.0        |

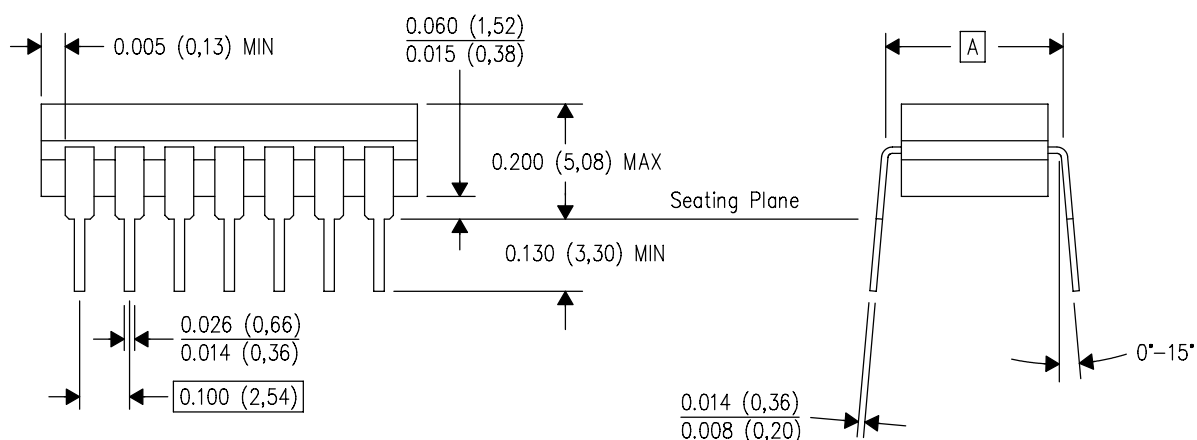
J (R-GDIP-T\*\*)

14 LEADS SHOWN

# CERAMIC DUAL IN-LINE PACKAGE



| PINS **<br>DIM | 14                     | 16                     | 18                     | 20                     |
|----------------|------------------------|------------------------|------------------------|------------------------|
| A              | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC | 0.300<br>(7,62)<br>BSC |
| B MAX          | 0.785<br>(19,94)       | .840<br>(21,34)        | 0.960<br>(24,38)       | 1.060<br>(26,92)       |
| B MIN          | —                      | —                      | —                      | —                      |
| C MAX          | 0.300<br>(7,62)        | 0.300<br>(7,62)        | 0.310<br>(7,87)        | 0.300<br>(7,62)        |
| C MIN          | 0.245<br>(6,22)        | 0.245<br>(6,22)        | 0.220<br>(5,59)        | 0.245<br>(6,22)        |

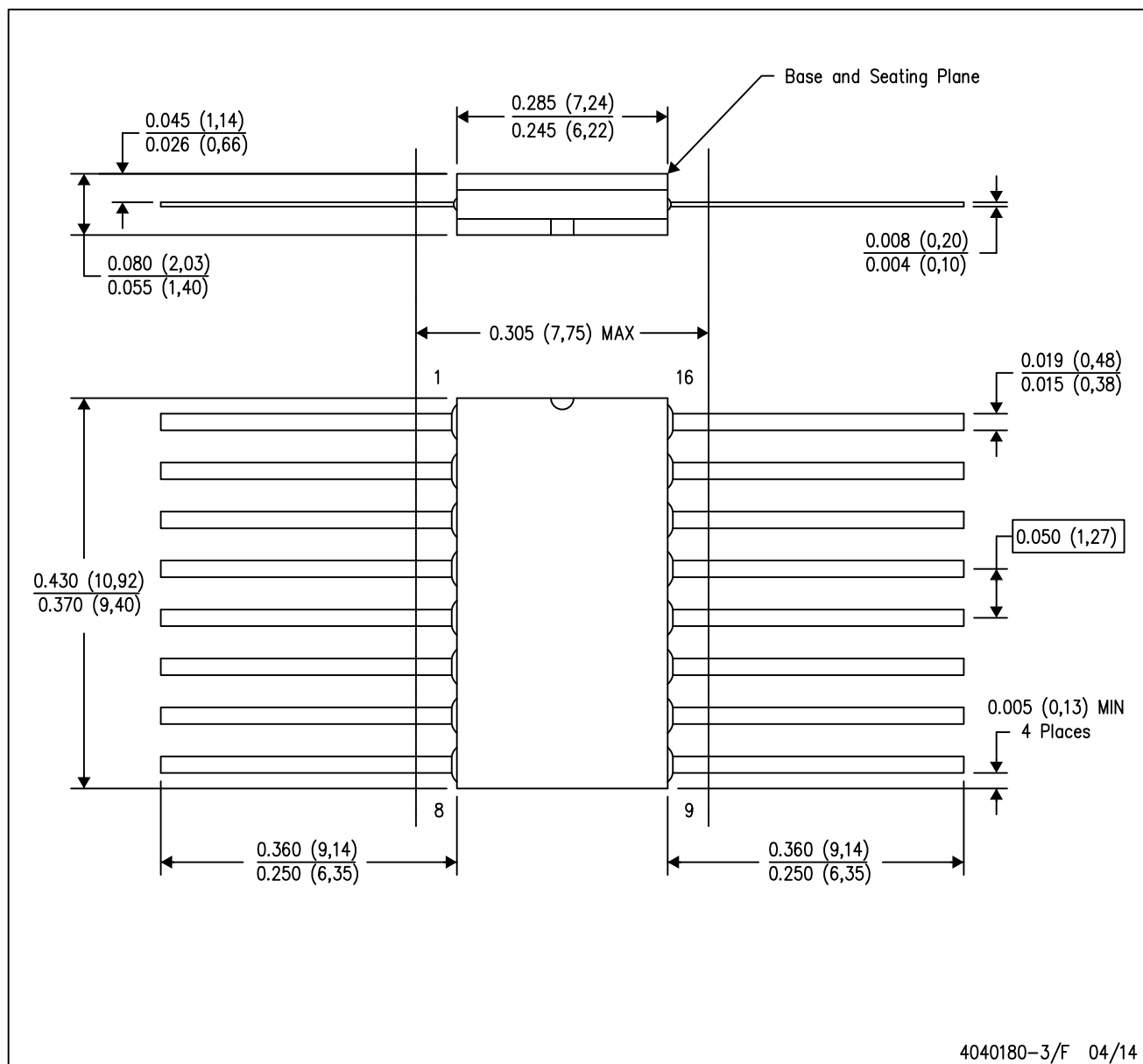


4040083/F 03/03

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. This package is hermetically sealed with a ceramic lid using glass frit.
  - D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
  - E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F16)

CERAMIC DUAL FLATPACK

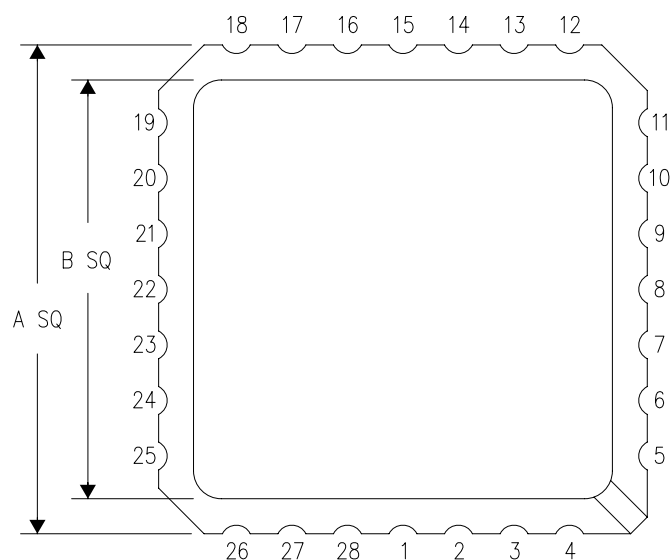


- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package can be hermetically sealed with a ceramic lid using glass frit.
  - Index point is provided on cap for terminal identification only.
  - Falls within MIL STD 1835 GDFP2-F16

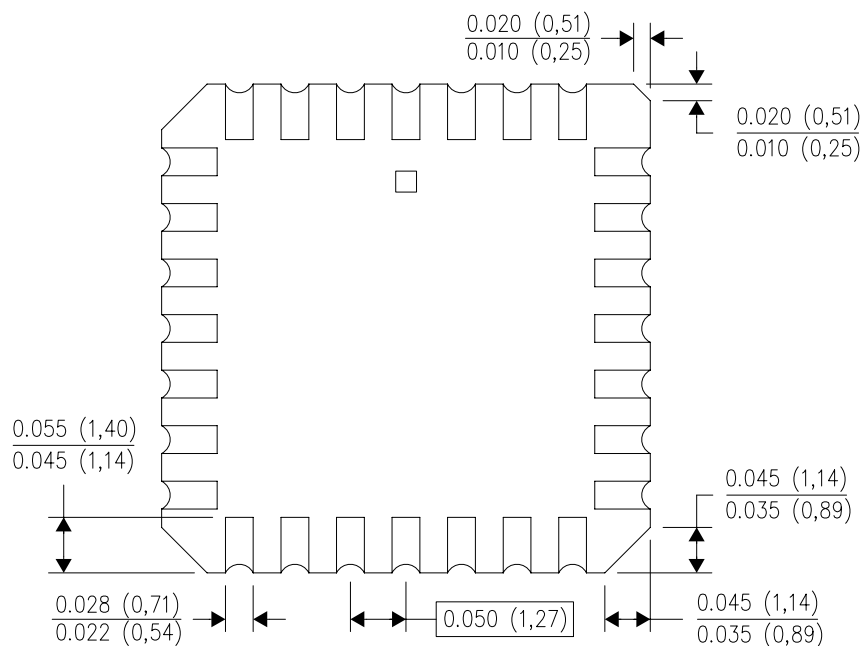
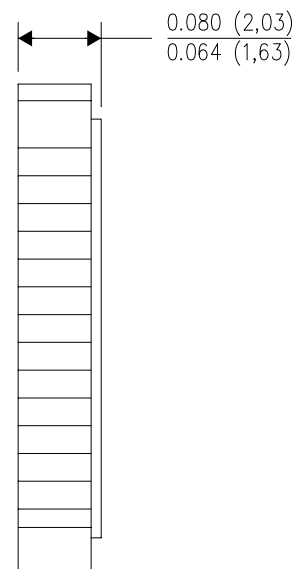
FK (S-CQCC-N\*\*)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



| NO. OF<br>TERMINALS<br>** | A                |                  | B                |                  |
|---------------------------|------------------|------------------|------------------|------------------|
|                           | MIN              | MAX              | MIN              | MAX              |
| 20                        | 0.342<br>(8,69)  | 0.358<br>(9,09)  | 0.307<br>(7,80)  | 0.358<br>(9,09)  |
| 28                        | 0.442<br>(11,23) | 0.458<br>(11,63) | 0.406<br>(10,31) | 0.458<br>(11,63) |
| 44                        | 0.640<br>(16,26) | 0.660<br>(16,76) | 0.495<br>(12,58) | 0.560<br>(14,22) |
| 52                        | 0.740<br>(18,78) | 0.761<br>(19,32) | 0.495<br>(12,58) | 0.560<br>(14,22) |
| 68                        | 0.938<br>(23,83) | 0.962<br>(24,43) | 0.850<br>(21,6)  | 0.858<br>(21,8)  |
| 84                        | 1.141<br>(28,99) | 1.165<br>(29,59) | 1.047<br>(26,6)  | 1.063<br>(27,0)  |



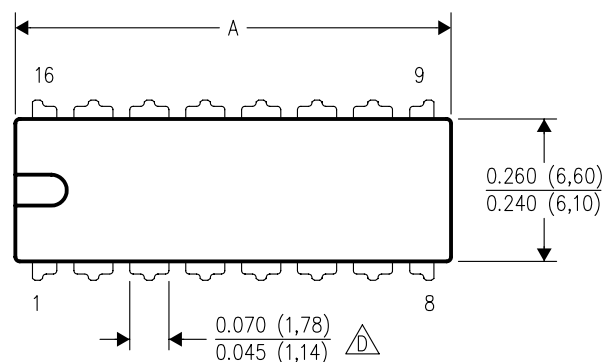
4040140/D 01/11

- NOTES:
- All linear dimensions are in inches (millimeters).
  - This drawing is subject to change without notice.
  - This package can be hermetically sealed with a metal lid.
  - Falls within JEDEC MS-004

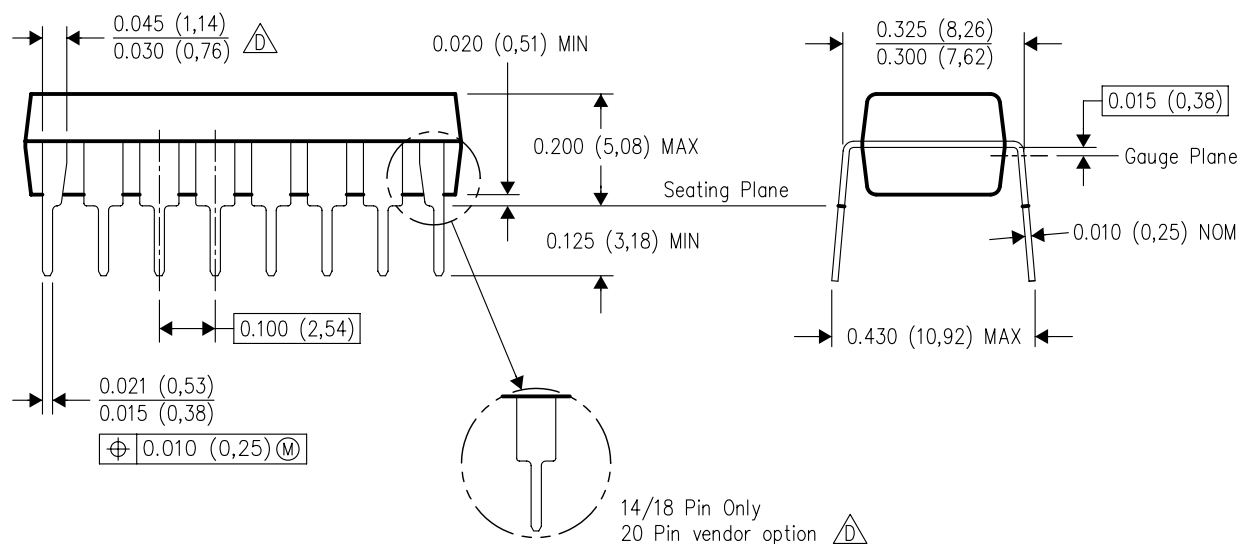
## N (R-PDIP-T\*\*)

16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



| PINS **             | 14               | 16               | 18               | 20               |
|---------------------|------------------|------------------|------------------|------------------|
| DIM                 |                  |                  |                  |                  |
| A MAX               | 0.775<br>(19,69) | 0.775<br>(19,69) | 0.920<br>(23,37) | 1.060<br>(26,92) |
| A MIN               | 0.745<br>(18,92) | 0.745<br>(18,92) | 0.850<br>(21,59) | 0.940<br>(23,88) |
| MS-001<br>VARIATION | AA               | BB               | AC               | AD               |

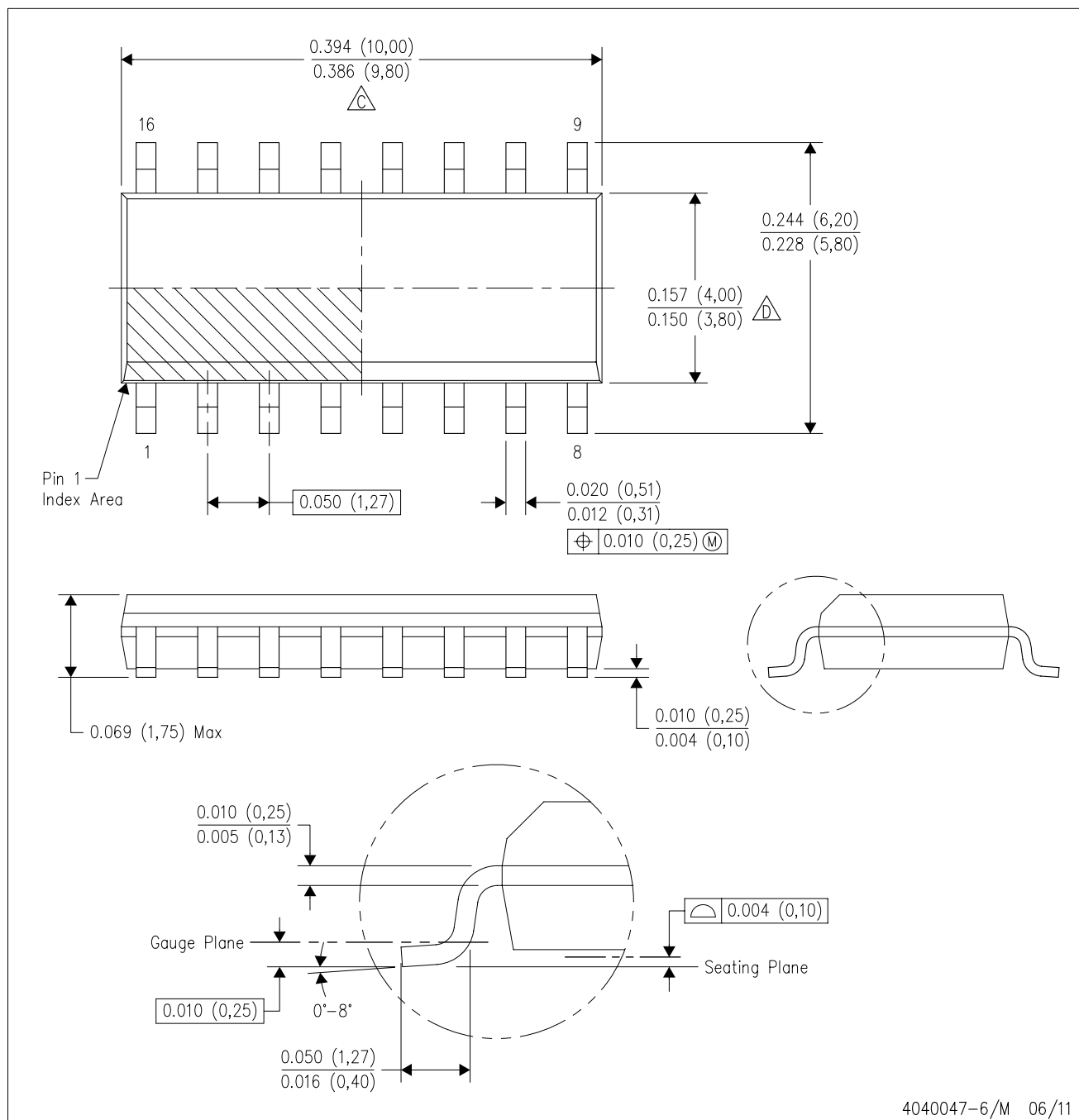


4040049/E 12/2002

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
  - The 20 pin end lead shoulder width is a vendor option, either half or full width.

## D (R-PDSO-G16)

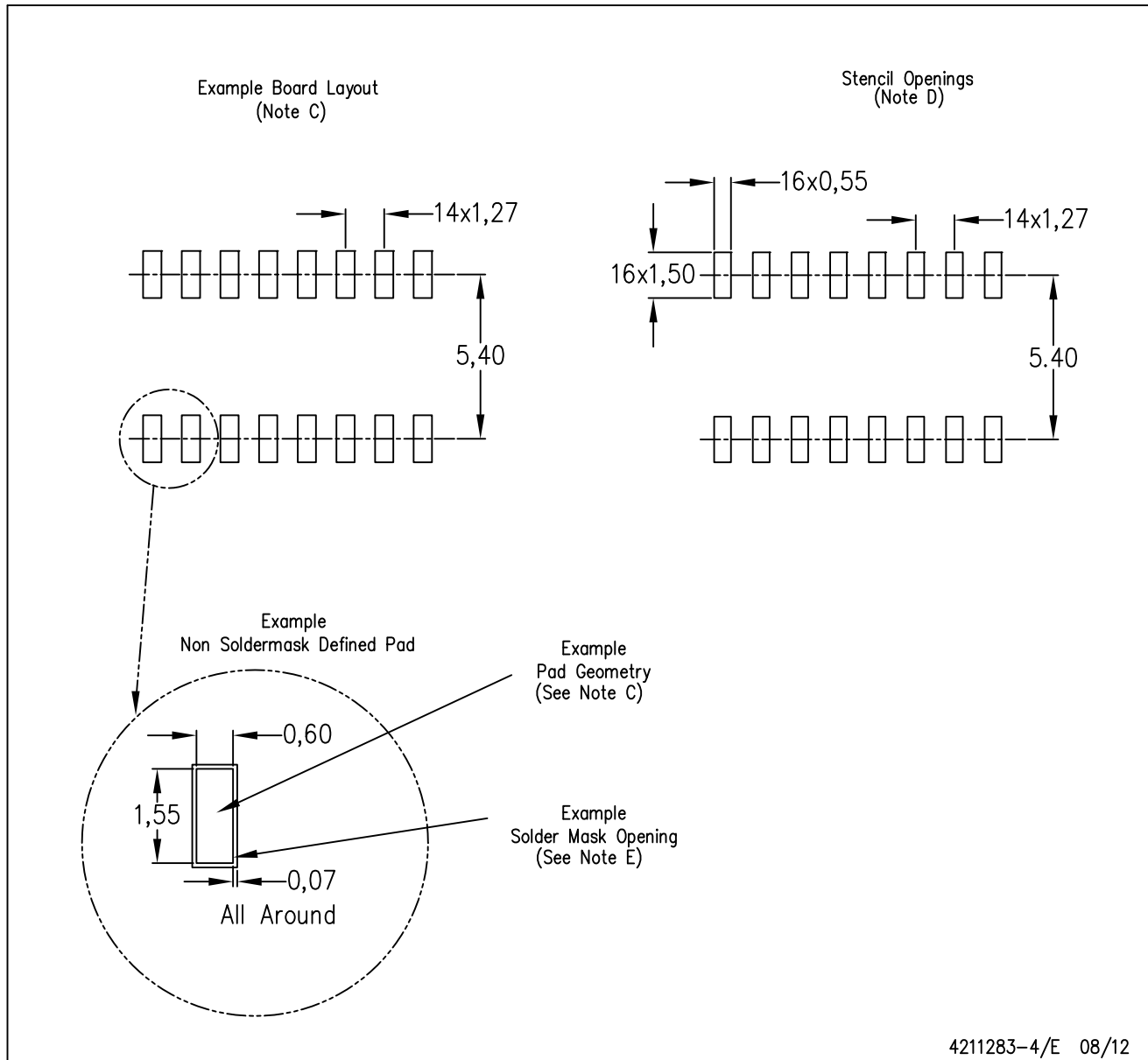
## PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  -  C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
  -  D. Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
  - E. Reference JEDEC MS-012 variation AC.

D (R-PDSO-G16)

PLASTIC SMALL OUTLINE



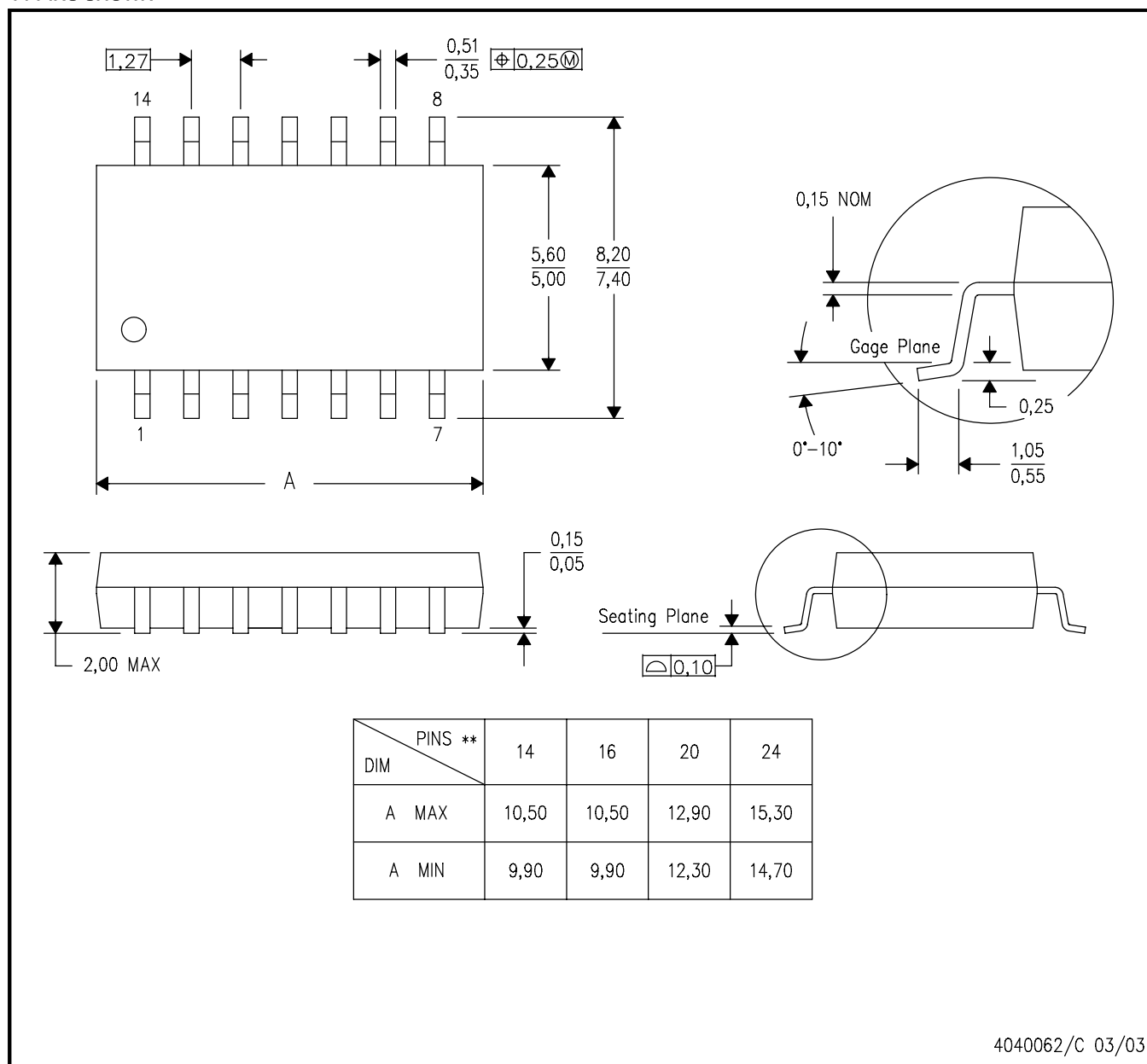
- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Publication IPC-7351 is recommended for alternate designs.
  - Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  - Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

# MECHANICAL DATA

NS (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

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|                               |                                                                                          |
|-------------------------------|------------------------------------------------------------------------------------------|
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