



UH8102

CMOS IC

LOW POWER HALL EFFECT SWITCH

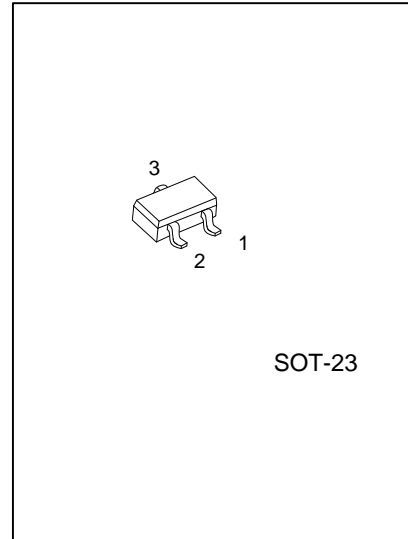
DESCRIPTION

UH8102 is a low-power integrated Hall switch designed to sense the applied magnetic flux density and give a digital output, which indicates the present condition of the magnitude sensed.

It mainly designed for battery-powered system and hand-held equipment, such as cellular flip-phones and PDA's, in which power consumption is one major concern. The typical power consumption of UH8100 at down to 10uW in 2.7V supply.

For **UH8102A**, the output will be at the "Low" level when no magnetic field is applied. When the applied magnetic flux density is stronger than the switching threshold, the output would be at the "High" level.

For **UH8102B**, the output will be at the "High" level when no magnetic field is applied. When the applied magnetic flux density is stronger than the switching threshold, the output would be at the "Low" level.



FEATURES

- *Micropower Operation
- *2.4V to 5.5V Battery Operation
- *Offset Canceling Technology
- *Superior Temperature Stability
- *Extremely Low Switch-Point Drift
- *Insensitive to Physical Stress

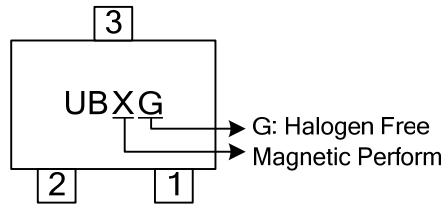
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UH8102AL-AE3-R	UH8102AG-AE3-R	SOT-23	O	I	G	Tape Reel
UH8102BL-AE3-R	UH8102BG-AE3-R	SOT-23	O	I	G	Tape Reel

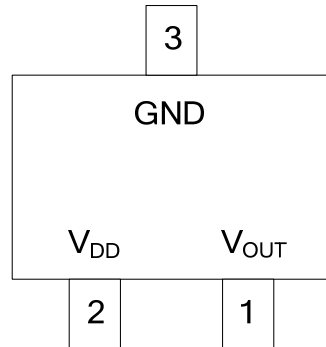
Note: O: Output, I: V_{DD}, G: Gnd

<p>UH8102XG-AE3-R</p> <ul style="list-style-type: none"> (1) Packing Type (2) Package Type (3) Halogen Free (4) Magnetic Perform 	<ul style="list-style-type: none"> (1) R:Tape Reel (2) AE3:SOT-23 (3) L: Lead Free, G:Halogen Free (4) A: LOW B: HIGH
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■ MARKING INFORMATION



■ PIN CONFIGURATION

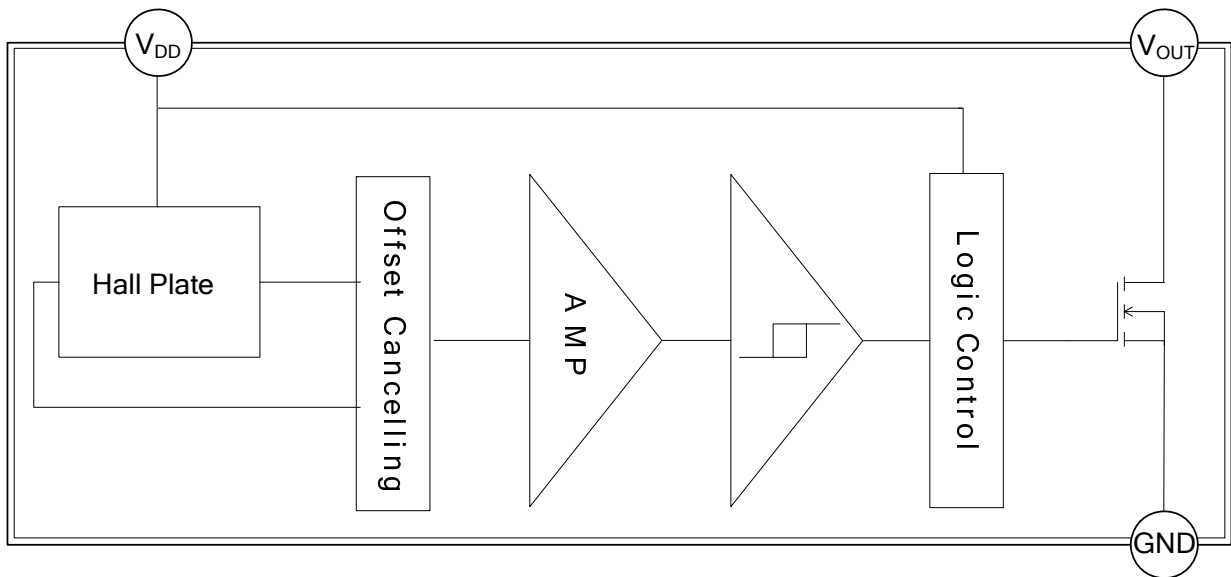


■ PIN DESCRIPTION

PIN NAME	PIN TYPE	PIN DESCRIPTION
V _{OUT}	O	Digital Output
V _{DD}	P	Power Supply
GND	G	Ground

Note: O=Output, P=Power Supply, G=Ground

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Magnetic Flux Density	B	Unlimited	mT
Supply Voltage	V _{DD}	5.5	V
Supply current	I _Q	-1 ~ +2.5	mA
Junction Temperature	T _J	150	°C
Operation Temperature	T _{OPR}	-40 ~ +85	°C
Storage Temperature	T _{STG}	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.
Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS (T_A=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V _{DD}	Operating	2.4	2.7	5.5	V
Output Voltage	V _{OUT}		-0.3	2.7	5.5	V
Ambient Temperature	T _A		-40	25	85	°C

■ ELECTRICAL CHARACTERISTICS (T_A=25° C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Output Saturation Voltage	V _{SAT}	V _{DD} =2.7V		0.1		V
Output Leakage Current	I _{OFF}			0.01		µA
Supply Current	I _{DD(EN)}	V _{DD} =2.7V	Chip enable	1.1		mA
	I _{DD(DIS)}			2.5		µA
	I _{DD(AVG)}			3	20	µA
Operating Time	T _{OP}	V _{DD} =2.7V		60		µs
Standby Time	T _{SD}			150		ms
Duty Cycle	D.C.			0.04		%

■ MAGNETIC CHARACTERISTICS (T_A=25°C, V_{DD}=2.7V)

For UH8102A (**LOW** level when no magnetic field is applied)

Rank 1

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Operation Points	B _{OP}	7	15	25	Gauss
Release Points	B _{RP}	15	25	35	
Hysteresis	B _{OP} -B _{RP}	5	10	15	

Rank 2

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Operation Points	B _{OP}	7		50	Gauss
Release Points	B _{RP}	15		70	
Hysteresis	B _{OP} -B _{RP}	5		50	

For UH8102B (**HIGH** level when no magnetic field is applied)

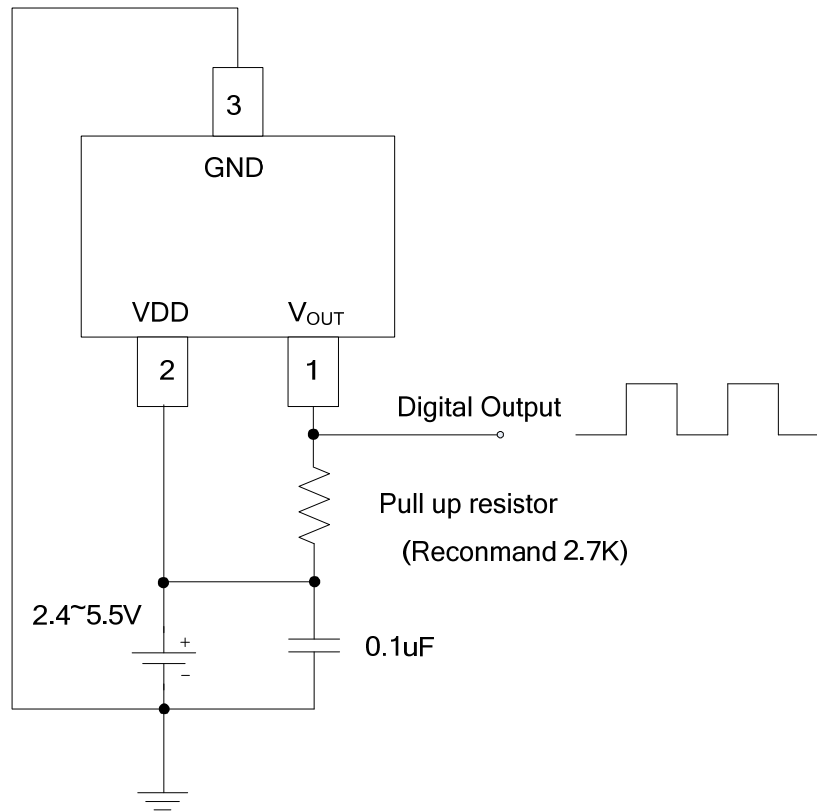
Rank 1

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Operation Points	B _{OP}	15	25	35	Gauss
Release Points	B _{RP}	7	15	25	
Hysteresis	B _{OP} -B _{RP}	5	10	15	

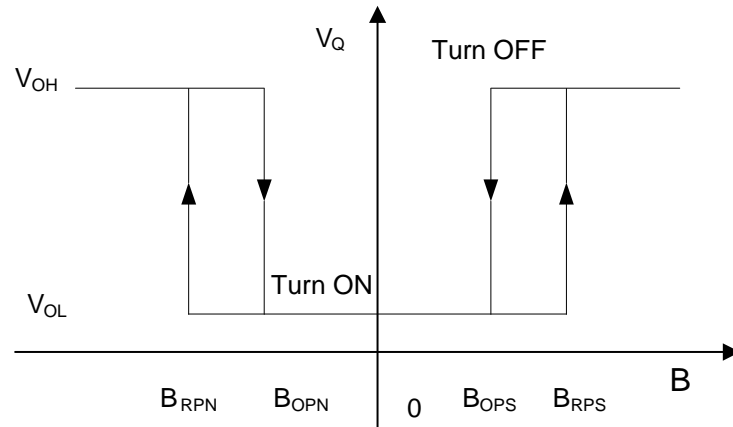
Rank 2

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Operation Points	B _{OP}	15		70	Gauss
Release Points	B _{RP}	7		50	
Hysteresis	B _{OP} -B _{RP}	5		50	

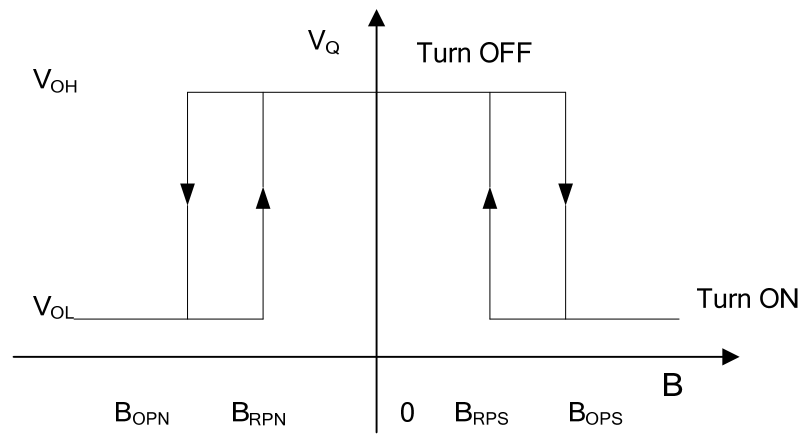
■ TYPICAL APPLICATION CIRCUIT



■ MAGNETIC FLUX



UH8102A (**LOW** level when no magnetic field is applied)



UH8102B (**HIGH** level when no magnetic field is applied)

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