

ISDN S-Interface Applications Transformers - RM Style

Designed to meet pulse waveform template of CCITT I.430 when using proper chip pair.

Electrical Specifications ^{1,2,3} at 25°C

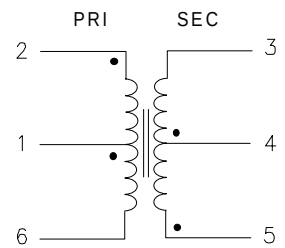
Part Number	Turns Ratio ($\pm 5\%$)	OCL min. (mH)	PRI-SEC $C_{w/w}$ max. (pF)	Leakage Ind. max. (μ H)	Primary DCR max. (Ω)	Secondary DCR max. (Ω)	Style & Schem.	Primary Pins
T-13750	1CT:2CT	30	50	15	2.2	4.8	RM5 - D	2-6
T-13751	1CT:1CT	30	50	22	2.2	1.5	RM5 - D	2-6
T-13752	1CT:2.5CT	30	50	15	2.2	8.2	RM5 - D	2-6
T-13753	1CT:1.8CT	30	50	15	2.2	4.2	RM5 - D	2-6
T-13754	1CT:2CT	20	50	30	1.2	3.5	RM6 - D	2-6
T-13755	1CT:2.5CT	22	50	20	2.8	6.5	RM6 - D	2-6
T-13756	1CT:1.8CT	22	50	20	2.8	5.5	RM6 - D	2-6

1. Hi-pot (Pri. to Sec.) 2000 V_{RMS} except T-13707. Isolation is 1000 V_{RMS}

2. ET-Product of 10 V- μ sec min.

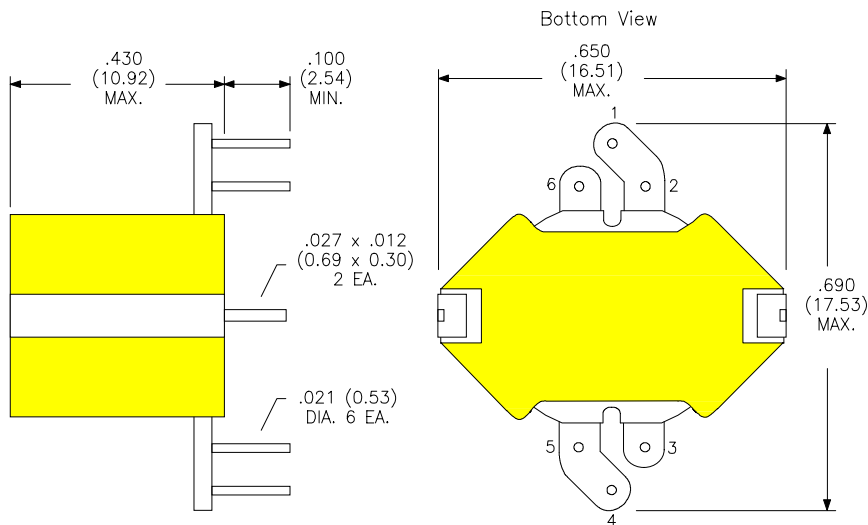
3. OCL Measured at Primary @10KHz & 700mV

Schematic "D"



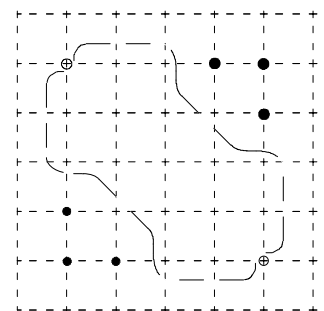
RM 5 Package

Package Physical Dimensions in inches (mm)

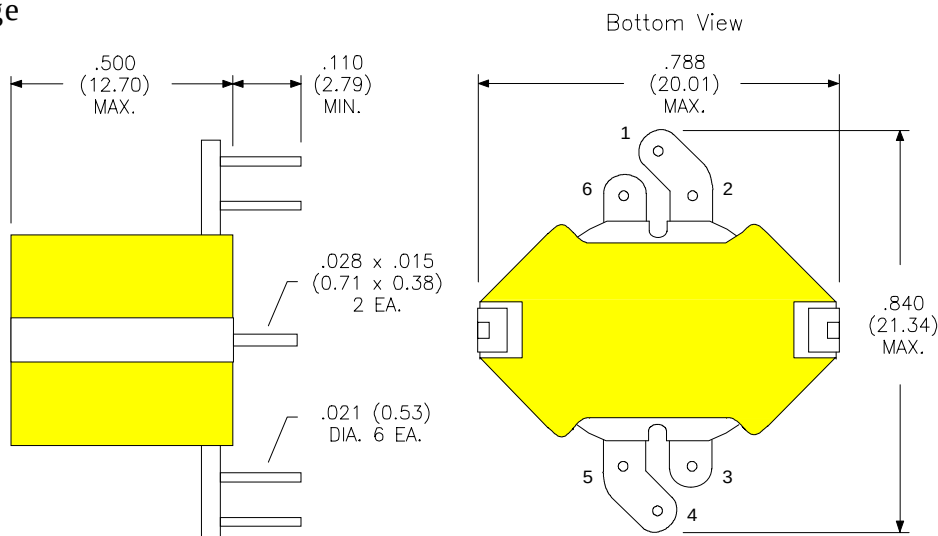


RM 5 Grid

Pin Position Grid 0.100"



RM 6 Package



RM 6 Grid

Pin Position Grid 0.100"

