

TA7270P
TA7271P

T-74-05-01

19W BTL AUDIO POWER AMPLIFIER.

The TA7270P/TA7271P are dual audio power amplifier for consumer applications. It is designed for high power, low distortion and low noise. It also contains various kind of protectors. It is suitable for car-audio power amplifier with high performance.

- $V_{CC}=13.2V, f=1kHz, R_L=4\Omega$

BTL	19W (Typ.)	THD=10%
	15W (Typ.)	THD=1%
DUAL	5.8W (Typ.)	THD=10%

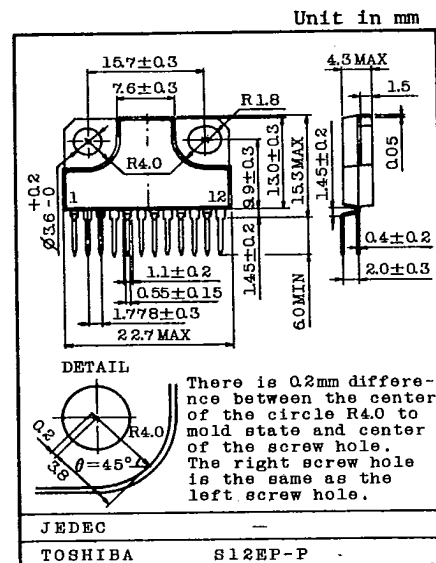
- $V_{CC}=13.2V, f=1kHz, R_L=4\Omega$

BTL	0.03% (Typ.)	P _{OUT} =4W, G _v =40dB
DUAL	0.06% (Typ.)	P _{OUT} =1W, G _v =52dB

- $V_{CC}=13.2V, R_L=4\Omega$

BTL	0.14mVrms (Typ.)	$G_V=40\text{dB}$, $R_g=0$, DIN NOISE: DIN 45405
DUAL	0.7 mVrms (Typ.)	$G_V=52\text{dB}$, $R_g=10\text{k}\Omega$, $BW=20\text{Hz} \sim 20\text{kHz}$

- Protector: Thermal Shut Down, Over Voltage Protection.
BTL-OCL DC Short Protection.



Weight : 4.04g (TYP.)

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MAXIMUM RATINGS ($T_a=25^{\circ}\text{C}$)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Peak Supply Voltage (0.2 sec)	V_{CC} surge	45	V
DC Supply Voltage	V_{CC} DC	25	V
Operating Supply Voltage	V_{CC} opr	18	V
Output Current (peak)	I_O (peak)	4.5	A
Power Dissipation	P_D	25	W
Operating Temperature	T_{opr}	$-30 \sim 75$	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	$-55 \sim 150$	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, $V_{CC}=13.2\text{V}$, $R_L=4\Omega$, $R_g=600\Omega$, $f=1\text{kHz}$, $T_a=25^{\circ}\text{C}$)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Quiescent Current	I_{CCQ}	2	$V_{IN}=0$	-	80	145	mA
BTL CONNECTION MODE	Output Power	$P_{OUT}(1)$	1 THD=10%	16	19	-	W
		$P_{OUT}(2)$	1 THD=1%	12	15	-	W
	Total Harmonic Distortion	THD(1)	1 $P_{OUT}=4\text{W}$, $G_v=40\text{dB}$	-	0.03	0.25	%
	Output Offset Voltage	V_{OFF}	1 $V_{IN}=0$	-	0	0.35	V
	Voltage Gain	$G_v(1)$	1 $V_{OUT}=0\text{dBm}$	-	40	-	dB
	Output Noise Voltage	$V_{NO}(1)$	1 $R_g=0$ DIN45405 Noise Filter	-	0.14	-	mVrms
	Ripple Rejection Ratio	R.R(1)	1 $f_{ripple}=100\text{Hz}$ $V_{ripple}=0\text{dBm}$	-	-52	-40	dB
DUAL MODE	Output Power	$P_{OUT}(3)$	2 THD=10%	5	5.8	-	W
	Total Harmonic Distortion	THD(2)	2 $P_{OUT}=1\text{W}$	-	0.06	0.30	%
	Voltage Gain	$G_v(2)$	2 $V_{OUT}=0\text{dBm}$	50	52	54	dB
	Voltage Gain Ratio	ΔG_v	2 $V_{OUT}=0\text{dBm}$	-1	0	1	dB
	Output Noise Voltage	$V_{NO}(2)$	2 $R_g=10\text{k}\Omega$ $BW=20\text{Hz} \sim 20\text{kHz}$	-	0.7	1.5	mVrms
	Ripple Rejection Ratio	R.R(2)	2 $f_{ripple}=100\text{Hz}$ $V_{ripple}=0\text{dBm}$	-	-52	-40	dB
	Cross Talk	C.T	2 $V_{OUT}=0\text{dBm}$	-	-57	-	dB
Input Resistance	R_{IN}	2	$f=1\text{kHz}$	-	33	-	$\text{k}\Omega$

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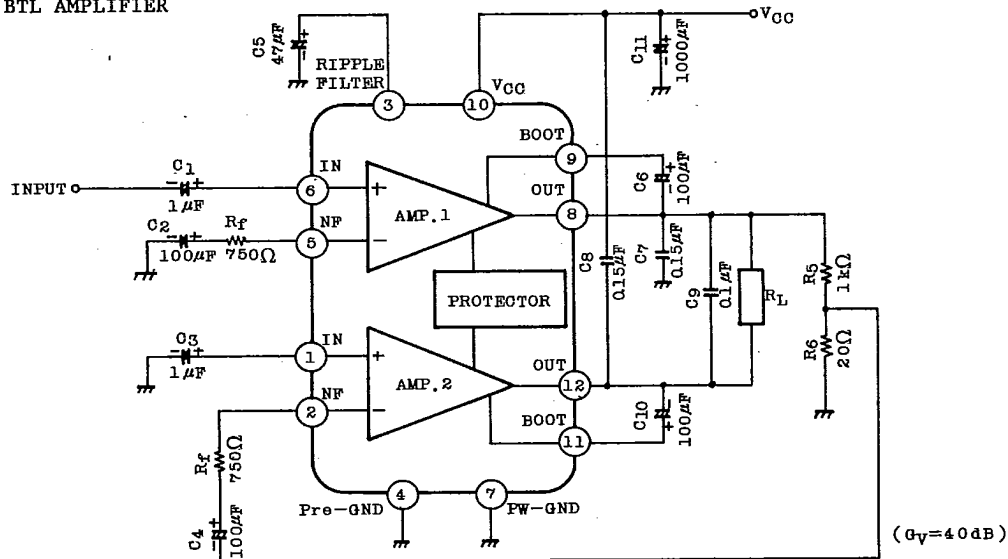
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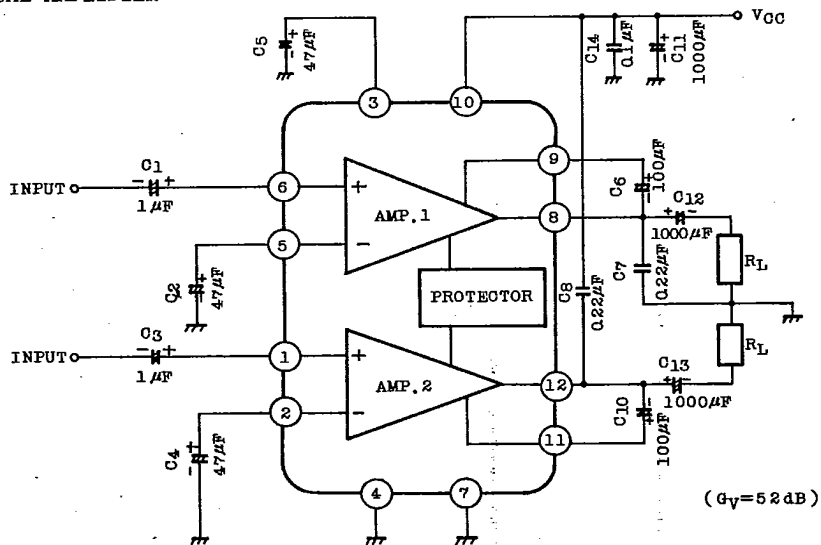
TEST CIRCUIT/APPLICATION CIRCUIT

TA7270P

(1) BTL AMPLIFIER



(2) DUAL AMPLIFIER



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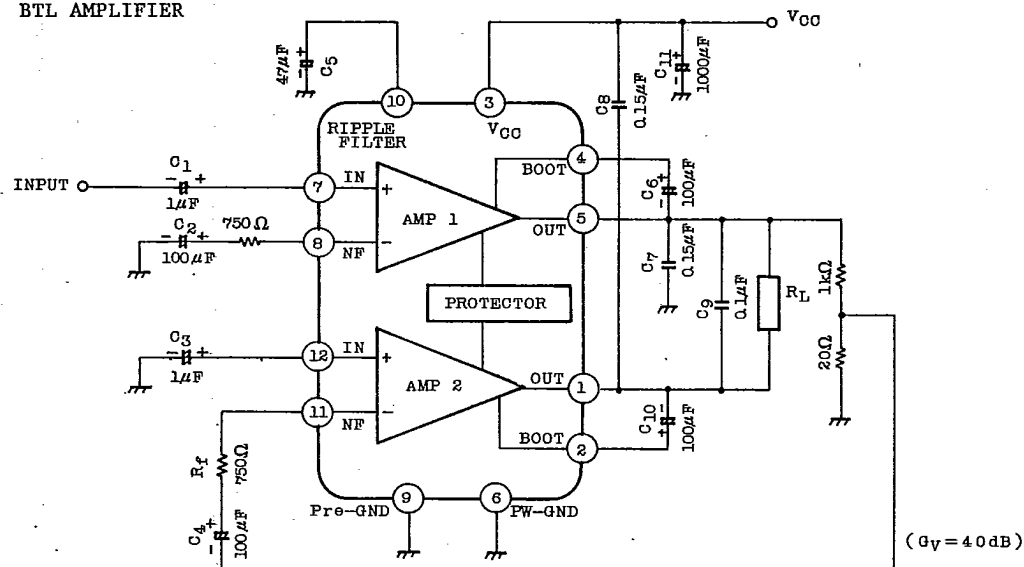
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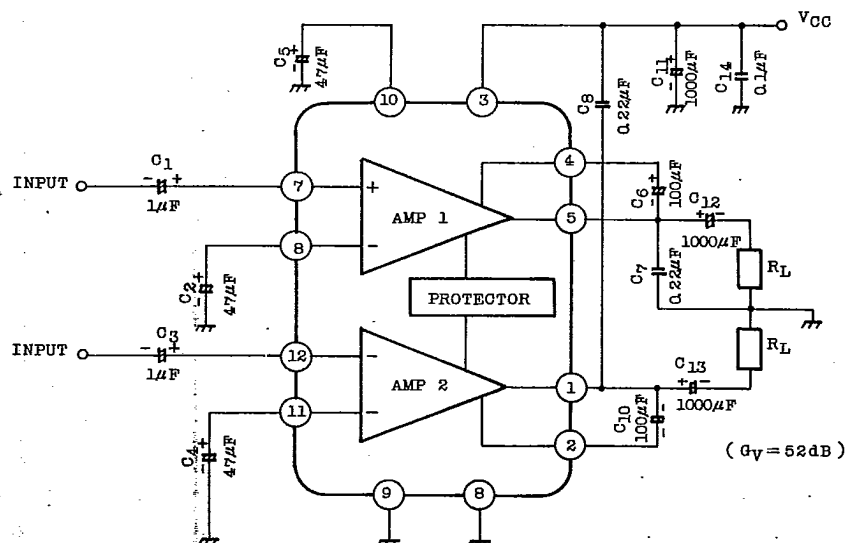
TEST CIRCUIT/APPLICATION CIRCUIT

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(1) BTL AMPLIFIER



(2) DUAL AMPLIFIER



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TYPICAL DC VOLTAGE OF EACH TERMINAL

($V_{CC}=13.2V$, $T_a=25^\circ C$, DUAL MODE TEST CIRCUIT)

TERMINAL No.	1	2	3	4	5	6	7	8	9	10	11	12
DC Voltage (V)	1.5	1.5	4.4	GND	1.5	1.5	GND	6.6	12.7	V_{CC}	12.7	6.6
	6.6	12.7	V_{CC}	12.7	6.6	GND	1.5	1.5	GND	4.4	1.5	1.5

APPLICATION INFORMATION

(This explanatory terminal number is for TA7270P)

1. VOLTAGE GAIN

(1) Dual Mode

The closed loop voltage gain G_v is determined by R_1 , R_2 , R_3 , R_4 and R_f .

$$G_v \div 20 \log \frac{R_2 + R_f + R_1}{R_f + R_1} + 20 \log \frac{R_3 + R_4}{R_4} \text{ (dB)}$$

when $R_f=0$, $G_v=52\text{dB(Typ.)}$

is given.

The recommended voltage gain is more than 40dB.

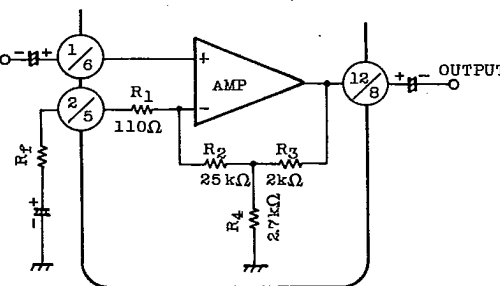


Fig 1

(2) BTL Mode

The recommended BTL connection amplifier is shown in Figure 2.

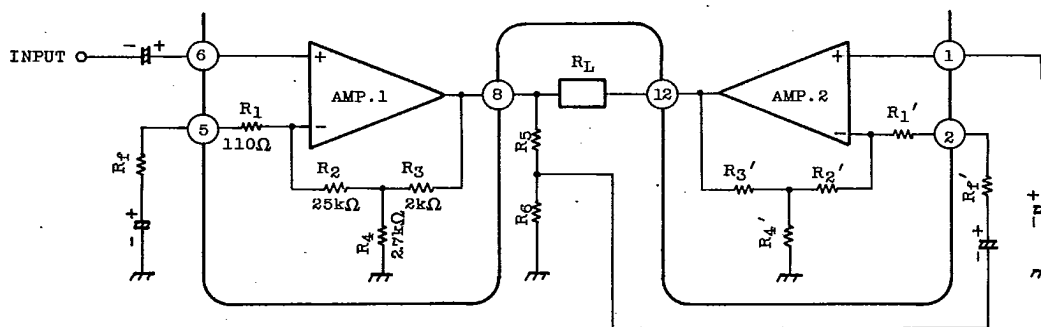


Fig. 2

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AMP.1 is noninverting amplifier and AMP.2 is inverting one. The output voltage is divided by resistors R_5 and R_6 .

This divided voltage is applied to inverting input of AMP.2. R_5 and R_6 are determined in the following equation.

$$\frac{R_5+R_6}{R_5} = 20 \log \frac{R_1'+R_f'+R_2'}{R_1'+R_f'} + 20 \log \frac{R_3'+R_4'}{R_4'} \dots G_v \text{ in Dual Mode}$$

The voltage gain in this circuit is 6dB higher than that in dual mode.

$$G_v = 20 \log \frac{R_1+R_f+R_2}{R_1+R_f} + 20 \log \frac{R_3+R_4}{R_4} + 6 \quad [\text{dB}]$$

In case of $R_f=0$

$$G_v=52+6=58[\text{dB}]$$

In case of $R_f=750\Omega$

$$G_v=34+6=40[\text{dB}]$$

2. MUTING

Audio muting can be accomplished by connecting pin ③ (ripple filter) to GND as shown in Fig.3.

Then, the bias circuits are cut off.

Amount of muting attenuation is more than 60dB.

Precaution in muting operation is as follows.

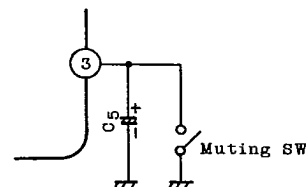


Fig.3

- (1) The recovery time at muting off depends on Capacitance C_2 , C_4 and C_5 in the test Circuit.
- (2) As this muting system is operated by the short-circuit of ripple filter : C_5 , the ripple rejection ratio becomes worse in a muting mode.
Note that some "POP-Noise" occur when bias is shut off with mute-on.

3. CAPACITOR C_7 , C_8

The purpose of capacitor C_7 , C_8 is to prevent oscillation.

These capacitors need to be small temperature coefficient. So celamic capacitor is unsuitabel.

A voltage gain less than 40dB results occasionally in a parastic oscillation.

Stability for parastic oscillation is promoted by connecting capacitor of 500~1000pF between pin ① and pin ② (pin ⑤ and pin ⑥).

The additional capacitors are recommended to be inserted.

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4. PRECAUTION AT PRINT BOARD DESIGN

- (1) Print Pattern board should be designed in consideration of stability for parastic oscillation.

The following parts-layout is recommended.

- 1st. Capacitors C6 and C10 are spaced most close to the output pin.
 - 2nd. Capacitor C7 or C8 is spaced close to the output pin next to C6 and C10.
 - 3rd. Capacitor C9 is spaced close to it next to C7 and C8.
 - 4th. Capacitor C11 is spaced close to it next to C9.
- (2) Input line (pin ⑥) and PW-GND line (pin ⑦) should not be spaced in parallel. In the paralled layout, output current signal in PW-GND line is bed back to input line by electromagnetic coupling. Then it deteriorates the total harmonic distortion, especially at high audio frequency region.
- (3) Undesirable terminating of capacitors deteriorates "pop" noise or THD. Capacitors C2, C4 and C5 should be terminated to Pre-GND (pin ④). Capacitors C7, C11, and C14 should be terminated to PW-GND (pin ⑦).
- (4) It is recommended to refer the standard print board.

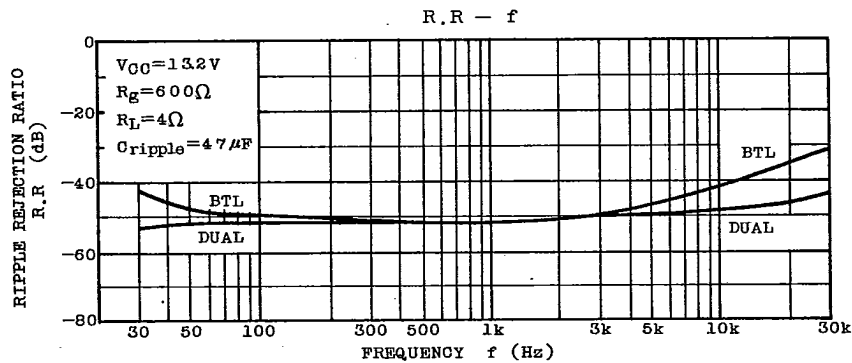
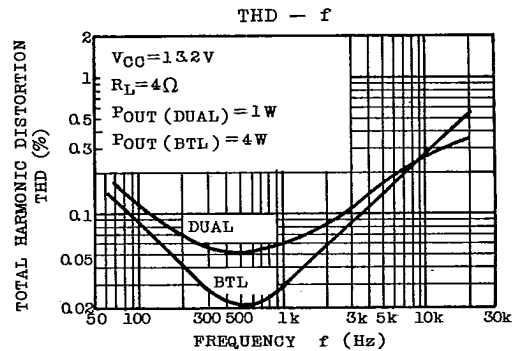
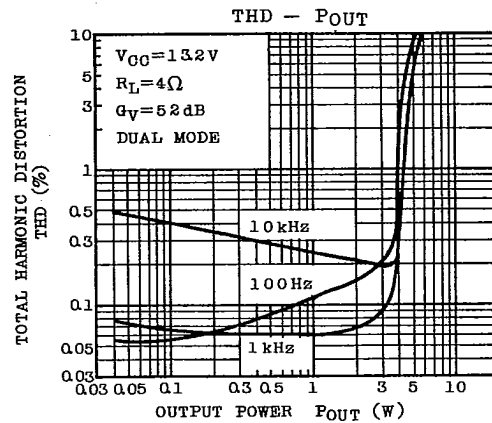
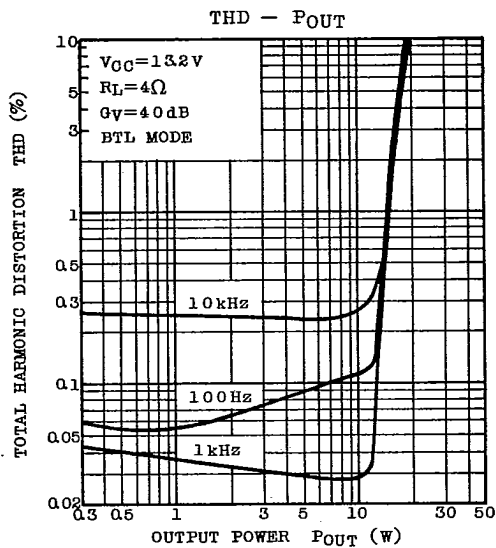
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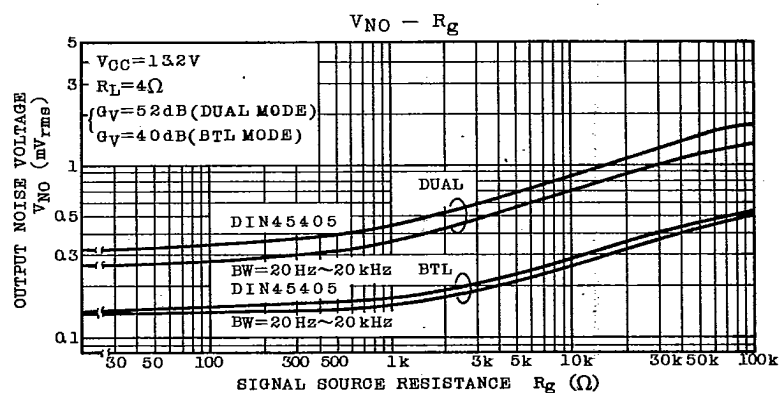
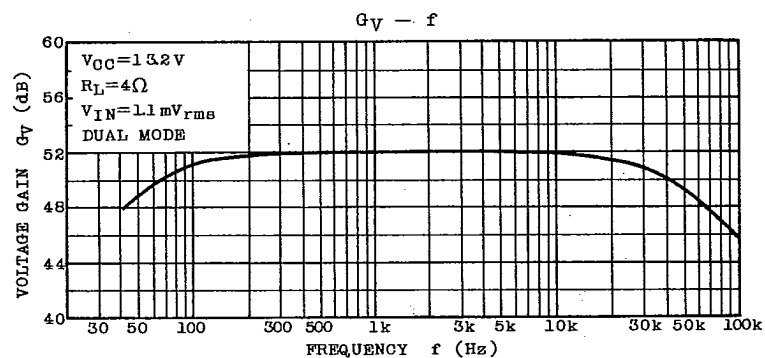
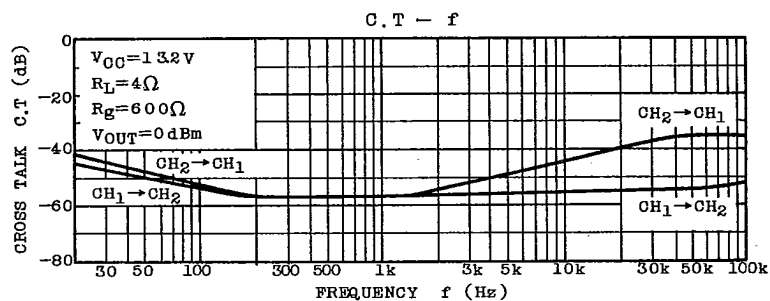
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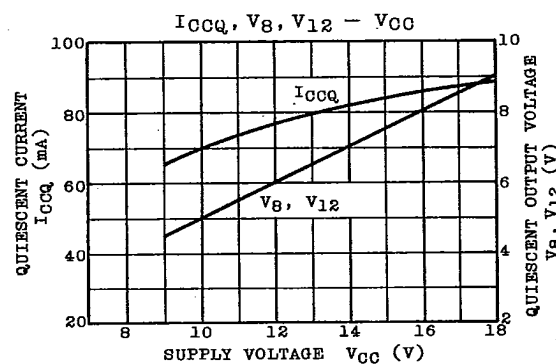
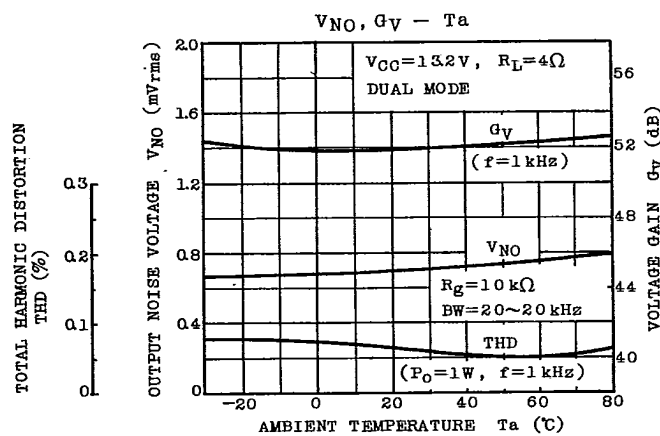
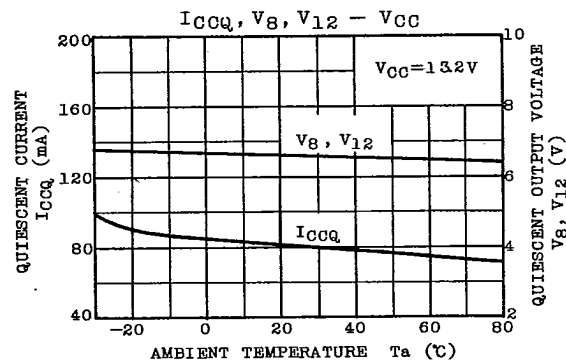
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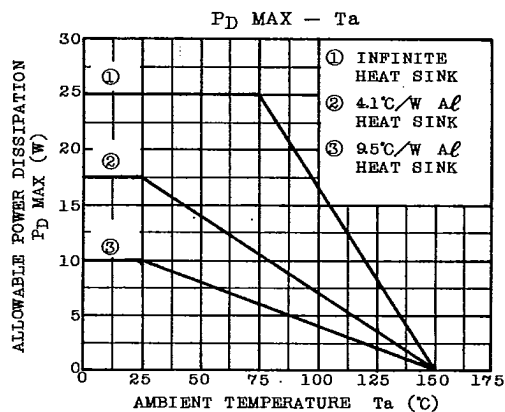
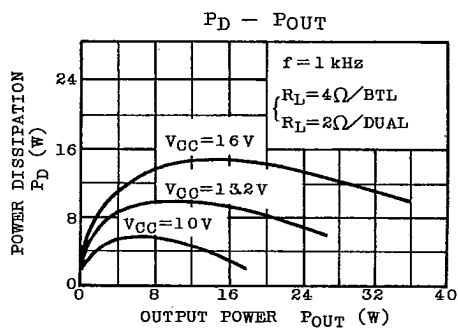
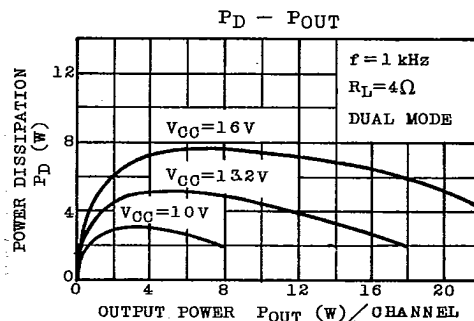
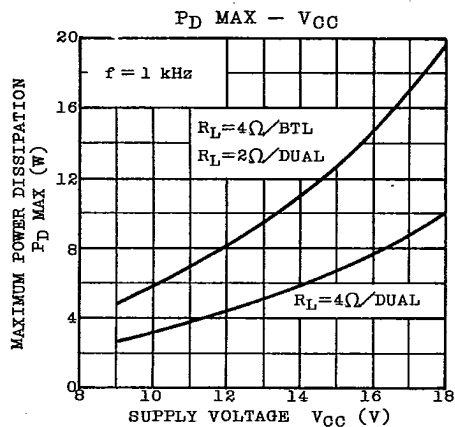
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