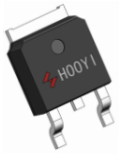
P-Channel Enhancement Mode MOSFET

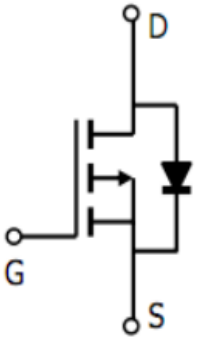
Feature Description

- -100V/-20A
  $R_{DS(ON)}$= 84mΩ(typ.)@V_{GS} = -10V
  $R_{DS(ON)}$= 96.5mΩ(typ.)@V_{GS} = -4.5V
- 100% Avalanche Tested
- Reliable and Rugged
- Lead- Free Devices Available
  (RoHS Compliant)

Pin Description



TO-252-2L



P-Channel MOSFET

Applications

- Power management in notebook computer
- Portable equipment and battery powered systems
- DC-DC Converters

Ordering and Marking Information

D HY12P10 YYXXXJWW G	Package Code D: TO-252-2L Date Code YYXXX WW Assembly Material G:Lead Free

Note: HOOYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate Termi-Nation finish;which are fully compliant with RoHS. HOOYI lead-free products meet or exceed the lead-Free require-ments of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HOOYI defines “Green” to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HOOYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr-oduct and/or to this document at any time without notice.

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage		-100	V
V_{GSS}	Gate-Source Voltage		±20	V
T_J	Maximum Junction Temperature		150	℃
T_{STG}	Storage Temperature Range		-55 to 150	℃
I_S	Source Current-Continuous(Body Diode)	Tc=25°C	-20	A
Mounted on Large Heat Sink				
I_{DM}	Pulsed Drain Current *	Tc=25°C	-80	A
I_D	Continuous Drain Current	Tc=25°C	-20	A
		Tc=100°C	-13	A
P_D	Maximum Power Dissipation	Tc=25°C	39	W
		Tc=100°C	16	W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3.2	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		110	°C/W
E_{AS}	SinglePulsed-Avalanche Energy ***	L=0.3mH	168	mJ

Note: * Repetitive rating；pulse width limited by max.junction temperature.
** Surface mounted on 1in2 FR-4 board.
*** Limited by $T_J$max , starting T_J=25°C, L = 0.3mH, R_G= 25Ω, V_{GS} =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HY12P10			Unit
			Min	Typ.	Max	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_{DS}= -250μA	-100	-	-	V
I_{DSS}	Drain-to-Source Leakage Current	V_{DS}= -100V,V_{GS}=0V	-	-	-1	μA
		T_J=55°C	-	-	-5	μA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}= -250μA	-1	1.8	-3	V
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±16V,V_{DS}=0V	-	-	±10	uA
$R_{DS(ON)}$*	Drain-Source On-State Resistance	V_{GS}= -10V,I_{DS}= -10A	-	84	96	mΩ
		V_{GS}= -4.5V,I_{DS}=-10A	-	96.5	107	
Diode Characteristics						
V_{SD}*	Diode Forward Voltage	I_{SD}= -10A,V_{GS}=0V	-	-0.85	-1.3	V
t_{rr}	Reverse Recovery Time	I_{SD}=-10A,dI_{SD}/dt=100A/μs	-	30	-	ns
Q_{rr}	Reverse Recovery Charge		-	42	-	nC

Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HY12P10			Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	-	8	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V,	-	2186	-	pF
C_{oss}	Output Capacitance	V_{DS}= -25V,	-	79	-	
C_{rss}	Reverse Transfer Capacitance	Frequency=1.0MHz	-	17	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}= -50V,R_G=3.3Ω, I_{DS}= -10A,V_{GS}= -10V	-	19	-	ns
T_r	Turn-on Rise Time		-	25	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	62	-	
T_f	Turn-off Fall Time		-	27	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge	V_{DS} = -80V, V_{GS}= -10V, I_D= -10A	-	60	-	nC
Q_{gs}	Gate-Source Charge		-	12	-	
Q_{gd}	Gate-Drain Charge		-	19	-	

Note: *Pulse test，pulse width ≤ 300us，duty cycle ≤ 2%

Typical Operating Characteristics

Figure 1: Power Dissipation

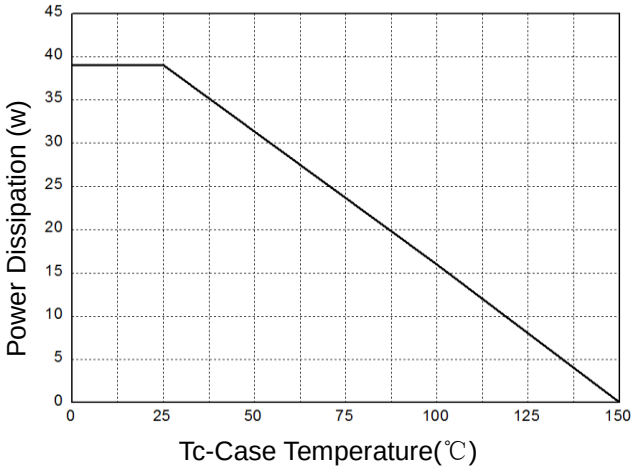


Figure 2: Drain Current

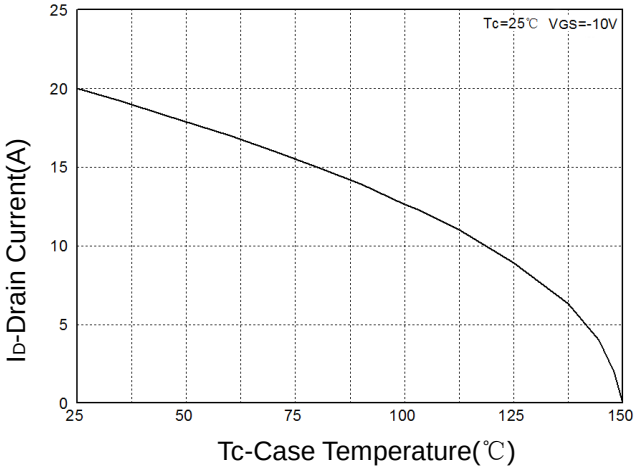


Figure 3: Safe Operation Area

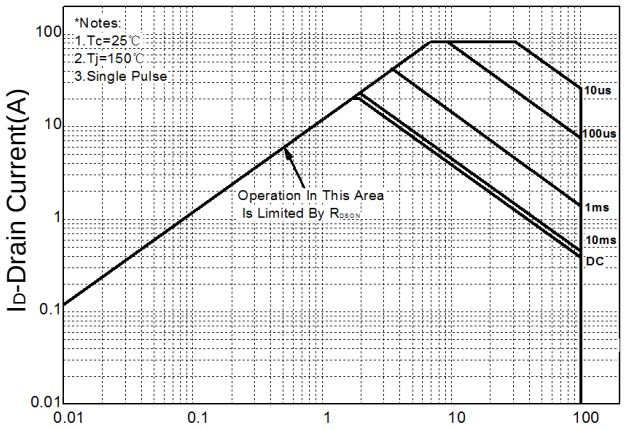
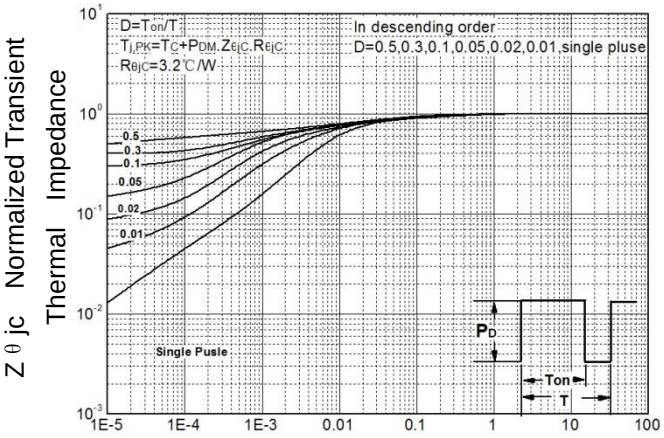


Figure 4: Thermal Transient Impedance



Maximum Effective Transient Thermal Impedance, Junction-to-Case

Figure 5: Output Characteristics

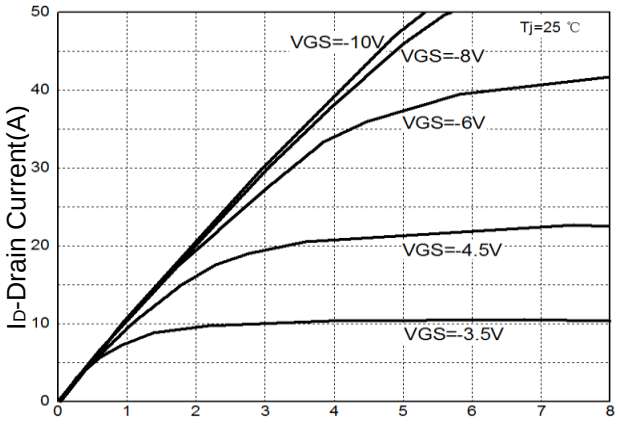
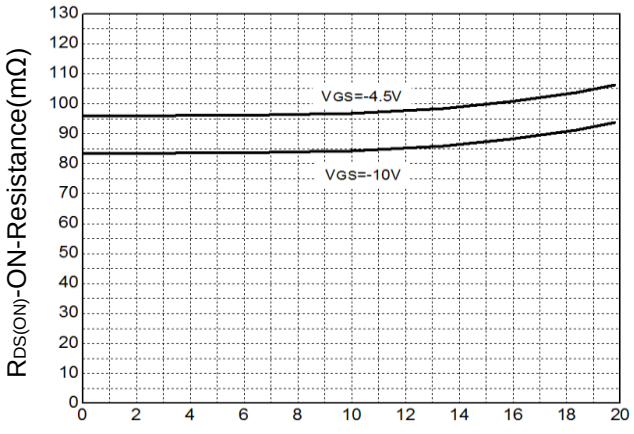


Figure 6: Drain-Source On Resistance



Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

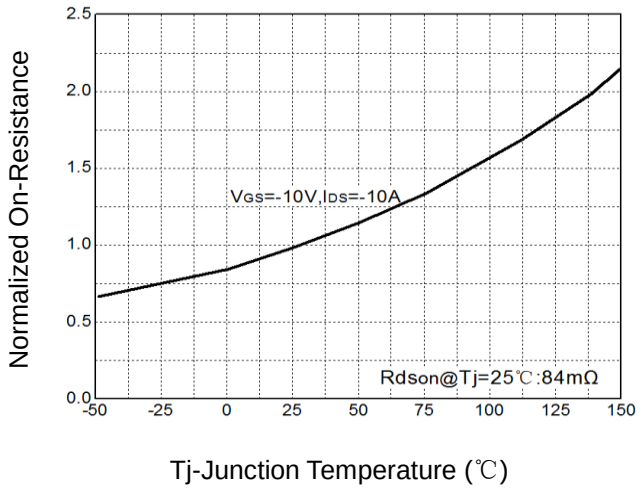


Figure 8: Source-Drain Diode Forward

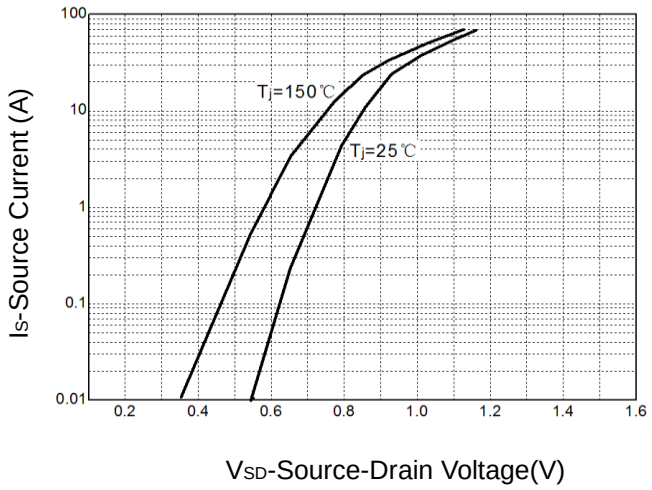


Figure 9: Capacitance Characteristics

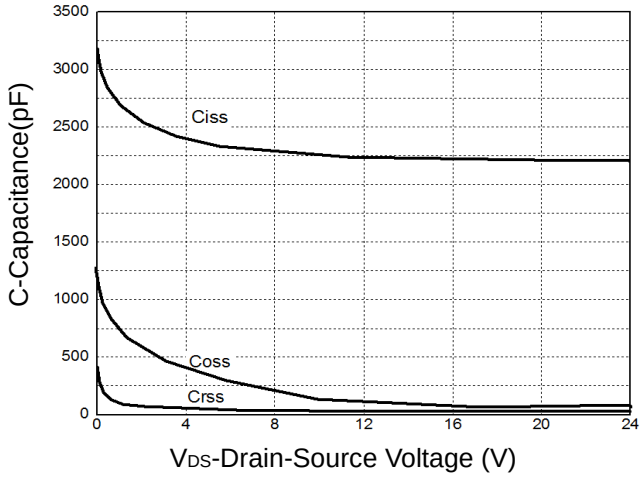
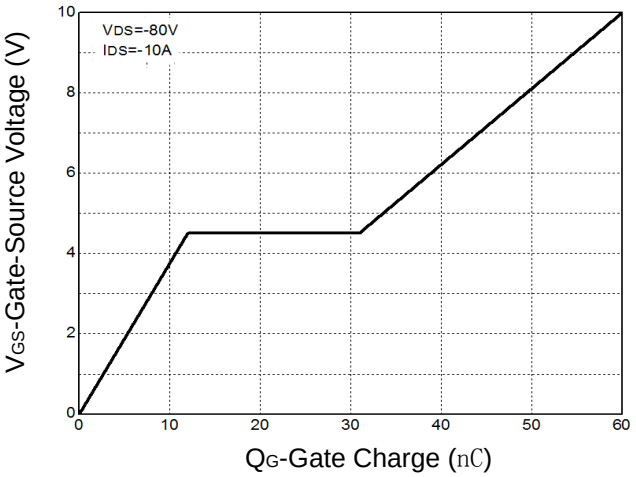
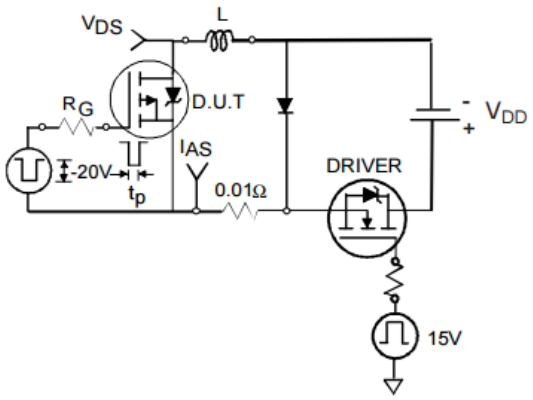
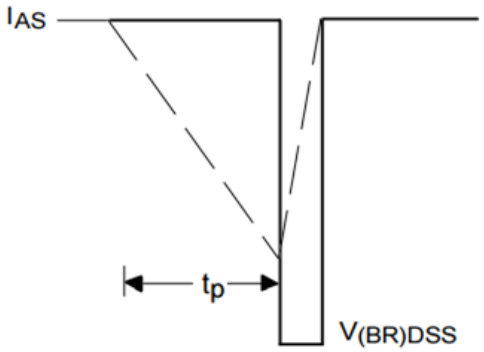


Figure 10: Gate Charge Characteristics

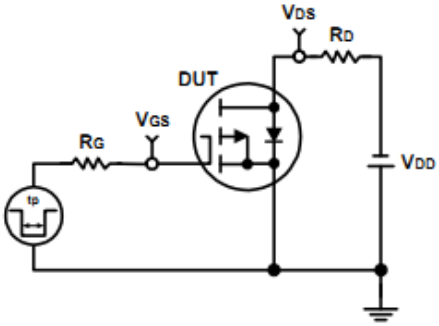


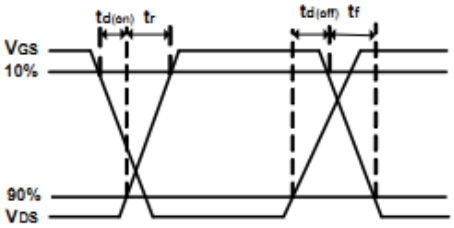
Avalanche Test Circuit and Waveforms



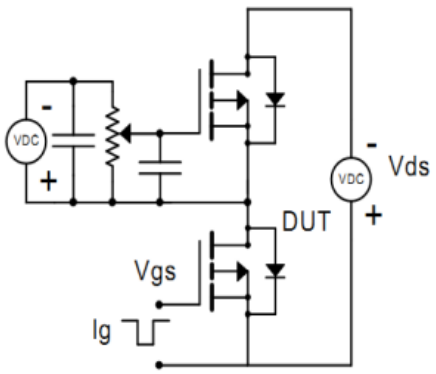


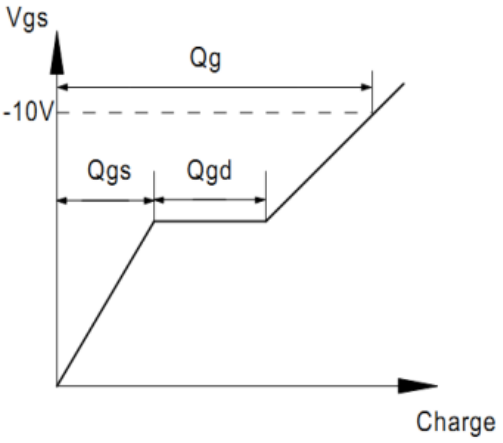
Switching Time Test Circuit and Waveforms





Gate Charge Test Circuit and Waveforms



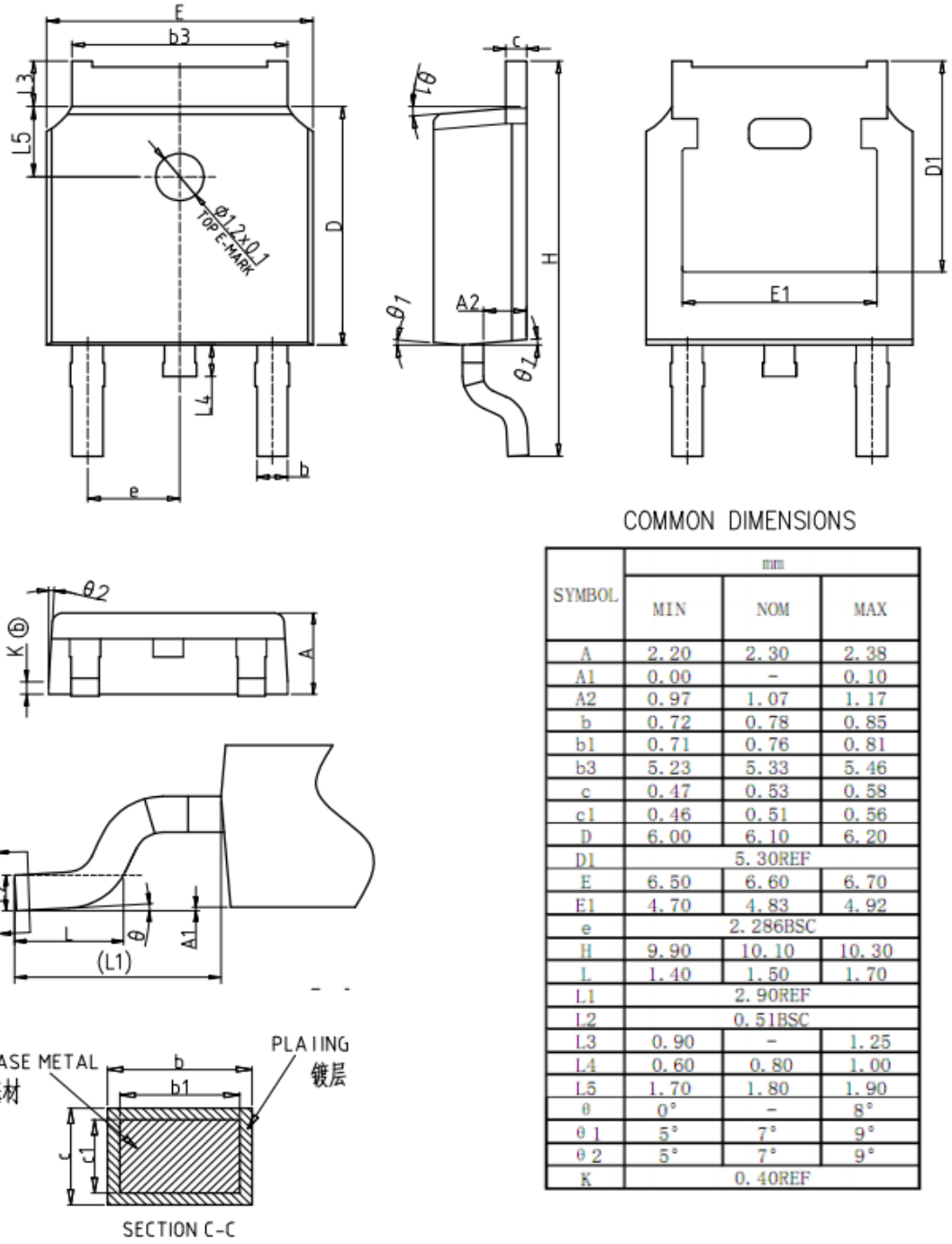


Device Per Unit

Package Type	Unit	Quantity
TO-252-2L	Tube	75

Package Information

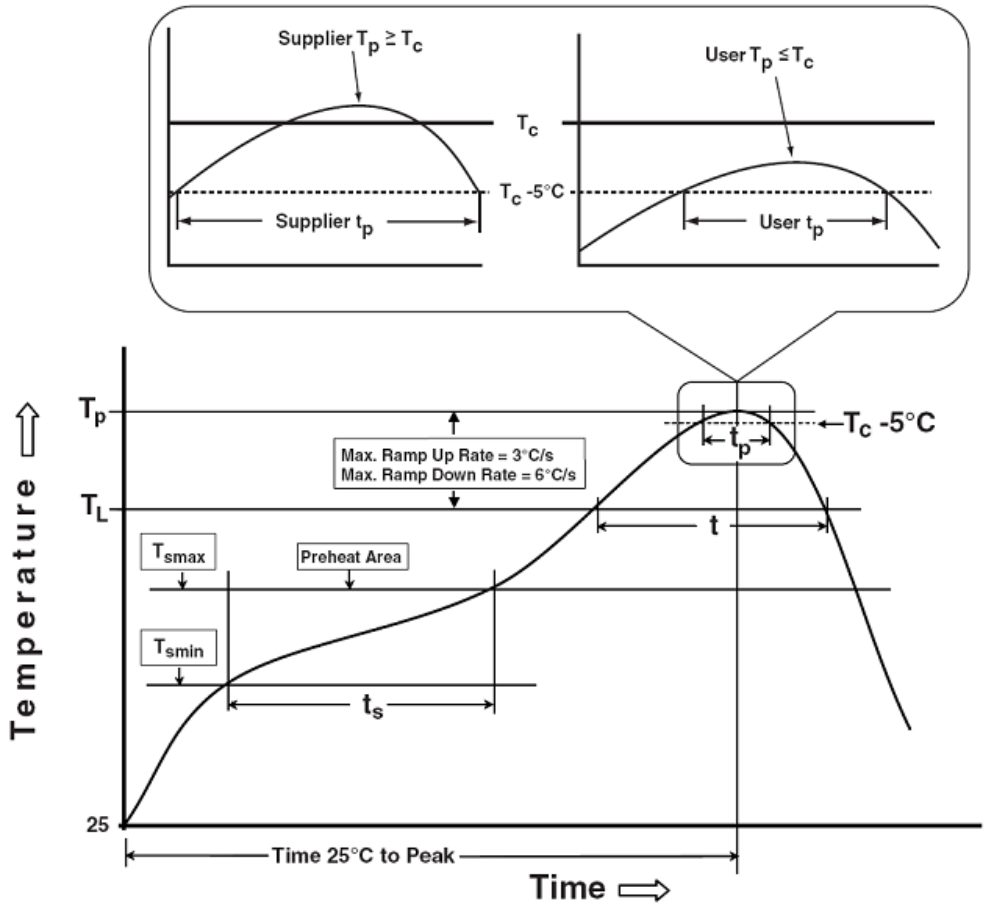
TO-252-2L



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	–	0.10
A2	0.97	1.07	1.17
b	0.72	0.78	0.85
b1	0.71	0.76	0.81
b3	5.23	5.33	5.46
c	0.47	0.53	0.58
c1	0.46	0.51	0.56
D	6.00	6.10	6.20
D1	5.30REF		
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e	2.286BSC		
H	9.90	10.10	10.30
L	1.40	1.50	1.70
L1	2.90REF		
L2	0.51BSC		
L3	0.90	–	1.25
L4	0.60	0.80	1.00
L5	1.70	1.80	1.90
θ	0°	–	8°
θ1	5°	7°	9°
θ2	5°	7°	9°
K	0.40REF		

Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (Tsmin to Tsmax) (t_s)	 100 °C 150 °C 60-120 seconds	 150 °C 200 °C 60-120 seconds
Average ramp-up rate ($T_{smax}$to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	SeeClassification Tempin table 2
Time (t_P)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate ($T_p$to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ⩾350
＜2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ 350-2000	Volume mm³ ⩾2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
⩾2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, Bias @ 125°C
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

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