F72815 Datasheet

Synchronous Buck PWM DC-DC Controller with External Reference Voltage Input

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Release Date: Aug, 2007
Version: V0.14

F72815 Datasheet Revision History

Version	Date	Page	Revision History
0.10	Jun, 2006		Preliminary version
0.11	Dec, 2006	8	Adjust Vref description
		11	Add application circuit
0.12	Jul, 2007	8	Adjustable input reference voltage description
		9	Add Typical characteristics
		13	Update company address
0.13	Jul, 2007	2	Pin configuration
			Shut down level: 0.15V
0.14	Aug, 2007	2	Pin description, VREF
		4	Power Good maximum value: 4.4V
			Feedback under voltage trip: 0.5V
			Remove free running frequency test mode
			Remove reference voltage
		14	Add SOP-8 EP dimension

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1. General Description

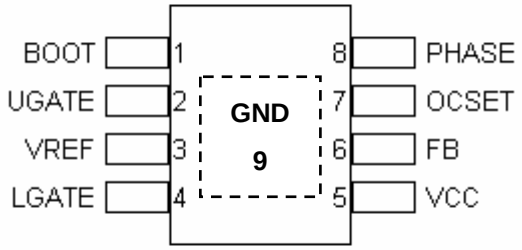
The F72815 is a high-efficiency synchronous buck PWM controller with an adjustable reference voltage input to provide an option for over voltage function, which generates logic-supply voltages in PC based systems. This high-performance , single output device includes internal soft-start, frequency-compensation networks, power good signal with delay, and integrates all of the control, output adjustment, monitoring and protection functions into a single package. This device operates at a fixed frequency, 300K Hz and provides an optimum compromise between efficiency, external component size, and cost. Adjustable over-current protection (OCP) monitors the voltage drop across the RDS (ON) of the lower MOSFET for synchronous buck PWM DC-DC controller. The over-current function cycles the soft-start in 4-times hiccup mode to provide fault protection, and in a routine hiccup mode for under-voltage protection.

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2. Feature List

- Provides one synchronous rectified buck DC-DC PWM controller
  - Fast transient response (high bandwidth error amplifier; full 0~100% duty cycle)
  - Fixed operation frequency : 300KHz
  - Single loop voltage-mode control
  - Few external components
  - Output range adjustable down to 0.8V
- Operates under 5V to 12V input
- Drives all low cost N-channel MOSFETs
- Uses MOSFET $R_{DS(ON)}$ as over current monitor, and no current sense resistor is required
- 0.8V internal reference voltage
  - ±2% accuracy over line, load and temperature
- Internal soft-start (typically 3 ms)
- Internal loop compensation
- Under voltage fault monitoring and protection
- Over voltage protection
- Adaptive Non-Overlapping Gate Driver
- Adjustable Reference Voltage Input for Over Voltage Function, Input Range: 0.78~1.2V, Enable Level: under 1.5V
- 8-SOP package (150 mil)

3. Pin Configuration



4. Pin Description

Pin No	Pin Name	Description
1	**BOOT**	Bootstrap supply pin for the upper gate driver. Connect the bootstrap capacitor between BOOT pin and the PHASE pin. The bootstrap capacitor provides the charge to turn on the upper MOSFET.
2	**UGATE**	Higher gate drive output. Connect this pin to gate of the high side MOSFET
3	**VREF**	Reference voltage input. The input range is 0.6V~1.8V.
4	**LGATE**	Lower gate drive output. Connect this pin to gate of the low side MOSFET
5	**VCC**	IC supply voltage. This voltage is monitored for power-on-reset purpose.
6	**FB**	Inverting input of the error amplifier used to compensate the feedback loop of the PWM controller.
7	**OCSET/SD**	This is multi-function pin. Connecting a resistor ROCSET from this pin to PHASE, and an internal 40uA current source from VCC. OCSET voltage can sense loading status when low side MOSFET turn on. When OCSET voltage is under 0.15V, shut down (SD) function will be enabled.
8	**PHASE**	Connect this pin to the source of the upper MOSFET and the drain of the lower MOSFET.

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9	**GND**	Power ground pin for the chip

5. Electrical Characteristic

Absolute Maximum Ratings

PARAMETER	SYMBOL	RATINGS	UNIT
IC supply voltage	VCC	15	V
PHASE to GND	VPHASE – GND	15	V
BOOT to PHASE	VBOOT - VPHASE	15	V
BOOT to GND	VBOOT - GND	VCC + 15	V
PWM controller outputs	UGATE	(VPHASE - 0.3) to (VBOOT + 0.3)	V
PWM controller outputs	LGATE	(GND - 0.3) to (VCC + 0.3)	V
Feedback voltages	FB	(GND - 0.3) ~ 7	V
ESD classification	HBM	2	kV
※ Maximum junction temperature (plastic package)	T_j	150	°C
※ Maximum storage temperature	T_{STO}	-65 ~ 150	°C
※ Maximum lead temperature (soldering 10s)		260	°C

Note: **If ICs are stressed beyond the limits listed in the "absolute maximum ratings", they may be permanently destroyed. These are stress ratings only and functional operations of the device at these or any other condition beyond those indicated under "recommended operating conditions" are not implied. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.**

Recommended Operating Conditions

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	VCC	5 ± 5%,12 ± 10%	V
Ambient Temperature		0 ~ 70	℃
Junction Temperature		0 ~ 125	℃

Package thermal information

PARAMETER	SYMBOL	SOIC	UNIT
Thermal resistance junction-ambient	Rth_ja	133(R), 160(O)	°C/W

DC and AC electrical characteristics (VCC = 12V, TA = 25℃)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
VCC SUPPLY CURRENT/Regulated Voltage						
Nominal supply current 5VCC	I_{CC}	UGATE, LGATE and DRIVE2 open		6	15	mA

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
POWER-ON RESET						
Rising VCC threshold				4.1	4.4	V
Falling VCC threshold				3.6	3.75	V
OSCILLATOR AND Protection						
Free running frequency	F_{OSC}		250	300	350	kHz
※ Ramp Amplitude	$\triangle V_{OSC}$			1.5		V_{P-P}
FB Under Voltage Trip		FB Falling	0.48	0.5	0.52	V
OCSET Current Source	I_{OCSET}		35	40	45	uA
Soft-start interval	T_{SS}		1	3	6	ms
Dead time	T_{DT}	2V to 2V	20		100	ns
PWM CONTROLLER GATE DRIVERS						
Upper Drive Source	R_{UGATE}	VDS = 1V, VGS = 12V,		5	10	Ω
Upper Drive Sink	R_{UGATE}	VDS = 1V, VGS = 12V		4	8	Ω
Lower Drive Source	R_{LGATE}	VDS = 1V, VGS = 12V		4	8	Ω
Lower Drive Sink	R_{LGATE}	VDS = 1V, VGS = 12V		3	5	Ω
Error Amplifier						
※ E/A Transconductance	gm			2.5		u A/V
※ DC Gain	A0			85*		dB

※: Design Guarantee

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6. Block Diagram

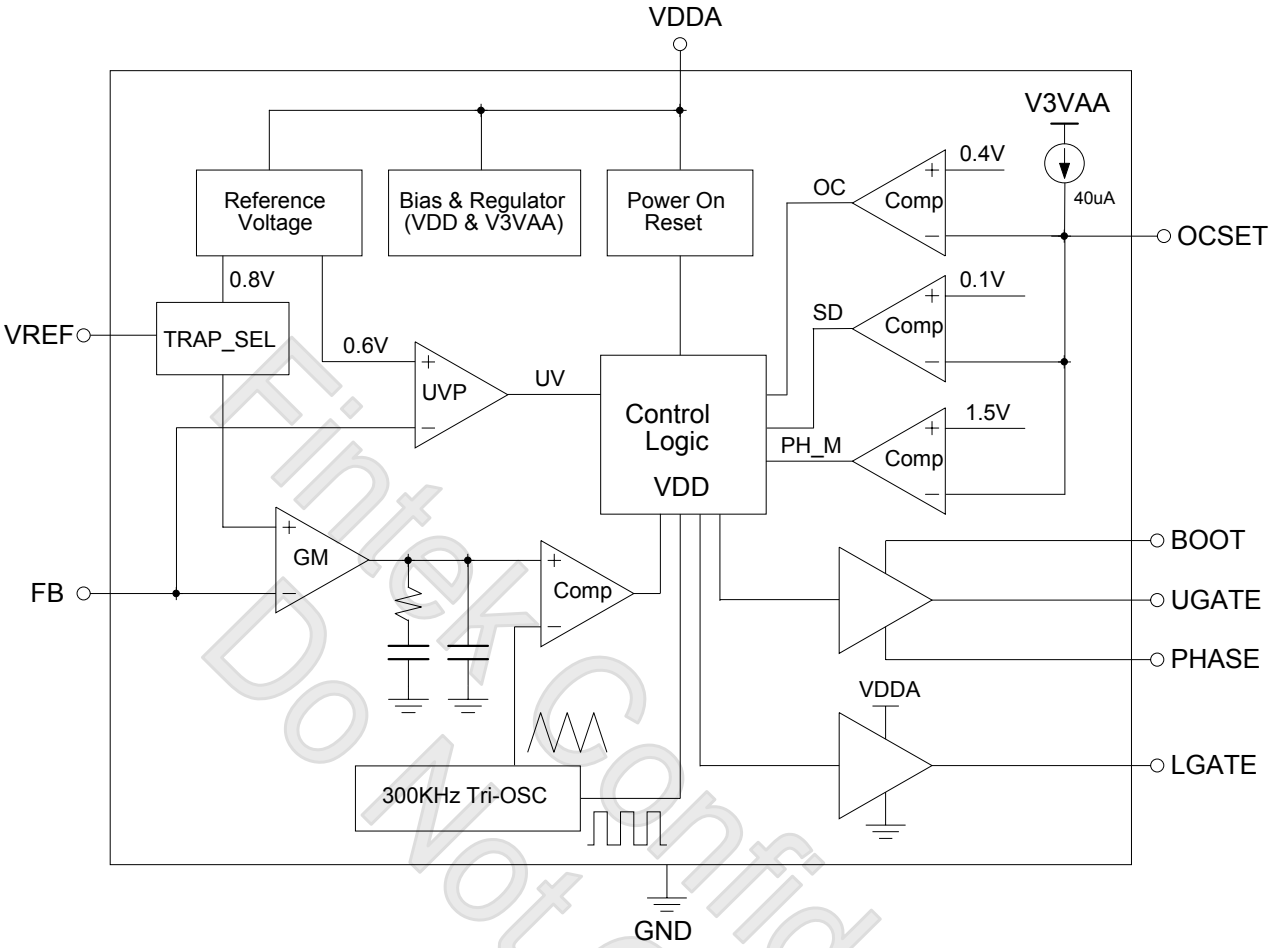


Figure 1: Block Diagram

7. Function Description

The F72815 is a single PWM DC-DC converter controller for application which requires high current such as motherboard, graphic card etc. The F72815 uses voltage mode PWM controller with fast transient response (driving two external N-channel MOSFETs in a synchronous rectified buck converter topology) and integrates some functions such as adjustable reference voltage input for overclock, soft-start, over current and short circuit protections etc. Over current protection is achieved by monitoring the PHASE negative voltage when heavy loading without the need of a current sensing resistor and short circuit condition is detected through FB pin. If over current condition occurs, the F72815 will initiate the soft start cycle. After two cycles and the fault condition persist, the controller will go into shut down status. In shut down status, both gate drive signals will be low. To restart the controller, either recycling VCC supply or momentarily pulling the OCSET pin below 0.15V. If under voltage condition occurs, it will keep hiccup mode in

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this event.

7.1 Bootstrap Operation

In the single power supply system, UGATE is powered by an external bootstrap circuit as Figure 3. The Boot capacitor, C_{BOOT} is charged and generated a floating reference voltage at PHASE pin. Typically, a 0.1uF C_{BOOT} is enough for the most of applications. The C_{BOOT} capacitor is refreshed and charged to a voltage of "VCC – Diode Drop" while low side power MOS turns on.

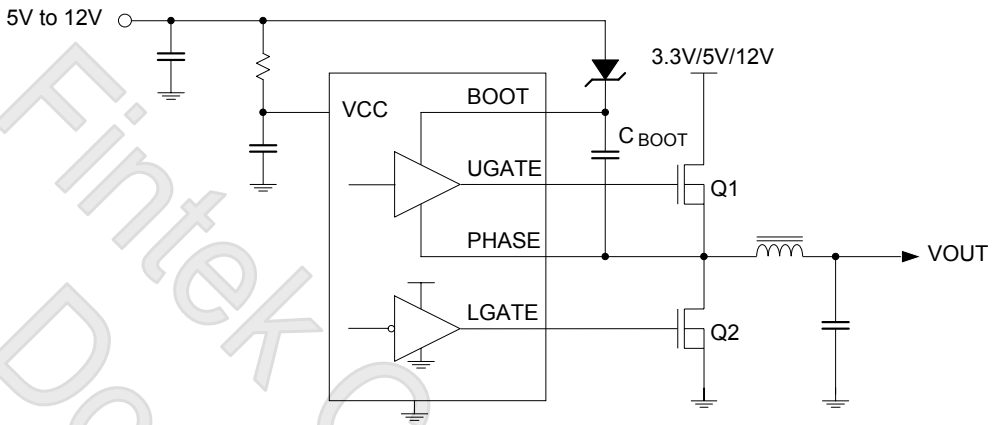


Fig 2. Power Supply Operation.

7.2 Power On Reset

The F72815 Power-on-reset monitors VCC supply voltage and input voltage at OCSET pin. POR level is 4.1V with 0.5V hysteresis at VCC voltage and 0.15V at OCSET voltage, The POR function initiates soft-start operation and resets control logic after the supply voltages exceeds its POR threshold voltage.

7.3 Soft-Start

When the POR function initiates the digital soft-start sequence, PWM error amplifier reference inputs are forced to track a voltage level proportional to the soft-start voltage. As the soft-start voltage slews up, the PWM comparator regulates the output relative to the tracked soft-start voltage, and slowly charges the output capacitor(s). In the digital soft-start sequence initiated by the POR function, the soft start function is used to prevent surge current from power supply input during power on. The soft start duration is 3mS in typical case.

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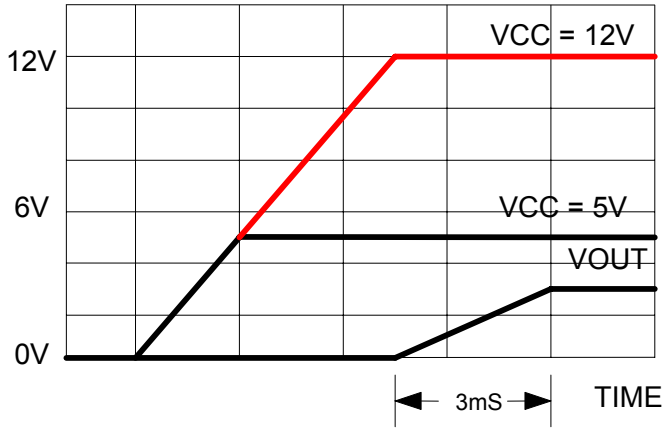


Figure 3 : Soft start duration

7.4 Over-current Protection

To sense the low-side MOSFET's RDS (ON) to set over-current trip point. Connecting a resistor (ROCSET) from this pin to PHASE to set the over-current trip point, ROCSET, an internal 40μA current source, and the lower MOSFET on resistance, RDS (ON), sets the converter over-current trip point (IOCSET) according to the following equation:

$$I_{OCSET} = \frac{40uA \times R_{OCSET} - 0.4V}{R_{DS(ON)} \text{ of the lower MOSFET}}$$

7.5 Under-voltage Protection

If the FB voltage drops under 0.5V, a fault signal is generated. When under voltage condition occurs, it will keep hiccup mode in this event.

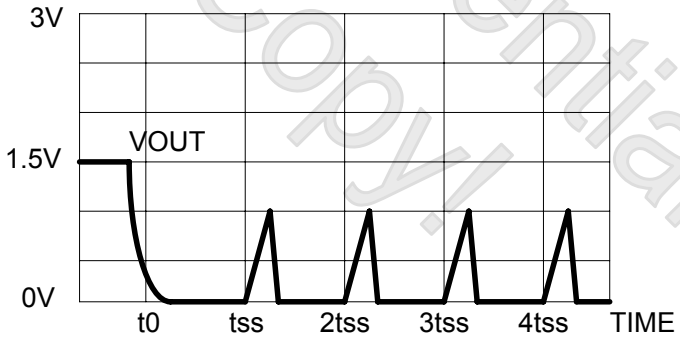


Figure 4 : Under voltage event occur

But UV_FB has some differences from OC, it always trigger VIN power sensing after 4 times hiccup.

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7.6 Shutdown

It can shut down the F72815 PWM controller to pull low the OCSET pin under 0.15V.

7.7 VIN Sense

The F72815 Vin power is sensed by OCSET voltage at first soft start event, when soft start event occurs. UGATE pulse will be enabled, and the F72815 detects the first ten UGATE high pulse. If there are four PH_M pulse during the ten pulse period, it means Vin ready, If Vin is ready, VOUT will be ramped up in next soft start event. Otherwise, we will check Vin voltage at the next soft start event.

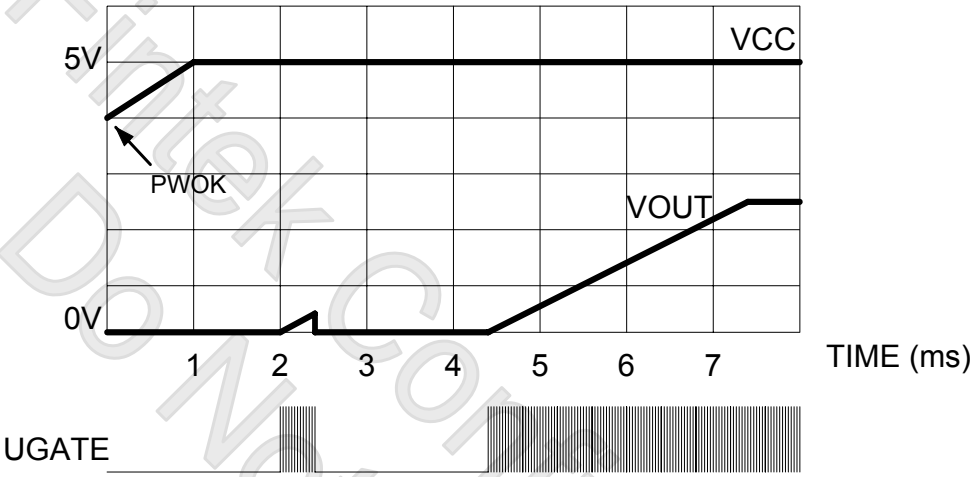


Figure 5

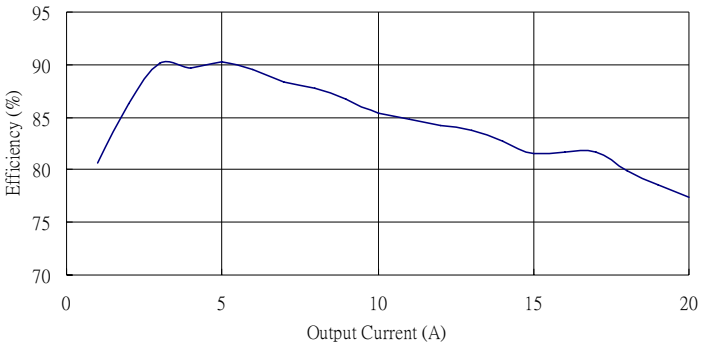
7.8 Adjustable Reference Voltage Input

The F72815 supports external reference voltage input to replace the internal feedback reference voltage. The output voltage can be adjusted according to the different external reference voltage. The high level of input reference voltage is over 0.6V, and then the F72815 will be active to track the reference voltage. The maximum of the reference voltage input is 1.8V.

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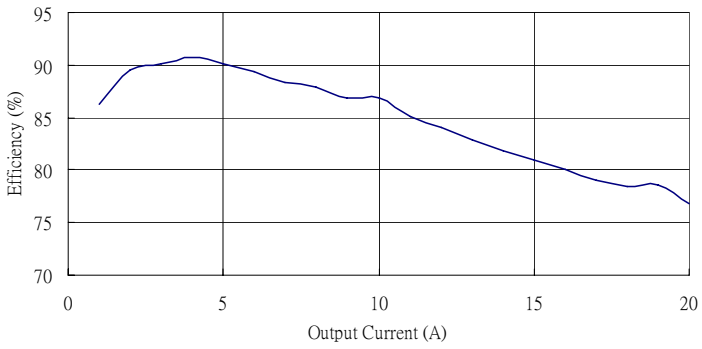
8. Typical Characteristic

Efficiency V.S. Output Current



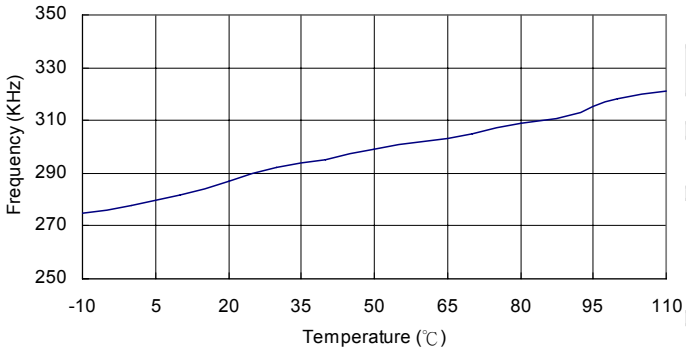
V_{CC}=12V, V_{IN}=5V, V_{OUT}=2.5V

Efficiency V.S. Output Current

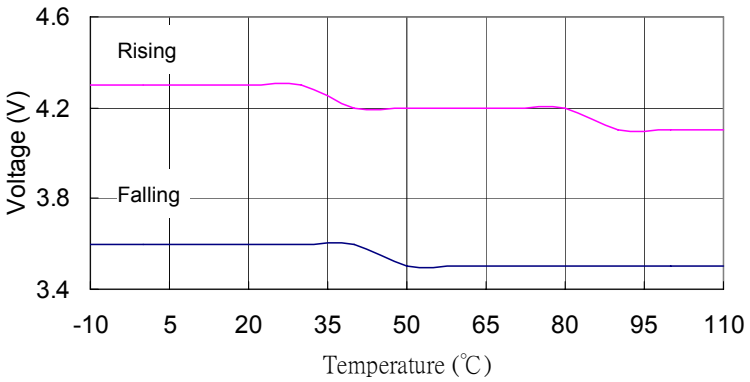


V_{CC}=5V, V_{IN}=5V, V_{OUT}=2.5V

Frequency V.S. Temperature

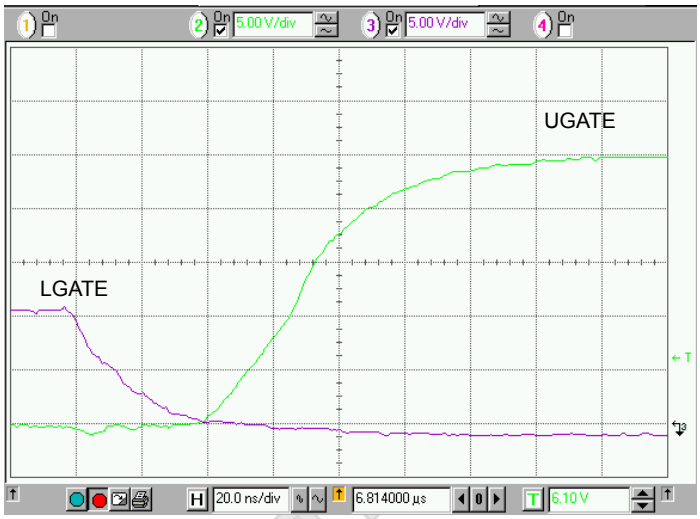


POR V.S. Temperature



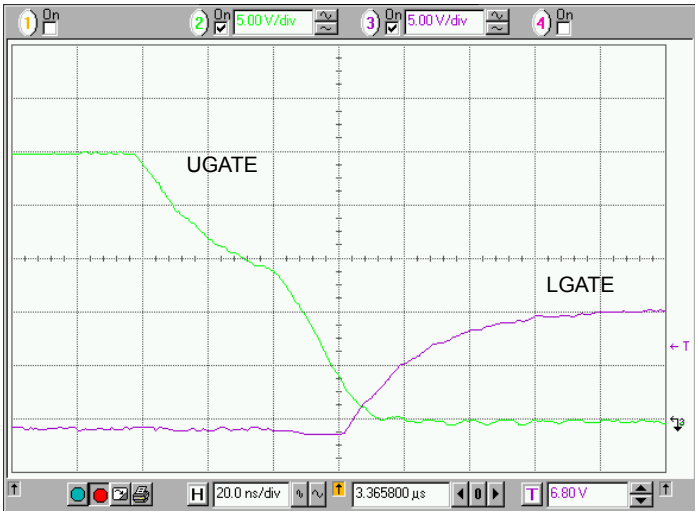
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Dead Time (Rising)



Time 20.0ns/Div.

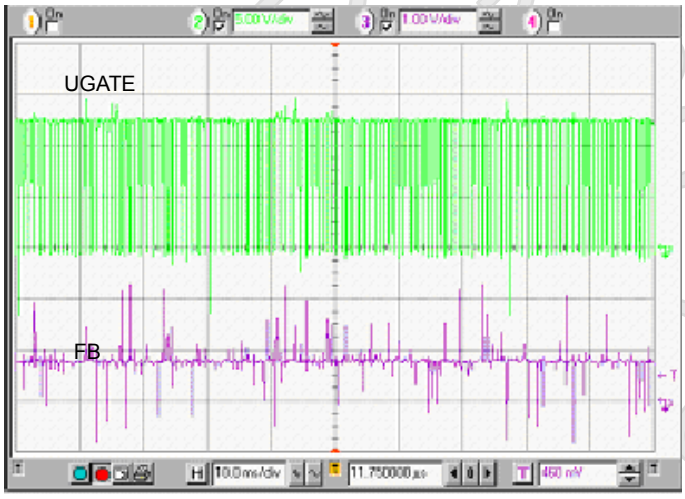
Dead Time (Falling)



Time 20.0ns/Div.

V_{CC}=12V, V_{IN}=5V, I_{OUT}=2.5A

Under Voltage Protection



FB > 0.5V



FB < 0.5V

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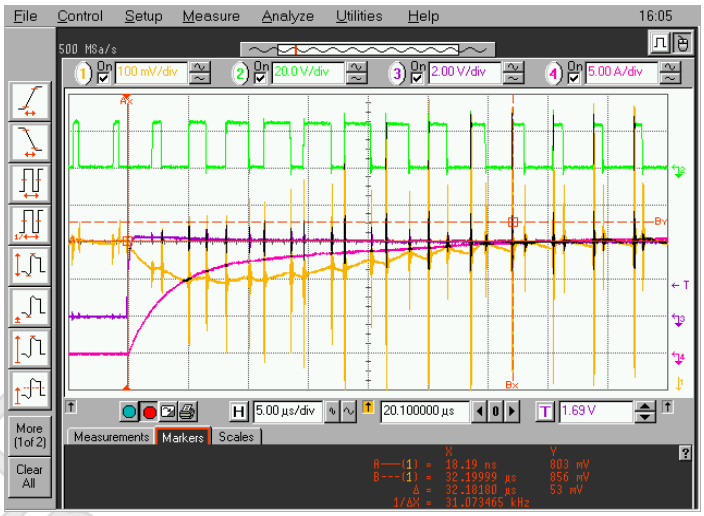
Transient Response Time

Rising

UGATE, 20.0V/Div.

V_{OUT}, 100mV/Div.

IOUT, 5.0A/Div



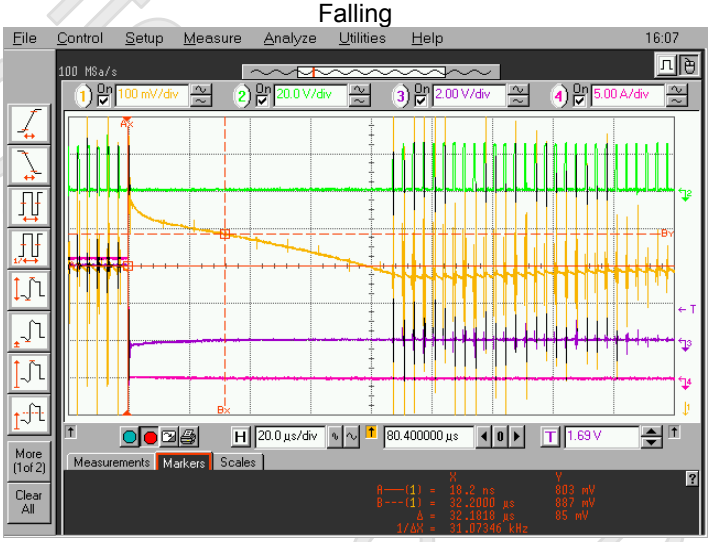
V_{CC}=5V V_{IN}=5V I_{OUT}=0-15A

Falling

UGATE, 20.0V/Div.

V_{OUT}, 100mV/Div.

IOUT, 5.0A/Div



V_{CC}=5V V_{IN}=5V I_{OUT}=15-0A

* The external reference input is 0.8V in all test conditions

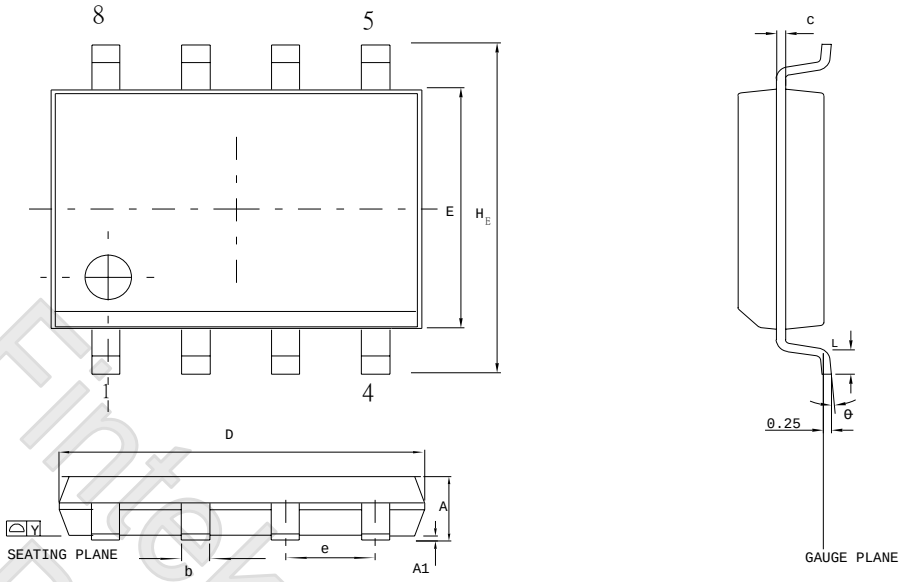
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9. Ordering Information

Part Number	Package Type	Production Flow
F72815SG	8-SOP (Green Package)	Commercial, 0°C to +70°C

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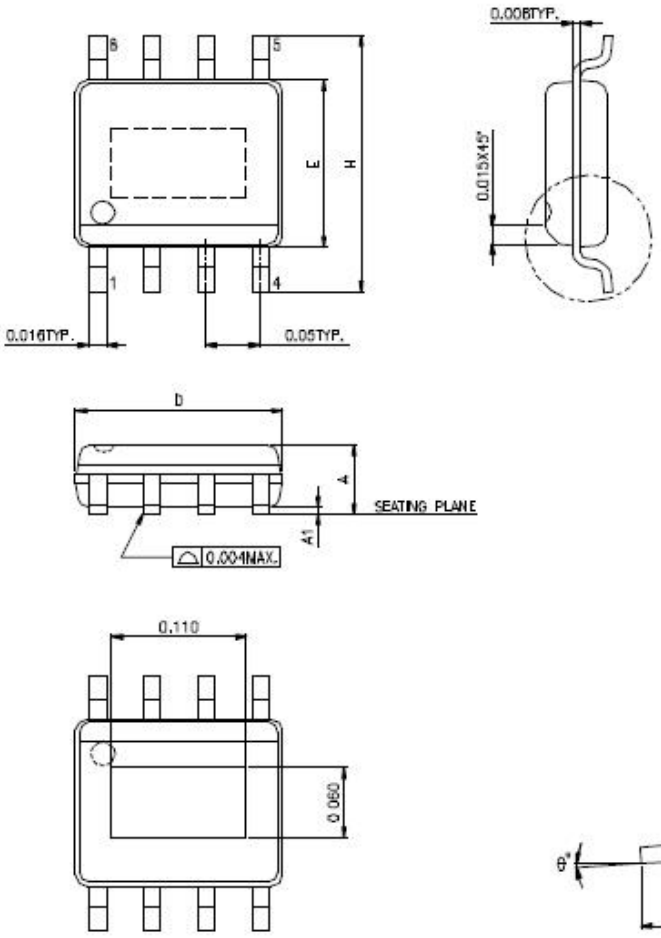
10. Package Dimensions (8-SOP/150 mil)



Control demensions are in milmeters .

SYMBOL	DIMENSION IN MM		DIMENSION IN INCH	
	MIN.	MAX.	MIN.	MAX.
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
b	0.33	0.51	0.013	0.020
c	0.19	0.25	0.008	0.010
E	3.80	4.00	0.150	0.157
D	4.80	5.00	0.188	0.196
e	1.27 BSC		0.050 BSC	
H_E	5.80	6.20	0.228	0.244
Y	—	0.10	—	0.004
L	0.40	1.27	0.016	0.050
θ	0	10	0	10

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SYMBOLS	MIN.	MAX.
A	0.053	0.069
A1	0.004	0.010
D	0.189	0.196
E	0.150	0.157
H	0.228	0.244
L	0.016	0.050
θ°	0	8

UNIT : INCH

NOTES:
1 JEDEC OUTLINE : MS-012 AA / E P. VERSION : N/A
2 DIMENSIONS "D" DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.MOLD FLASH, PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED .15mm (.006in) PER SIDE.
3.DIMENSIONS "E" DOES NOT INCLUDE INTER-LEAD FLASH, OR PROTRUSIONS. INTER-LEAD FLASH AND PROTRUSIONS SHALL NOT EXCEED .25mm (.010in) PER SIDE.

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11. Application Circuit

