Ordering number: EN 3417

Monolithic Digital IC

SANYO No. 3417

LB1687M

3-Phase Brushless Motor Driver

The LB1687M is a 3-phase brushless motor driver IC ideally suited for use in VCR capstan motor, drum motor drive applications.

Features and Functions

(1) 120° voltage linear type
(2) Soft switching type eliminating noises caused by current switching and making the value of external capacitors smaller (comparable to those of chip capacitors)
(3) On-chip FG amplifier
(4) On-chip thermal shutdown circuit
(5) FG signal can be used to detect the rotational speed of a motor so that the hall amp gain is changed in two steps, thus reducing torque ripple and noise.
(6) Motor drivable at voltage down to motor supply voltage 5 V

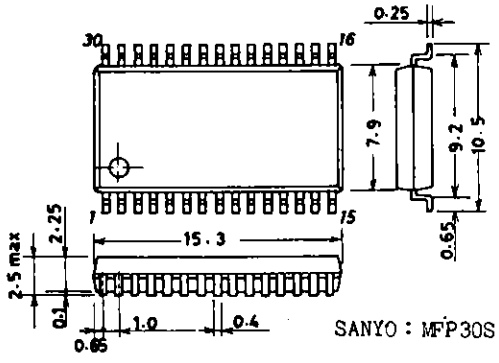
Absolute Maximum Ratings at Ta=25°C

			unit
Maximum Supply Voltage	$V_{CC}1$ max	20	V
	$V_{CC}2$ max	7.0	V
Output Voltage	V_{OUT}.V.W	22	V
Output Current	I_{OUT}	1.5	A
Allowable Power Dissipation	Pd max	1.05	W
Operating Temperature	Topr	−20 to +75	°C
Storage Temperature	T_{stg}	−55 to +125	°C

Allowable Operating Conditions at Ta=25°C

			unit
Supply Voltage Range	$V_{CC}1$	5 to 18	V
	$V_{CC}2$	4.3 to 6.5	V

Package Dimensions 3073A-M30IC
(unit: mm)



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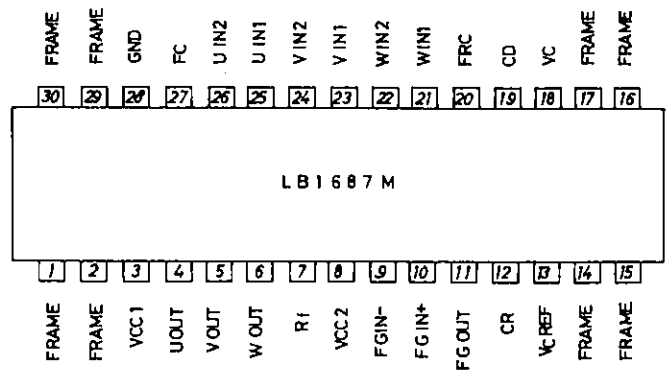
Electrical Characteristics at Ta=25°C, $V_{CC}1$=12V, $V_{CC}2$=5V

			min	typ	max	unit	Note
[Power Supply]							
Supply Current 1	$I_{CC}1$	V_C=0, R_L=∞		17	30	mA	
Supply Current 2	$I_{CC}2$	V_C=0		6.5	9.5	mA	
[Output]							
Output Saturation Voltage	V_O (sat)1	I_{OUT}=0.5A, Sink+Source		1.6	2.2	V	
	V_O (sat)2	I_{OUT}=1.0A, Sink+Source		2.0	3.0	V	
Output TRS Withstand Voltage	V_O (sus)	I_{OUT}=20mA	20			V	※
Output Static Voltage	V_{OQ}	V_C=0	5.8	6.1	6.4	V	
[Hall Input-Output]							
Hall Amp Input Offset Voltage	V_H offset		−5		+5	mV	
Hall Amp Input Bias Current	V_H bias			1	5	μA	
Hall Amp Common-Mode Input Voltage Range	V_H ch		1.3		3.7	V	
Hall Input-Output Voltage Gain	$G_{VHO}1$			56		dB	
	$G_{VHO}2$			43		dB	
[Control-Output]							
Control Amp Input Impedance							
Control-Output Drive Gain	G_{VCO}		38	41	44	dB	
Control-Output CH Difference	Δ G_{VCO}		−2		2	dB	
[FG Amp]							
FG Amp Input Offset Voltage	V_{FG} offset		−8		+8	mV	
Open Loop Voltage Gain	G_{VFG}	f=1kHz		60		dB	
Source Output Saturation Voltage	$V_{FG\ OU}$	I_O=2mA	3.7			V	
Sink Output Saturation Voltage	$V_{FG\ OD}$	I_O=−2mA			1.3	V	
Common-Mode Signal Rejection Ratio	CHR			80		dB	※
FG Amp Common-Mode Input Voltage Range	$V_{FG\ CH}$		0		3.5	V	
Phase Margin				20		deg	※
[Motor Detection]							
Motor Detection Amp Hysteresis Width			35	50	65	mV	
CR Pin Threshold Voltage		V_{CR} low to high	2.35	2.5	2.65	V	
[Thermal Shutdown]							
Thermal Shutdown Temperature	T_{SD}		150	180	210	°C	※
Thermal Shutdown Hysteresis	Δ T_{SD}			15		°C	※

Note: ※ indicates design goals not measured values.

Pin Assignment



Note: All FRAME pins are connected to GND.

Truth Table

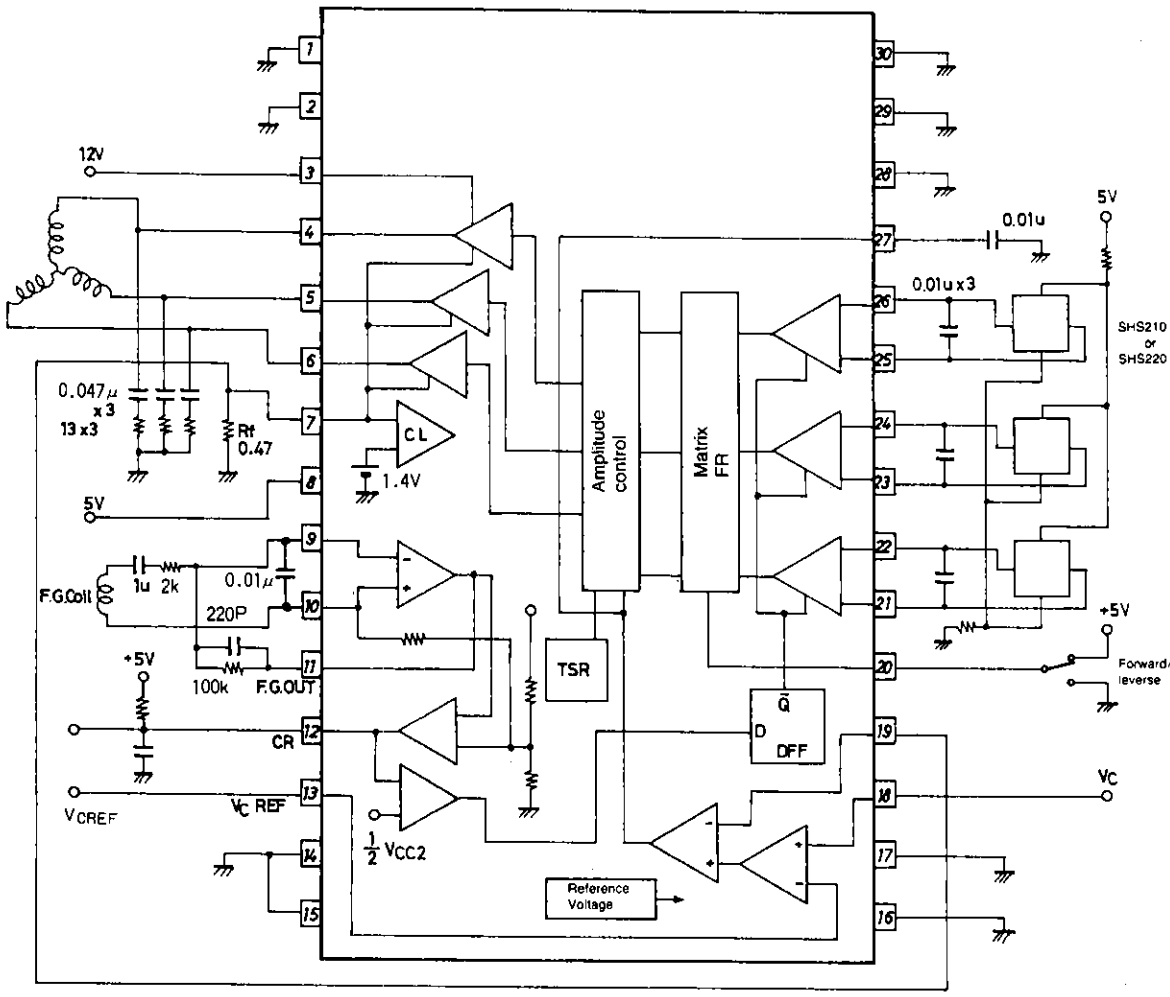
Item	Source → sink	Input			Forward/ reverse control
		U	V	W	
1	W phase → V phase	H	H	L	L
	V phase → W phase				H
2	W phase → U phase	H	L	L	L
	U phase → W phase				H
3	V phase → W phase	L	L	H	L
	W phase → V phase				H
4	U phase → V phase	L	H	L	L
	V phase → U phase				H
5	V phase → U phase	H	L	H	L
	U phase → V phase				H
6	U phase → W phase	L	H	H	L
	W phase → U phase				H

Input: High: Each phase input 1 is more than 0.2V higher than each phase input 2.
Low: Each phase input 1 is more than 0.2V lower than each phase input 2.
Forward/reverse control:
High: 2.0V to $V_{CC}2$
Low: 0 to 0.3 V

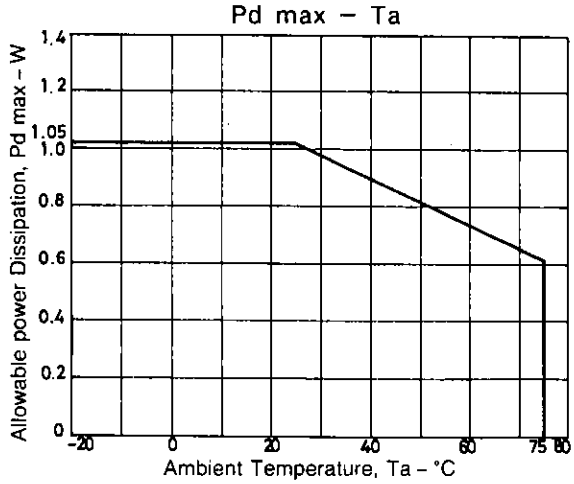
Pin Description

Pin	Pin No.	Functions
$U_{IN}1$, $U_{IN}2$ $V_{IN}1$, $V_{IN}2$ $W_{IN}1$, $W_{IN}2$	25, 26 23, 24 21, 22	U phase Hall element input pin; 'H' of logic; $V_{IN}1 > V_{IN}2$ V phase Hall element input pin; 'H' of logic; $V_{IN}1 > V_{IN}2$ W phase Hall element input pin; 'H' of logic; $V_{IN}1 > V_{IN}2$
U_{OUT} V_{OUT} W_{OUT}	4 5 6	U phase output pin V phase output pin W phase output pin
$V_{CC}1$	3	Power supply pin for applying output
$V_{CC}2$	8	Power supply pin for applying voltage section other than output section; this voltage must be stabilized to be free from ripple, noise, etc.
Rf	7	Output current detect pin; by connecting Rf across this pin and ground, output current is detected as voltage. The result is used to control the overcurrent protection circuit.
C_D	19	Pin for fetching current (voltage) detected with Rf; to take feed back for Rf, the control-output voltage gain can be reduced; when not using, ground.
F_C	27	Frequency characteristic correction pin
V_C	18	Speed-phase control pin Control is of voltage-controlled type that controls output voltage.
V_{CREF}	13	Control reference voltage.
GND	28	Ground except for output The minimum output transistor potential is at Rf pin.
F/RC	20	Forward/reverse control pin By setting this pin to high (more than 2 V) or low (less than 0.3 V), truth value is changed to perform forward and reverse rotation.
FGin−, FGin+	9,10	FG signal input pin
FG_{OUT}	11	FG amp output pin
CR	12	The voltage at this pin can be used to change the hall input gain. Connection of an external resistor and capacitor makes it possible to detect the rotational speed of a motor and change the hall input-output voltage gain in two steps.

Equivalent Circuit Block Diagram



Unit (resistance: Ω, capacitance: F)



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