## VS-MBR2035CT-M3, VS-MBR2045CT-M3

Vishay Semiconductors

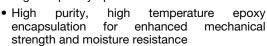
# High Performance Schottky Rectifier, 2 x 10 A

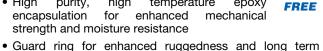


PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 10 A							
$V_{R}$	35 V, 45 V							
V <sub>F</sub> at I <sub>F</sub>	0.57 V							
I <sub>RM</sub> max.	15 mA at 125 °C							
T <sub>J</sub> max.	150 °C							
E <sub>AS</sub>	8 mJ							
Package	3L TO-220AB							
Circuit configuration	Common cathode							

#### **FEATURES**

- 150 °C T<sub>.I</sub> operation
- · Low forward voltage drop
- High frequency operation





COMPLIANT

HALOGEN

- reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

#### **DESCRIPTION**

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I <sub>F(AV)</sub>	Rectangular waveform (per device)	20	Α					
V <sub>RRM</sub>		35/45	V					
I <sub>FRM</sub>	T <sub>C</sub> = 135 °C (per leg)	20	^					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1060	A					
V <sub>F</sub>	10 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.57	V					
T <sub>J</sub>	Range	-65 to +150	°C					

VOLTAGE RATINGS								
PARAMETER SYMBOL VS-MBR2035CT-M3 VS-MBR2045CT-M3								
Maximum DC reverse voltage	$V_R$	35	45	V				
Maximum working peak reverse voltage	$V_{RWM}$	35	45	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CON	VALUES	UNITS				
Maximum average forward per leg		T = 125 °C rotod V		10				
current per device	I <sub>F(AV)</sub>	$T_C = 135$ °C, rated $V_R$		20				
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 k	Hz, T <sub>C</sub> = 135 °C	20				
Non-repetitive peak surge current	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	1060 A				
,	1 3101	Surge applied at rated load condition half wave, single phase, 60 Hz		150				
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to a Frequency limited by T <sub>J</sub> max	2					
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 2  A,  L = 4  \text{m}$	8	mJ				

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		20 A	T <sub>J</sub> = 25 °C	0.84				
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	10 A	T <sub>J</sub> = 125 °C	0.57	V			
		20 A	1J = 125 C	0.72				
Maximum instantaneous reverse current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	Rated DC voltage	0.1	mA			
waxiiiiuiii iiistantaneous reverse current	IRM (")	T <sub>J</sub> = 125 °C	nated DC voltage	15	l IIIA			
Threshold voltage	V <sub>F(TO)</sub>	T. – T. maximum		0.354	V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		17.6	m $Ω$			
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal ran	600	pF				
Typical series inductance	Ls	Measured from top of term	8.0	nΗ				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

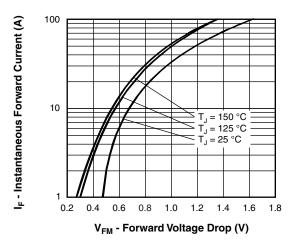
#### Note

 $^{(1)}$  Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction temperature rang	ge T <sub>J</sub>		-65 to +150	°C				
Maximum storage temperature rang	je T <sub>Stg</sub>		-65 to +175					
Maximum thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation		°C/W				
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased (only for TO-220)	0.50	O/W				
Approximate weight			2	g				
Approximate weight			0.07	OZ.				
Mounting torque mini	mum	Non-lubricated threads	6 (5)	kgf · cm				
	mum	Non-lubricated tilleads	12 (10)	(lbf · in)				
Marking davise		Casa atula 21 TO 220AP	MBR2	035CT				
Marking device		Case style 3L TO-220AB	MBR2	MBR2045CT				



## Vishay Semiconductors



100  $T_1 = 150 \, ^{\circ}C$ I<sub>R</sub> - Reverse Current (mA) 10 1 T<sub>J</sub> = 100 °C 0.1 0.01 = 25 °C 0.001 0.0001 20 25 10 15 30 35 40 45 V<sub>R</sub> - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

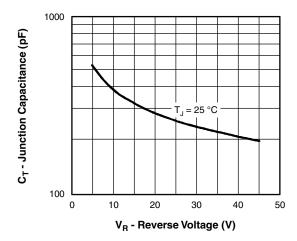


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

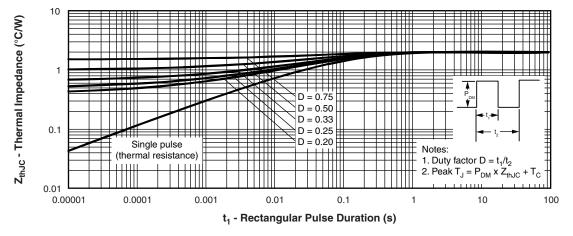


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

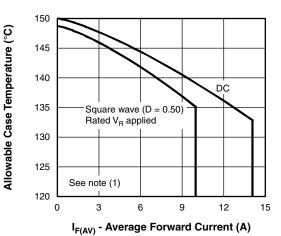


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

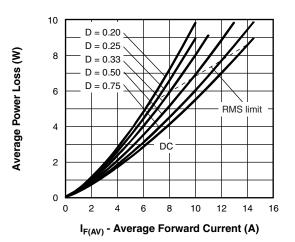


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

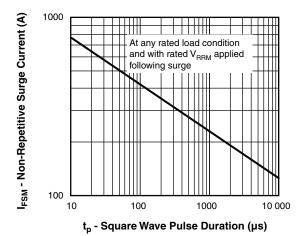


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

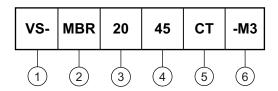
 $^{(2)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = rated V<sub>R</sub>

# **VS-MBR2035CT-M3, VS-MBR2045CT-M3**

Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Schottky MBR series

- Current rating (20 = 20 A)

35 = 35 V 45 = 45 V

5 - CT = essential part number

6 - Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N BASE QUANTITY PACKAGING DESC								
VS-MBR2035CT-M3	50	Antistatic plastic tube						
VS-MBR2045CT-M3	50	Antistatic plastic tube						

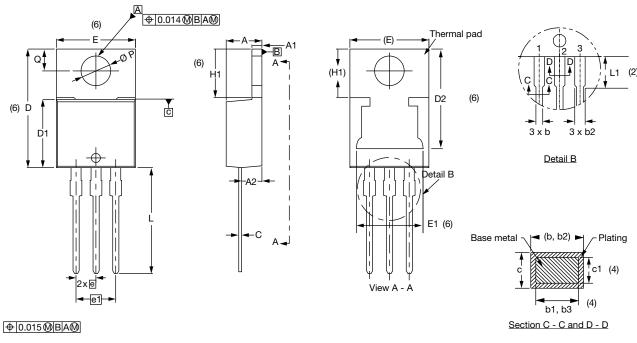
LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?96154						
Part marking information	www.vishay.com/doc?95028						
SPICE model	www.vishay.com/doc?95295						



## Vishay Semiconductors

### **TO-220AB 3L**

#### **DIMENSIONS** in millimeters and inches



Lead tip	
	-

Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIN	IETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
с1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355								

### Notes

- $^{(1)}$  Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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