General Description

The WSP6047 is the highest performance trench P-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSP6047 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

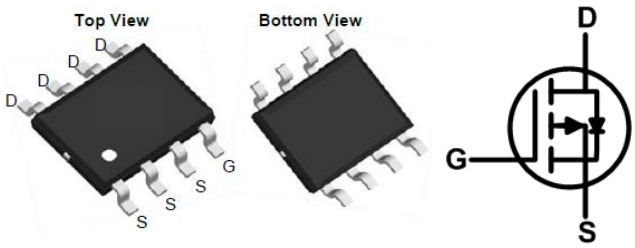
Product Summery

BVDSS	RDSON	ID
-60V	64mΩ	-4.0A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- CCFL Back-light Inverter

SOP-8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_C=25℃	Continuous Drain Current	-4.0	A
I_D@T_C=70℃	Continuous Drain Current	-2.2	A
I_{DP}	Pulsed Drain Current	-20	A
P_D@T_C=25℃	Total Power Dissipation	2.0	W
T_J/T_{STG}	Operating/Storage Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	---	62	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case	---	4	℃/W

P-Channel Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-10V , I_D=-4A	---	64	84	mΩ
		V_{GS}=-4.5V , I_D=-3A	---	80	104	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =-250uA	-1.0	-1.74	-2.5	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-60V , V_{GS}=0V	---	---	-1	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
Q_g	Total Gate Charge (-4.5V)	V_{DS} = -30V, I_D = -3.5A, V_{GS} = -10V	---	23	---	nC
Q_{gs}	Gate-Source Charge		---	2.4	---	
Q_{gd}	Gate-Drain Charge		---	5.7	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD} = -30V, I_D = -1A, V_{GS} = -10V, R_{GEN} = 6Ω	---	13	---	ns
T_r	Rise Time		---	4	---	
$T_{d(off)}$	Turn-Off Delay Time		---	45	---	
T_f	Fall Time		---	6	---	
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}= -30V, f=1MHz	---	1135	---	pF
C_{oss}	Output Capacitance		---	95	---	
C_{rss}	Reverse Transfer Capacitance		---	60	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	-4.0	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.2	V

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C: The current rating is based on the t≤ 10s junction to ambient thermal resistance rating.

P-Channel Typical Characteristics

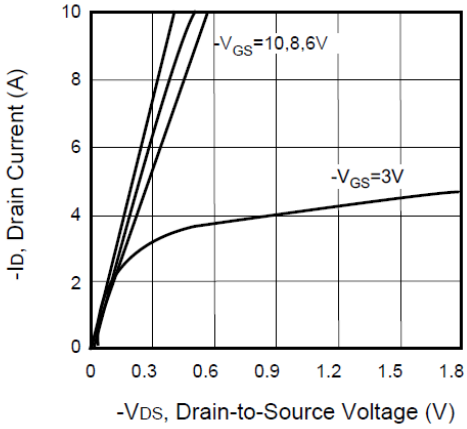


Figure 1. Output Characteristics

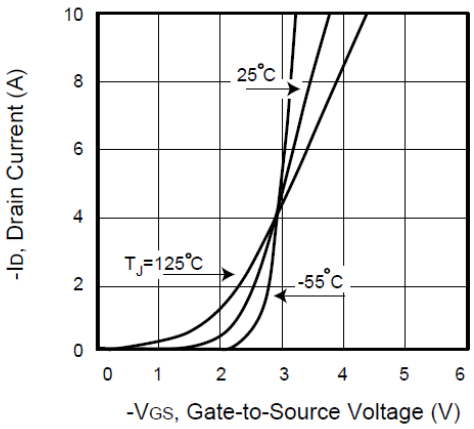


Figure 2. Transfer Characteristics

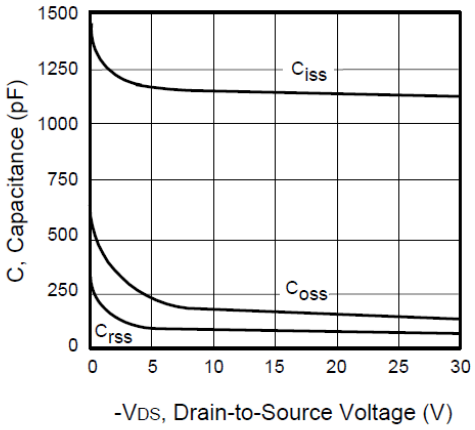


Figure 3. Capacitance

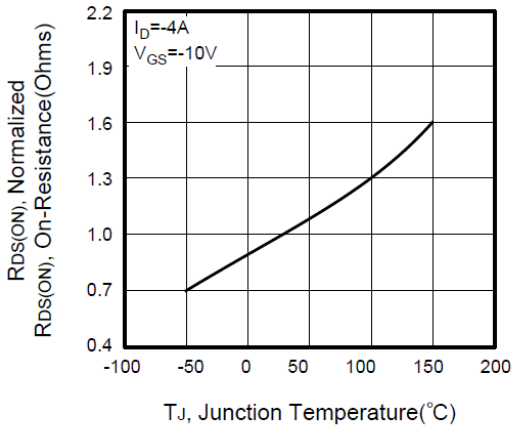


Figure 4. On-Resistance Variation with Temperature

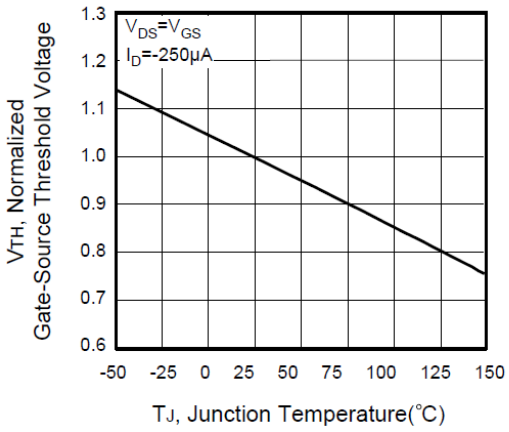


Figure 5. Gate Threshold Variation with Temperature

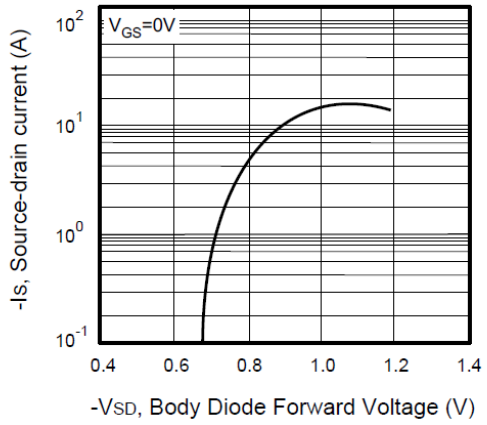


Figure 6. Body Diode Forward Voltage Variation with Source Current

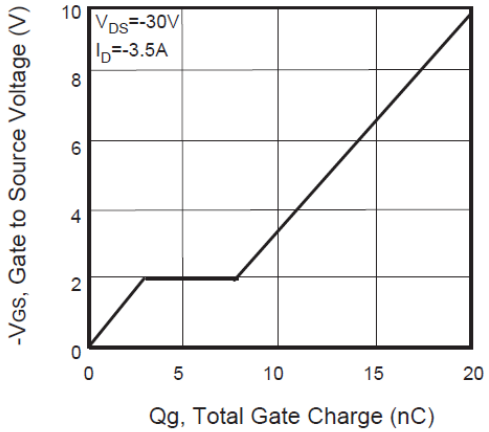


Figure 7. Gate Charge

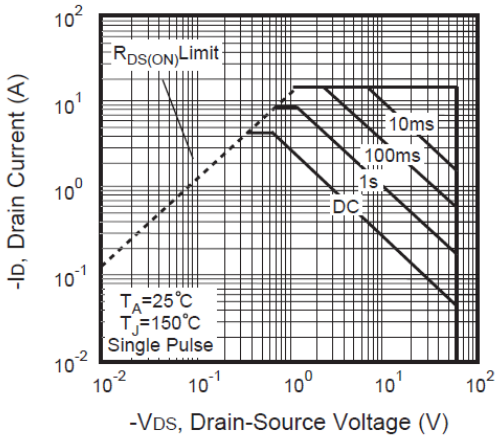


Figure 8. Maximum Safe Operating Area

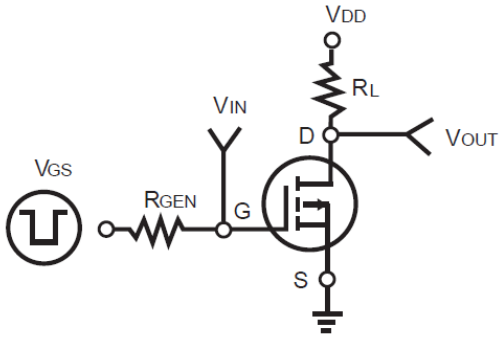


Figure 9. Switching Test Circuit

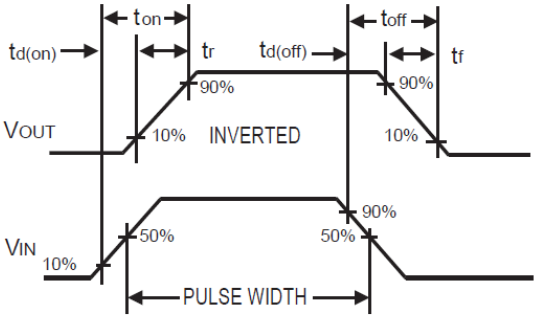


Figure 10. Switching Waveforms

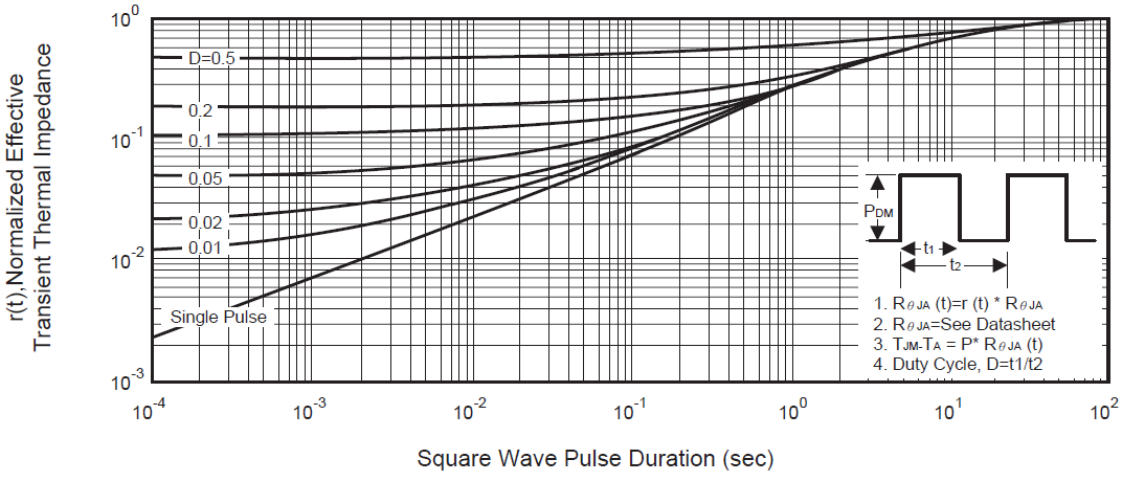


Figure 11. Normalized Thermal Transient Impedance Curve

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