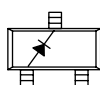


PIN Diodes

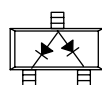
SMP1320 Series (KMP1320 Series)

■ Features

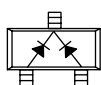
- Low Resistance 0.9Ω
- Low Capacitance 0.3pF
- Packages rated MSL1, 260°C per JEDEC J-STD-020)



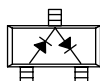
SMP1320-001
SMP1320-001LF
L_s = 1.5 nH



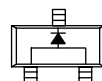
SMP1320-003
SMP1320-003LF
L_s = 1.5 nH



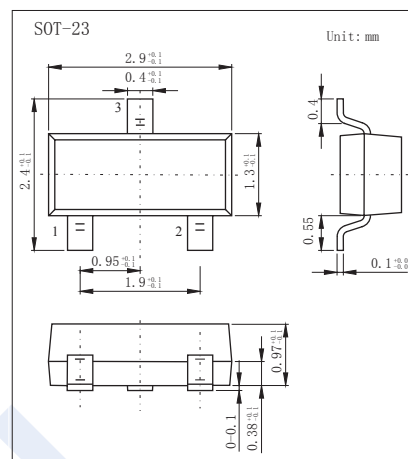
SMP1320-004
SMP1320-004LF
L_s = 1.5 nH



SMP1320-005
SMP1320-005LF
Ls = 1.5 nH



SMP1320-007
SMP1320-007LF
Ls = 0.4 nH



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Reverse Voltage	V _R	50	V
Power Dissipation	P _D	250	mW
Junction Temperature	T _J	150	°C
Operating temperature	T _A	-65 to 150	
Storage Temperature range	T _{stg}	-65 to 150	

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Reverse breakdown voltage	V_R	$I_R=100\text{ }\mu\text{A}$	50			V
Forward voltage	V_F	$I_F=10\text{ mA}$		0.85		
Reverse voltage leakage current	I_R	$V_R=50\text{ V}$			10	μA
Series Resistance	R_S	$I=1\text{ mA}, F=100\text{ MHz}$		2		Ω
		$I=10\text{ mA}, F=100\text{ MHz}$			0.9	
Capacitance between terminals	C_T	$V_R=30\text{ V}, f=1\text{ MHz}$			0.3	pF
Carrier lifetime	T_I	$I_F=10\text{ mA}$		0.4		μs
I region width				8		μm

PIN Diodes

SMP1320 Series (KMP1320 Series)

■ Resistance vs Temperature @ 500 MHz

I_F (mA)	R_S @ -55°C (Ω)	R_S @ -15°C (Ω)	R_S @ +25°C (Ω)	R_S @ +65°C (Ω)	R_S @ +100°C (Ω)
0.02	29.6	29.2	30.8	32.0	32.7
0.10	7.2	7.7	8.3	8.8	8.8
0.3	3.4	3.6	3.8	4.0	4.1
0.5	2.5	2.7	2.8	2.9	3.0
1.0	1.7	1.8	1.9	2.0	1.9
10	0.84	0.85	0.76	0.76	0.67
20	0.73	0.73	0.64	0.64	0.56
100	0.59	0.57	0.47	0.48	0.40

■ Marking

NO	SMP1320-001	SMP1320-003	SMP1320-004	SMP1320-005	SMP1320-007
	KMP1320-001	KMP1320-003	KMP1320-004	KMP1320-005	KMP1320-007
Marking	PL1	PL9	PL3	PL2	PLB

NO	SMP1320-001LF	SMP1320-003LF	SMP1320-004LF	SMP1320-005LF	SMP1320-007LF
	KMP1320-001LF	KMP1320-003LF	KMP1320-004LF	KMP1320-005LF	KMP1320-007LF
Marking	RL1	RL9	RL3	RL2	RLB

PIN Diodes

SMP1320 Series (KMP1320 Series)

■ Typical Characteristics

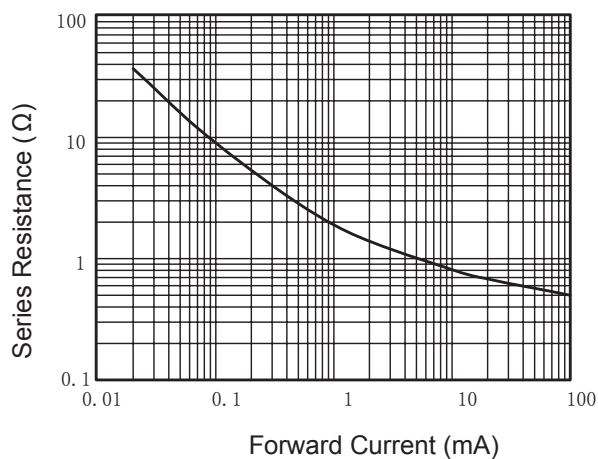


Figure 2. Series Resistance vs Current @ 100 MHz

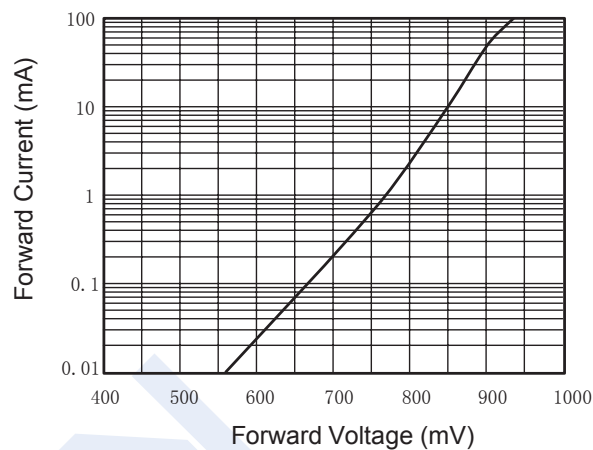


Figure 3. DC Characteristics

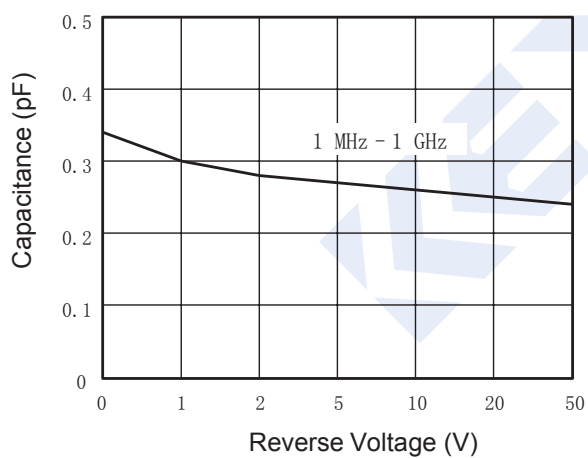


Figure 4. Capacitance vs Reverse Voltage

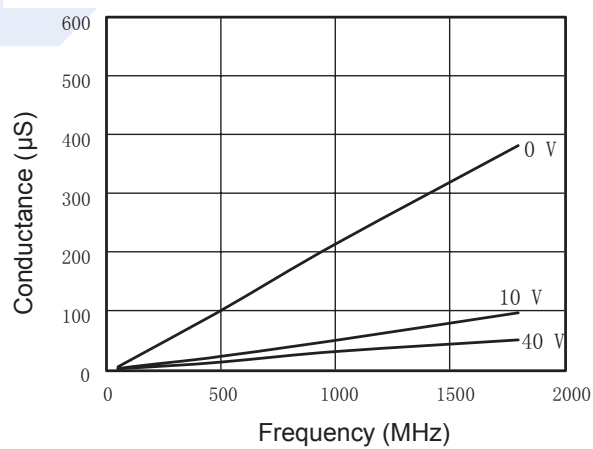


Figure 5. Conductance vs Frequency and Reverse Voltage