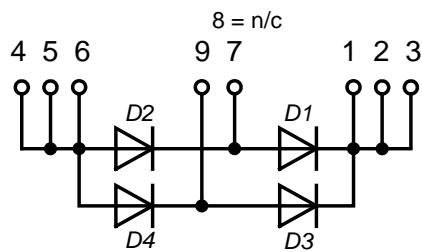
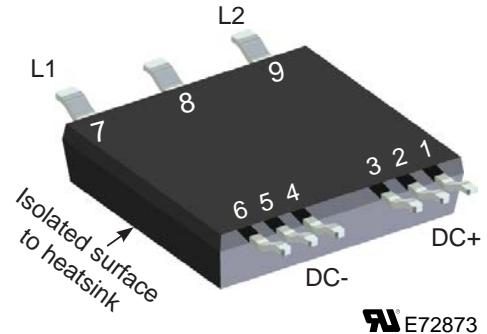


High Efficiency Standard Rectifier

Single Phase Rectifier Bridge

$V_{RRM} = 800 \text{ V}$
 $I_{DAV} = 124 \text{ A}$
 $V_F = 1.15 \text{ V}$

Part number
DLA100B800LB



Features / Advantages:

- Planar passivated chips
- Very low leakage current
- Very low forward voltage drop
- Improved thermal behaviour
- AEC-Q 101 qualified 2018

Applications:

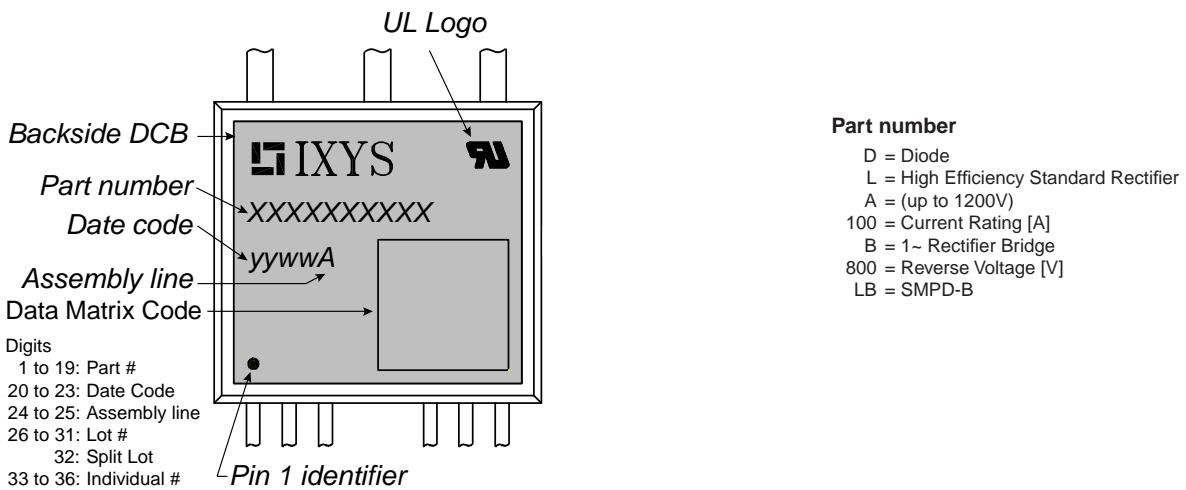
- Diode Bridge for main rectification

Package: SMPD

- DCB isolated backside
- Isolation Voltage 3000 V
- Epoxy meets UL 94V-0
- RoHS compliant

Diodes			Ratings		
Symbol	Definitions	Conditions	min.	typ.	max.
$V_{RRM\ op}$	operating reverse blocking voltage	$T_{VJ} = 25^\circ C$			800 V
$V_{RRM\ peak}$	peak repetitive reverse blocking voltage				1200 V
I_R	reverse current, drain current	$V_R = 800 V$ $T_{VJ} = 25^\circ C$ $T_{VJ} = 150^\circ C$		10 μA 0.1 mA	
V_F	forward voltage drop	$I_F = 50 A$ $I_F = 100 A$ $T_{VJ} = 25^\circ C$ $I_F = 50 A$ $I_F = 100 A$ $T_{VJ} = 150^\circ C$		1.23 V 1.45 V 1.15 V 1.44 V	
I_{DAV}	average forward current	rectifier output current with: rectangular; $d = 0.5$ (per diode) sine 180° (per diode)	$T_C = 135^\circ C$		132 A 124 A
V_{F0} r_F	threshold voltage slope resistance	for power loss calculation only	$T_{VJ} = 175^\circ C$	0.75 V 4.2 mΩ	
R_{thJC}	thermal resistance junction to case				
R_{thJH}	thermal resistance junction to heatsink	with thermal transfer paste (IXYS test setup)		1.45	1.60 K/W
P_{tot}	total power dissipation		$T_C = 25^\circ C$		150 W
I_{FSM}	max. forward surge current	$t = 10 ms$; (50 Hz), sine; $V_R = 0 V$ $t = 8.3 ms$; (50 Hz), sine; $V_R = 0 V$	$T_{VJ} = 45^\circ C$ $T_{VJ} = 45^\circ C$		400 A 430 A
		$t = 10 ms$; (50 Hz), sine; $V_R = 0 V$ $t = 8.3 ms$; (50 Hz), sine; $V_R = 0 V$	$T_{VJ} = 150^\circ C$ $T_{VJ} = 150^\circ C$		350 A 375 A
I^2t	value for fusing	$t = 10 ms$; (50 Hz), sine; $V_R = 0 V$ $t = 8.3 ms$; (50 Hz), sine; $V_R = 0 V$	$T_{VJ} = 45^\circ C$ $T_{VJ} = 45^\circ C$		800 A²s 780 A²s
		$t = 10 ms$; (50 Hz), sine; $V_R = 0 V$ $t = 8.3 ms$; (50 Hz), sine; $V_R = 0 V$	$T_{VJ} = 150^\circ C$ $T_{VJ} = 150^\circ C$		610 A²s 570 A²s
C_J		$V_R = 800 V$; $f = 1 MHz$	$T_{VJ} = 25^\circ C$	13	pF

Package SMPD			Ratings		
Symbol	Definitions	Conditions	min.	typ.	max.
I_{RMS}	RMS current	wide terminal standard terminal		100 60	A A
T_{stg}	storage temperature		-55	150	°C
T_{op}	operation temperature		-55	150	°C
T_{VJ}	virtual junction temperature		-55	175	°C
Weight				8	g
F_c	mounting force with clip		40	130	N
$d_{Spp/App}$	creepage distance on surface /	terminal to terminal	1.6		mm
$d_{Spb/Apb}$	striking distance through air	terminal to backside	4.0		mm
V_{ISOL}	isolation voltage	$t = 1$ second $t = 1$ minute	50/60 Hz; RMS; $I_{ISOL} < 1$ mA	3000 2500	V V



Ordering	Part Name	Marking on Product	Delivering Mode	Base Qty	Ordering Code
Standard	DLA100B800LB-TRR	DLA100B800LB-TRR	Tape&Reel	200	514621
	DLA100B800LB	DLA100B800LB	Tube	20	514614

Equivalent Circuits for Simulation *on die level

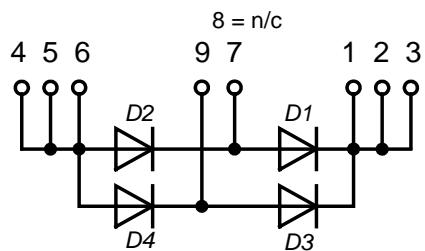
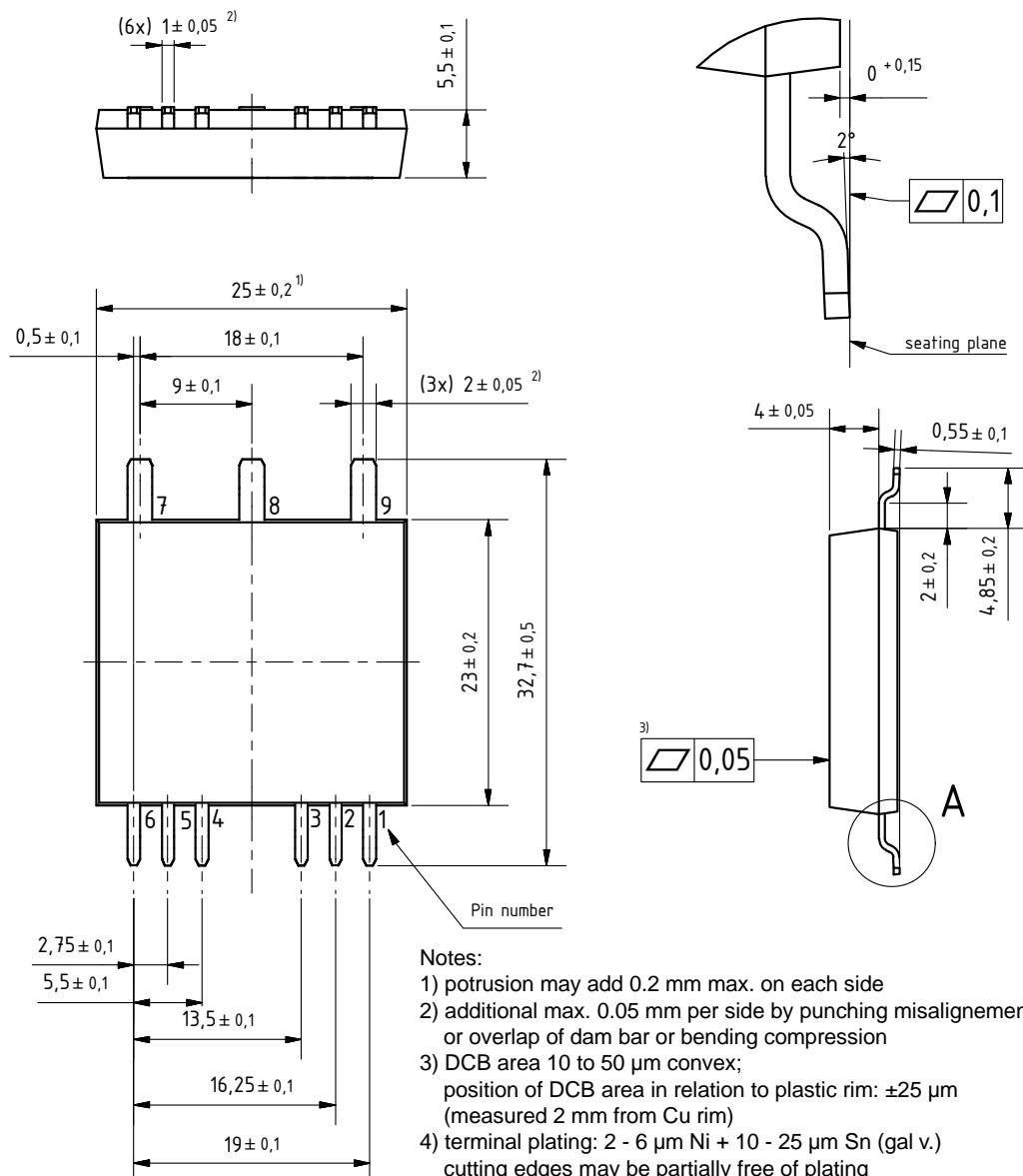
$T_{VJ} = 175^\circ\text{C}$



$V_{0\ max}$	threshold voltage	0.75	V
$R_{0\ max}$	slope resistance *	4.2	$\text{m}\Omega$

Outlines SMPD

A (8 : 1)

Dimensions in mm
(1 mm = 0.0394")

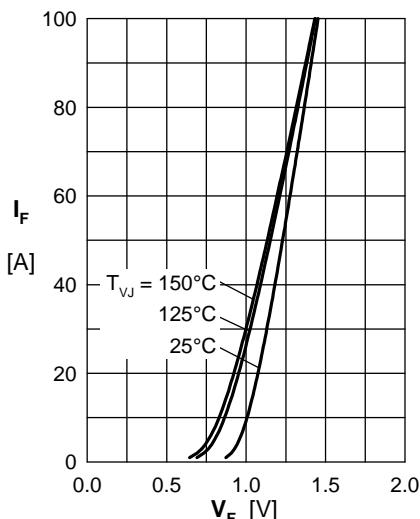


Fig. 1 Forward current versus voltage drop per diode

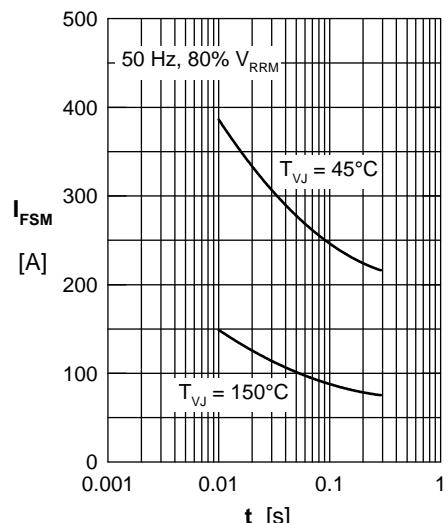


Fig. 2 Surge overload current

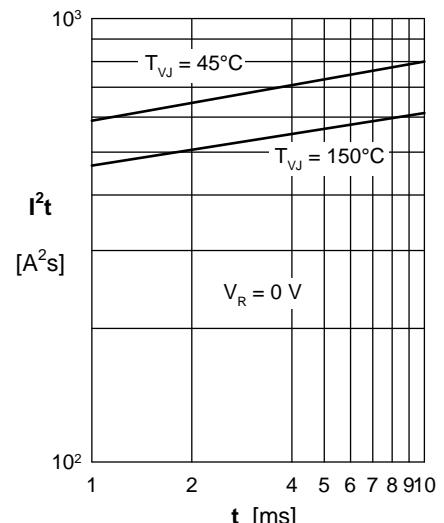


Fig. 3 I²t versus time per diode

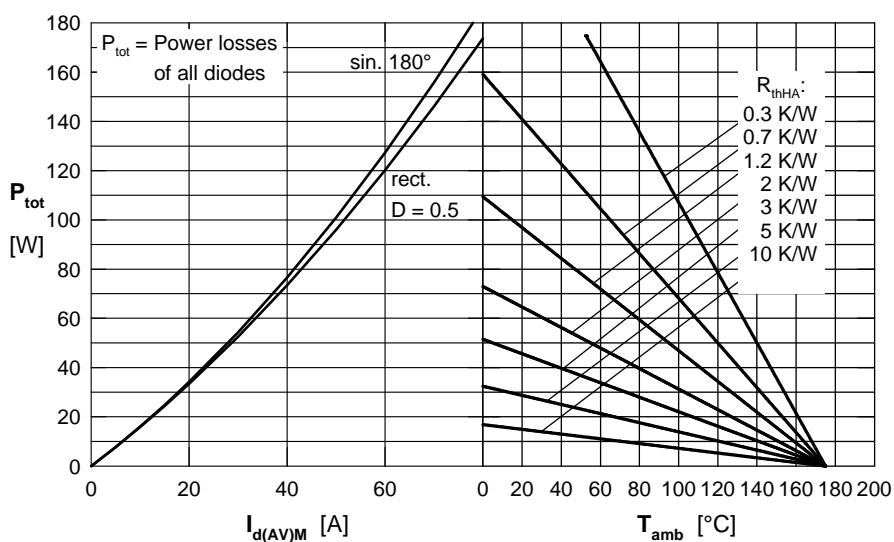


Fig. 4 Power dissipation vs. bridge output current and ambient temperature

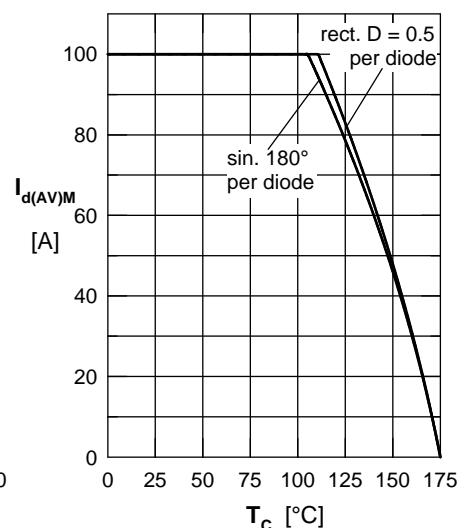


Fig. 5 Max. bridge output current vs. case temperature

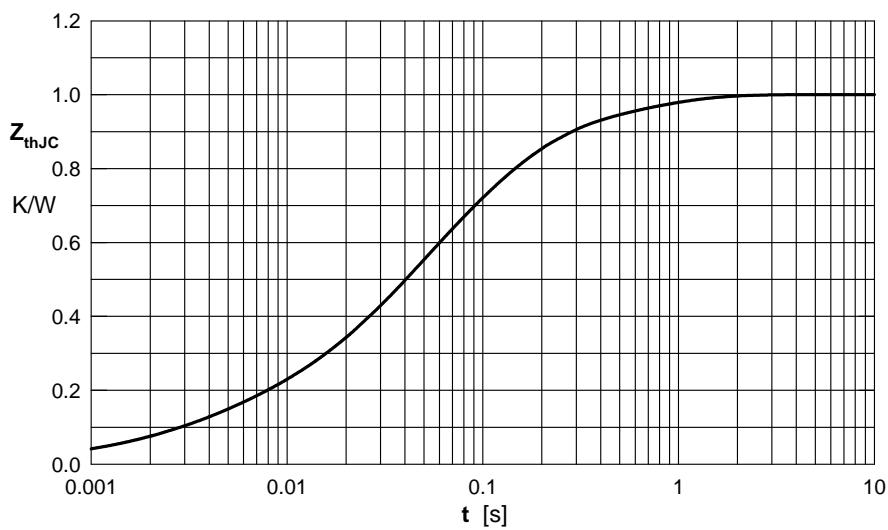


Fig. 6 Transient thermal impedance junction to case

Constants for Z_{thJC} calculation:

i	R _{thi} [K/W]	t _i [s]
1	0.09	0.003
2	0.116	0.062
3	0.386	0.1
4	0.128	0.55