CHENMKO ENTERPRISE CO.,LTD

CHFMA4PT

SURFACE MOUNT
Dual Digital Silicon Transistor

VOLTAGE 50 Volts CURRENT 100 mAmpere

Lead free devices

APPLICATION

* Switching circuit, Inverter, Interface circuit, Driver circuit.

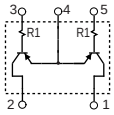
FEATURE

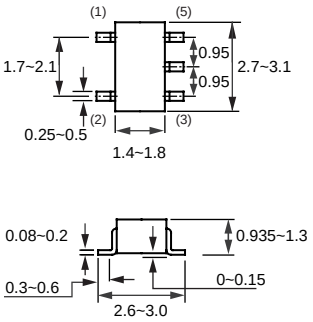
* Small surface mounting type. (SC-74A)
* High current gain.
* Suitable for high packing density.
* Low colloector-emitter saturation.
* High saturation current capability.
* Two CHDTA114T chips in a package.
* Built in bias resistor(R1=10kΩ, Typ.)



SC-74A

CIRCUIT





Dimensions in millimeters

SC-74A

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-Base voltage		-50	V
V_{CEO}	Collector-Emitter voltage		-50	V
V_{EBO}	Emitter-Base voltage		-5	V
I_C	Collector current		-100	mA
P_C	Collector Power dissipation	$T_{amb} \leq 25\ ^{\circ}C$, Note 1	300	mW
T_{STG}	Storage temperature		−55 +150	$^{\circ}C$
T_J	Junction temperature		150	$^{\circ}C$

Note

1. Transistor mounted on an FR4 printed-circuit board.

RATING CHARACTERISTIC (CHFMA4PT)

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise speciÞed.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
BV_{CBO}	Collector-Base breakdown voltage	I_C= -50uA	-50.0	–	–	V
BV_{CEO}	Collector-Emitter breakdown voltage	I_C= -1mA	-50.0	–	–	V
BV_{EBO}	Emitter-Base breakdown voltage	I_E= -50uA	-5.0	–	–	V
$V_{CE(sat)}$	Collector-Emitter Saturation voltage	I_C= -10mA; I_B= -1mA	–	–	-0.3	V
I_{CBO}	Collector-Base current	V_{CB}= -50V	–	–	-0.5	uA
I_{EBO}	Emitter-Base current	V_{EB}= -4V	–	–	-0.5	uA
h_{FE}	DC current gain	I_C= -1mA; V_{CE}= -5.0V	100	250	600	
R_1	Input resistor		7	10	13	KΩ
f_T	Transition frequency	I_E=5mA, V_{CE}= -10.0V f=100MHz	–	250	–	MHz

Note

1.Pulse test: tp≤300uS; δ≤0.02.

RATING CHARACTERISTIC CURVES (CHFMA4PT)

Typical Electrical Characteristics

Fig.1 DC current gain vs. collector current

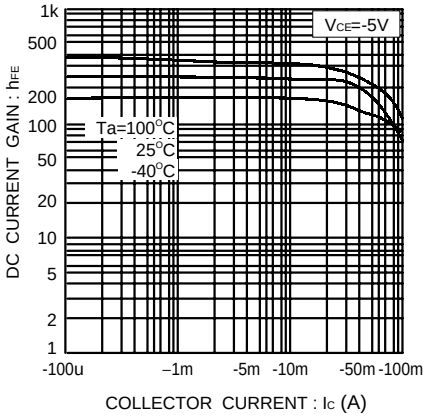


Fig.2 Collector-emitter saturation voltage vs. collector current

