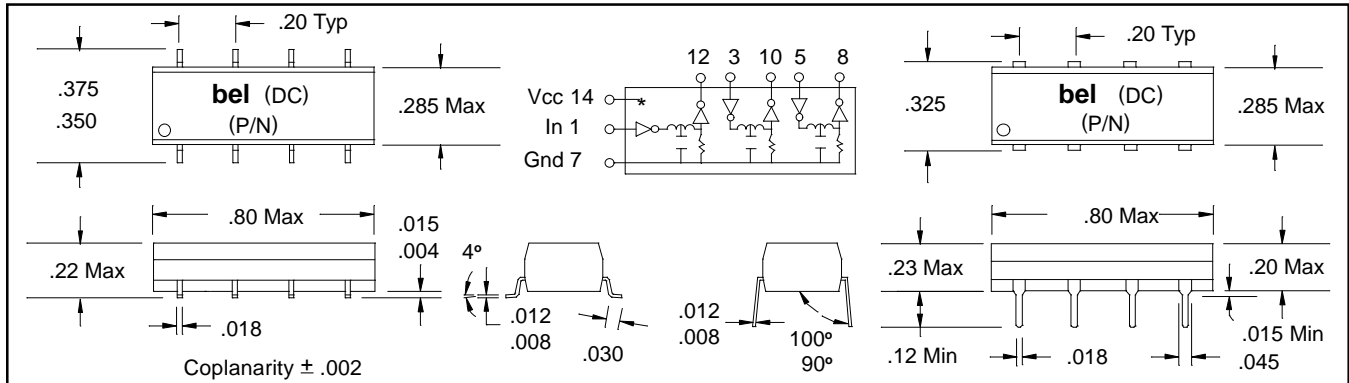


## TRIPLE LINE LEADING EDGE CONTROL STANDARD DELAY MODULES

Cat 12-R3



### Part Numbers

SMD	Thru-Hole	Time Delay	Tolerance	Rise Time
S422-0005-A3	A447-0005-A3	5 ns	± 1.0 ns	3 ns
S422-0010-A3	A447-0010-A3	10 ns	± 1.5 ns	3 ns
S422-0015-A3	A447-0015-A3	15 ns	± 1.5 ns	3 ns
S422-0020-A3	A447-0020-A3	20 ns	± 1.5 ns	3 ns
S422-0025-A3	A447-0025-A3	25 ns	± 1.5 ns	3 ns
S422-0030-A3	A447-0030-A3	30 ns	± 1.5 ns	3 ns
S422-0035-A3	A447-0035-A3	35 ns	± 1.5 ns	3 ns
S422-0040-A3	A447-0040-A3	40 ns	± 2.0 ns	3 ns
S422-0045-A3	A447-0045-A3	45 ns	± 2.0 ns	3 ns
S422-0050-A3	A447-0050-A3	50 ns	± 2.5 ns	3 ns
S422-0060-A3	A447-0060-A3	60 ns	± 3.0 ns	4 ns
S422-0070-A3	A447-0070-A3	70 ns	± 3.5 ns	4 ns
S422-0080-A3	A447-0080-A3	80 ns	± 4.0 ns	4 ns

### Test Conditions @ 25°C

Ein	Pulse Voltage	3.2 Volts
Trin	Rise Time	3.0 ns (10%-90%)
PW	Pulse Width	1.2 x Total Delay
PP	Pulse Period	4 x Pulse Width
Iccl	Supply Current	80 ma Typical
Vcc	Supply Voltage	5.0 Volts

### Electrical Characteristics

	Min.	Max.	Units
Vcc	4.75	5.25	V
Vih	2.0		V
Vil		0.8	V
Ioh		-1	ma
Iol		20	ma
Voh	2.7		V
Vol		0.5	V
Vik		-1.2	V
Iih		20	ua
Iil		-0.6	ma
Ios	-60	-150	ma
Icch		70	ma
Iccl		90	ma
Ta	0°	70°	C
PW	100		%
d		50	%
Tc	Temp. Coeff. of Total Delay (TD) 100 + (25000/TD) PPM/°C		

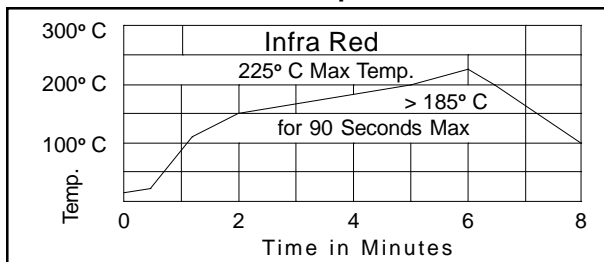
### Tolerances

Input to Output ± 5% , Unless otherwise specified  
Delays measured @ 1.5 V levels on Leading Edge only  
with 15pf loads on Outputs  
Rise and Fall Times measured from 0.75 V to 2.4 V levels

### Drive Capabilities

Nh	Logic 1 Fanout	-	10 TTL Loads Max.
Nl	Logic 0 Fanout	-	10 TTL Loads Max.

### Recommended Temperature Profile



### Notes

Transfer molded for better reliability  
Compatible with TTL & DTL circuits  
Terminals: Electro-Tin plate phosphor bronze  
Performance warranty is limited to specified parameters listed  
SMD - Tape & Reel available:  
32mm Wide x 16mm Pitch, 500 pieces per 13" reel

**Other Delays and Tolerances Available  
Consult Sales**

Specifications subject to change without notice.

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