

IC for CMOS Regulator+Reset Monolithic IC MM3018

Outline

This is a reset + regulator composite IC developed using the CMOS process.

The CMOS process is used to achieve ultra-low current consumption, aiming at use in memory cards and portable devices. Further, this exceptional IC can be used even when board mounting space is limited, due to the wafer level package, WLCSP-6.

Features

1. Built-in thermal shutdown circuit
2. Built-in current limit circuit
3. Low current consumption 15 μ A typ.
4. Series with reset detection voltage in 0.1V steps
5. Delay time from voltage detection to reset release can be set easily.

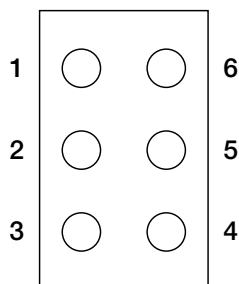
Package

WLCSP-6

Applications

1. Microcomputer, CPU and MPU reset circuit
2. Logic circuit reset circuit
3. Battery voltage check
4. Back-up circuit switching circuit
5. Level detection circuit
6. Mechanical system reset circuit

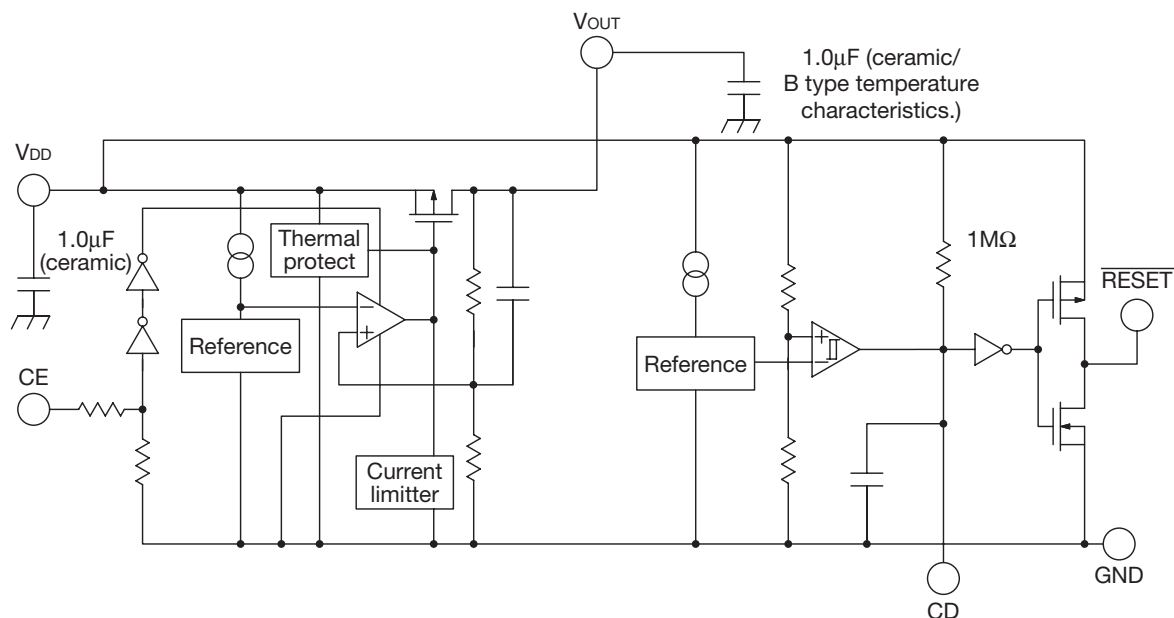
Pin Assignment



WLCSP-6
(TOP VIEW)

1	V _{DD}
2	CE
3	RESET
4	CD
5	GND
6	V _{OUT}

Block Diagram



Pin Description

Pin no.	Pin name	Functions						
1	V _{DD}	Voltage-supply pin						
2	CE	V _{OUT} ON/OFF control pin <table><tr><th>CONT</th><th>V_{OUT}</th></tr><tr><td>L</td><td>OFF</td></tr><tr><td>H</td><td>ON</td></tr></table> Connect CONT-TERMINAL with V _{DD} ,When it is not used.	CONT	V _{OUT}	L	OFF	H	ON
CONT	V _{OUT}							
L	OFF							
H	ON							
3	RESET	RESET-output pin RESET pin logic <table><tr><th></th><th>RESET</th></tr><tr><td>V_{DD}<V_S</td><td>L</td></tr><tr><td>V_{DD}>V_S</td><td>H</td></tr></table>		RESET	V _{DD} <V _S	L	V _{DD} >V _S	H
	RESET							
V _{DD} <V _S	L							
V _{DD} >V _S	H							
4	CD	Capacitor connect pin with delay						
5	GND	GND pin						
6	V _{OUT}	V _{OUT} pin (100mA)						

Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings	Units
Storage temperature	T _{STG}	-55~+125	°C
Supply voltage for V _{DD}	V _{DD}	-0.3~+8	V
Supply voltage for CE	V _{CE}	-0.3~V _{DD} +0.3	V
Supply voltage for CD	V _{CD}	-0.3~V _{DD} +0.3	V
Allowable loss	P _d	600*	mW

Note: * With the double Bided PC Board of glass epoxy.
 (Copper plane 80% 25X25X1.0^t mm)

Recommended Operating Conditions (Ta=25°C)

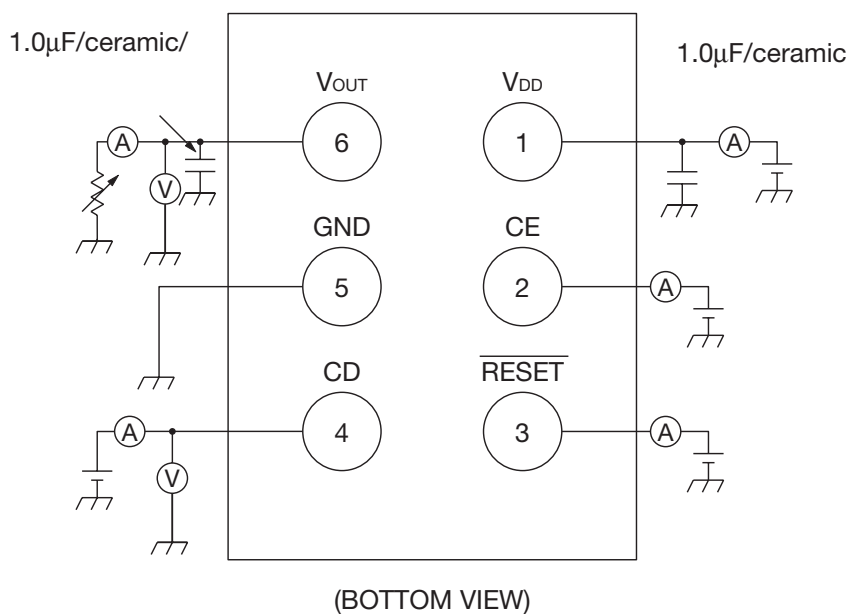
Item	Symbol	Ratings	Units
Operating temperature	T _{OPR}	-40~+85	°C
Operating supply voltage when regulator works	V _{OP-REG}	2.5~3.6	V
Operating supply voltage when reset works	V _{OP-RES}	0.7~3.6	V
Output current	I _{OUT}	100	mA

Electrical Characteristics (Except where noted otherwise, Ta=25°C, CE=3.6V) (Represent model MM3018A)

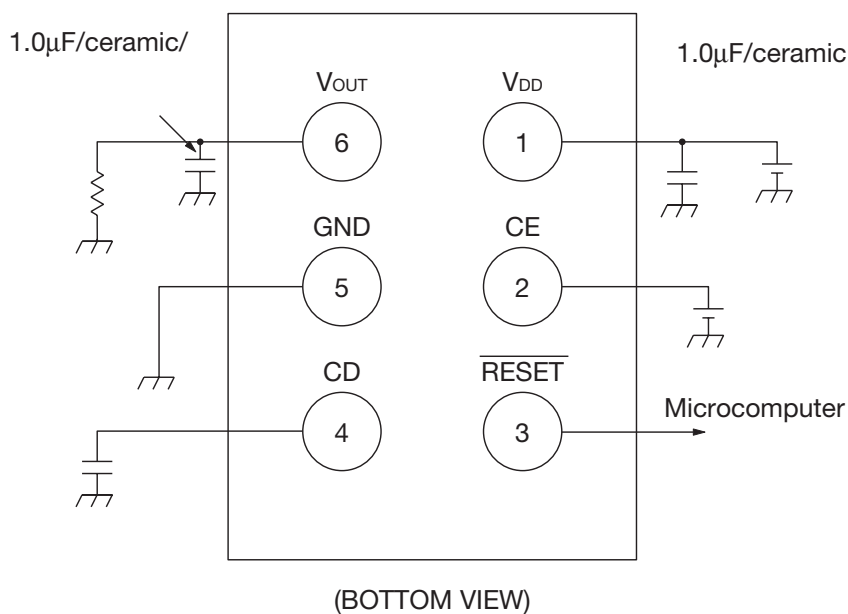
Item	Symbol	Measurement conditions	Min.	Typ.	Max.	Units
Current consumption						
V _{IN} input current 1	I _{ccq1}	V _{DD} =3.6V, I _{OUT} =0mA		15	30	μA
Input current when regulator is OFF	I _{ccq2}	V _{DD} =3.6V, I _{OUT} =0mA, CE=0.5V		2	5	μA
Input current when reset works	I _{ccq3}	V _{DD} =CE=2.2V, I _{OUT} =0mA		18	35	μA
Regulator						
Output Voltage 1	V _{OUT1}	V _{DD} =3.6V, I _{OUT} =50mA	1.764	1.8	1.836	V
Drop voltage	V _{IO}	V _{DD} =1.7V, I _{OUT} =50mA		120	250	mV
Line regulation	ΔV ₁	V _{DD} =2.5~3.6V, I _{OUT} =50mA		10	30	mV
Load regulation	ΔV ₂	V _{DD} =3.6V, I _{OUT} =0~100mA		30	90	mV
V _{OUT} temperature coefficient *	ΔV _{OUT} /ΔT	T _j =-40~+85°C		±100		ppm/°C
CE terminal current	I _{ON}	V _{DD} =3.6V		0.5	1.0	μA
High threshold voltage	H		V _{DD} -0.5		V _{DD}	V
Low threshold voltage	L				0.5	V
V_{DD} reset						
Detecting voltage	V _S	V _{DD} =H→L	2.254	2.3	2.346	V
V _S temperature coefficient *	ΔV _S /ΔT	T _j =-40~+85°C, V _{DD} =H→L		±100		ppm/°C
Hysteresis voltage	ΔV _S	V _{DD} =H→L→H	69	115	161	mV
Output current 1	I _{OUT1}	Nch V _{DS} =0.05V, V _{DD} =0.7V	0.01	0.05		mA
Output current 2	I _{OUT2}	Nch V _{DS} =0.5V, V _{DD} =1.5V	1	2		mA
Output current 3	I _{OUT3}	Pch V _{DS} =-2.1V, V _{DD} =3.6V	1	2		mA
CD pin threshold voltage	V _{TCD}	V _{DD} =2.53V	1.012	1.265	1.518	V
CD pin resistance	R _D		0.7	1.0	1.3	MΩ
CD pin resistance temperature coefficient *	R _D	T _j =-40~+85°C		-4500		ppm/°C
Threshold operating voltage	V _{OPL}	V _{DS} ≤ 0.1V			0.7	V

Note: * design guaranteed

Measuring Circuit



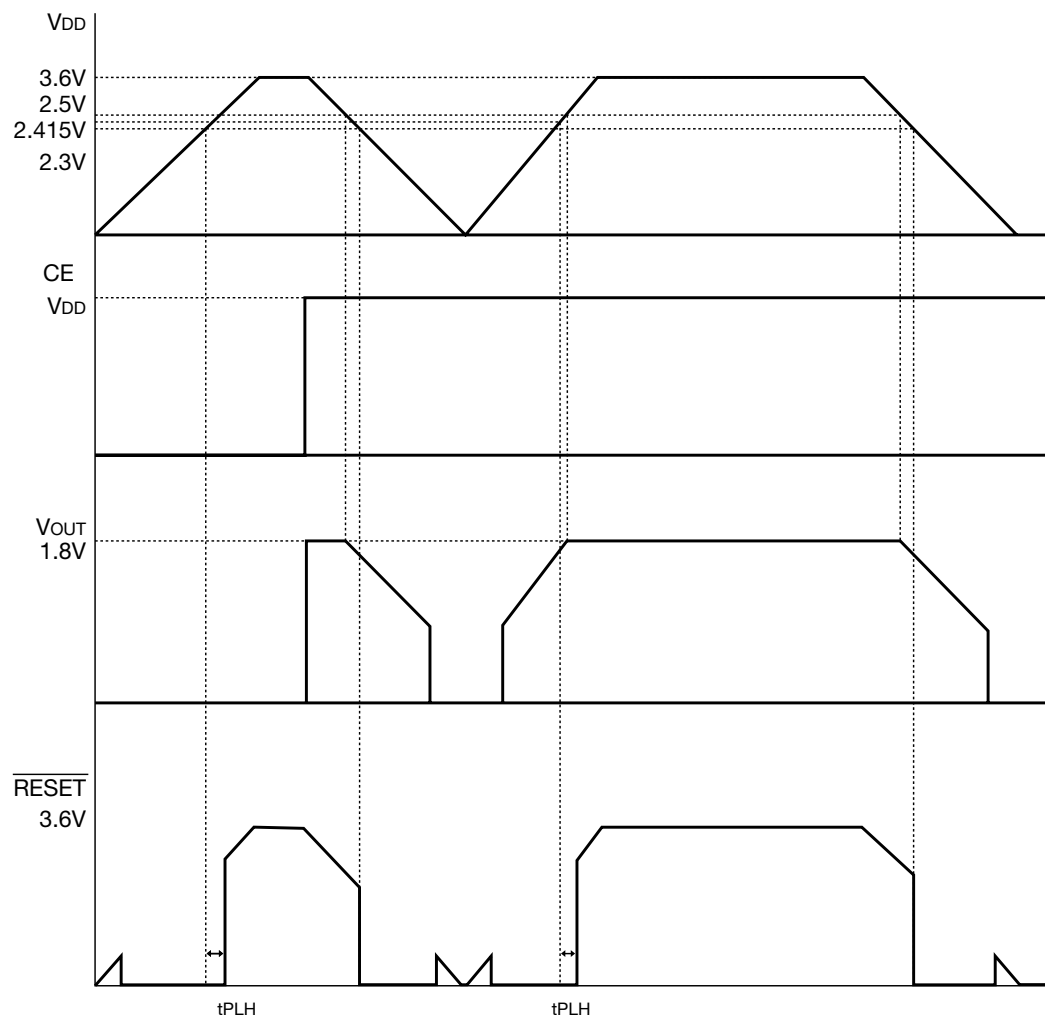
Application Circuit



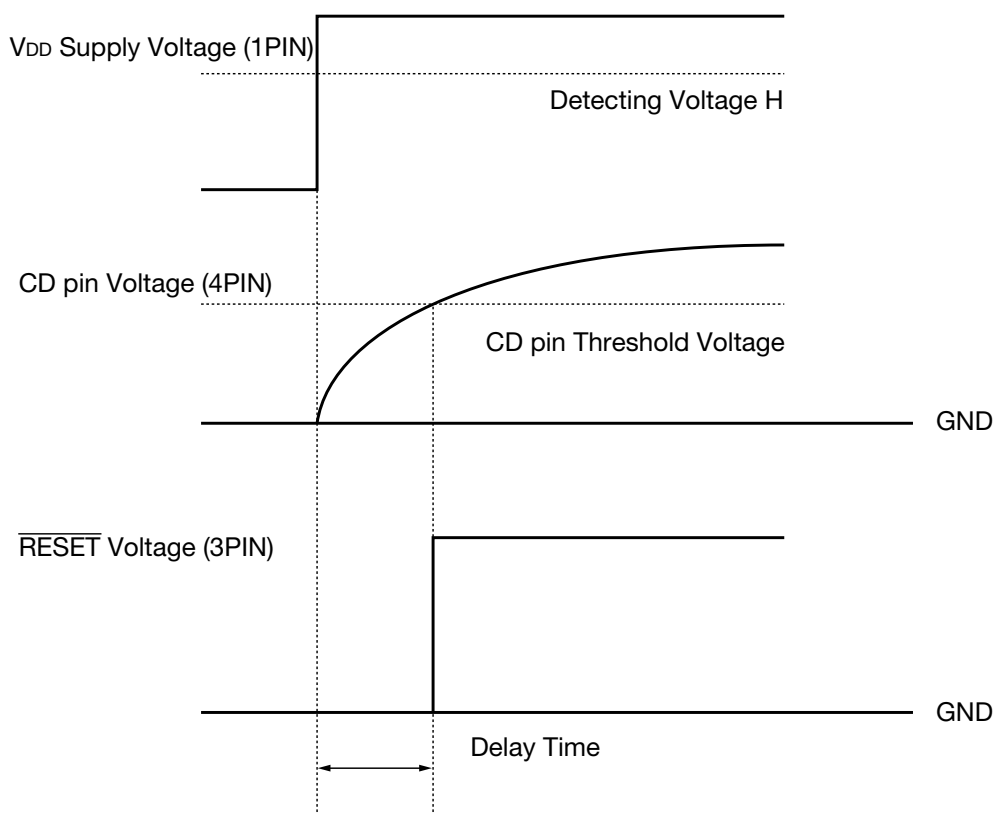
Note

1. An output-capacitor recommended the capacitor with more than 1.0μF and B type temperature characteristics.
2. The wire of V_{DD} and GND is required to print full ground plane for noise and stability.
3. The input capacitor must be connected a distance of less than 1cm from input pin.
4. In case the output voltage is above the input voltage, the overcurrent flow by internal parasitic diode from output to input.

Timing Chart



Delay Time

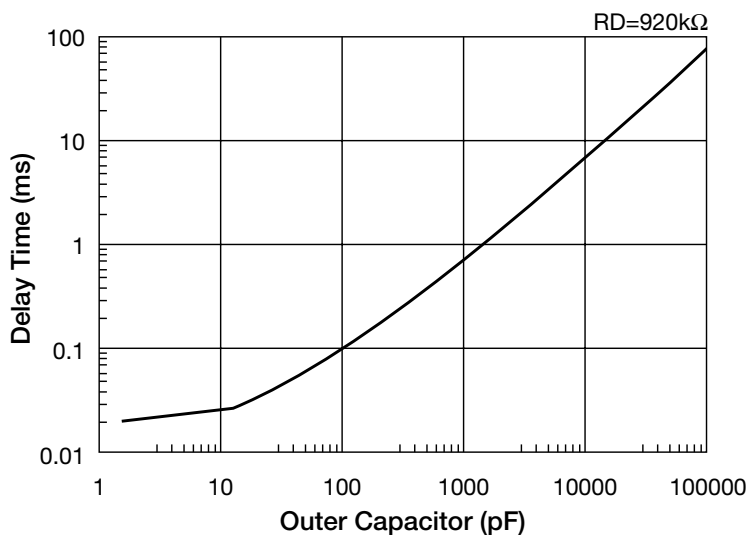


Delay Time (t_{PLH}) $t_{PLH} \approx 0.77 \times RD (\Omega) \times CD (F)$

RD: CD pin resistance

CD: Outer capacitor

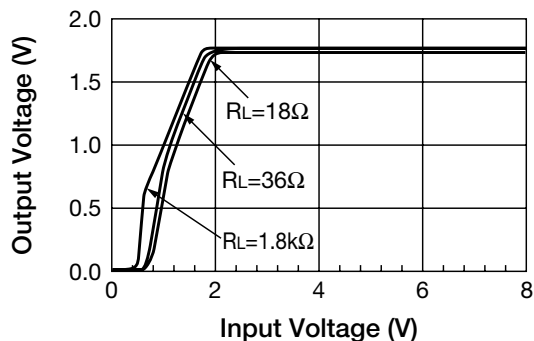
Outer Capacitor Vs Delay Time



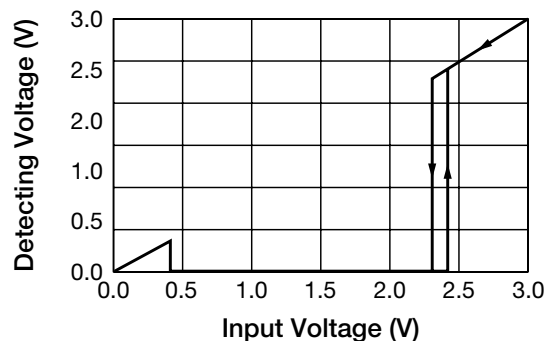
note: These are typical characteristics.

Characteristics
 (Except where noted otherwise, $T_a=25^{\circ}\text{C}$, $V_{DD}=3.6\text{V}$, $V_{CE}=V_{DD}$, $C_{IN}=1.0\mu\text{F}$, $C_O=1.0\mu\text{F}$) (Represent model MM3018A)

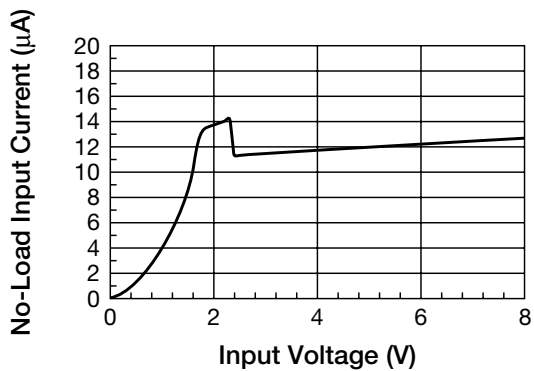
Output Voltage



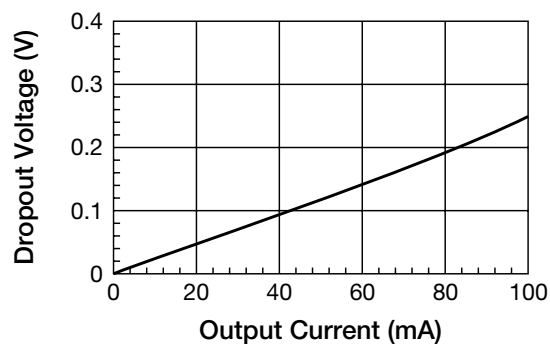
Detecting Voltage



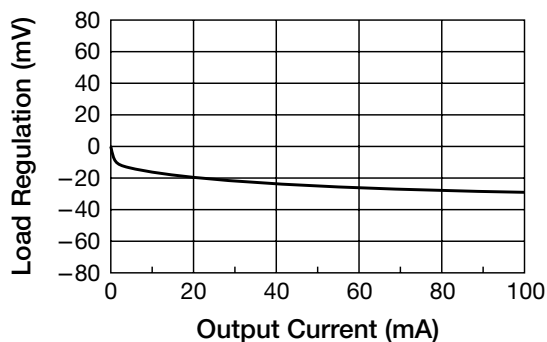
No-Load Input Current



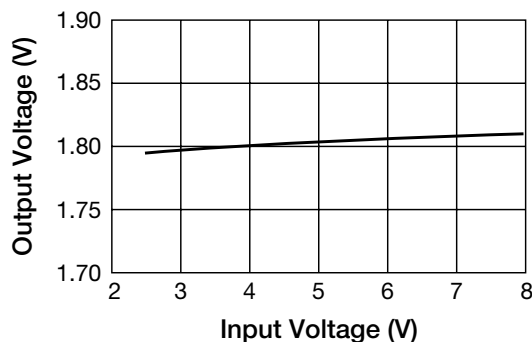
Dropout Voltage



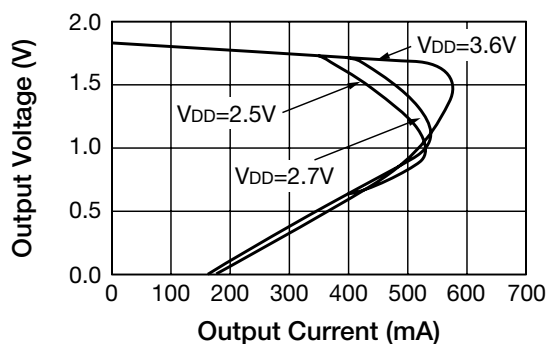
Load Regulation



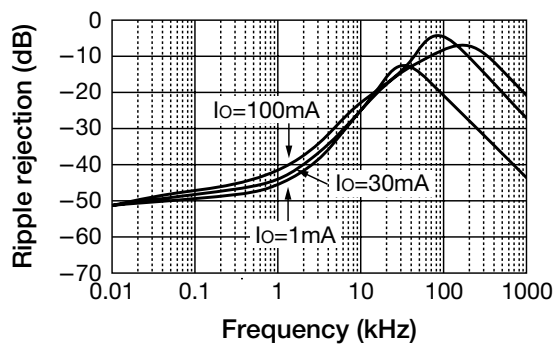
Line Regulation



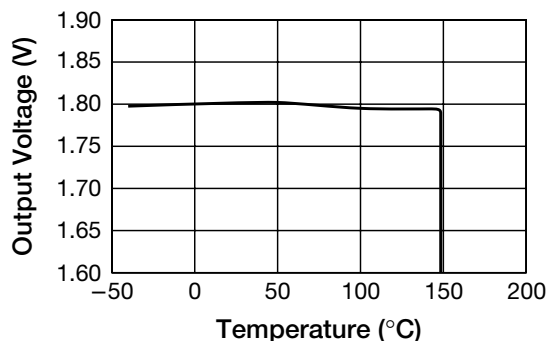
Current Limit



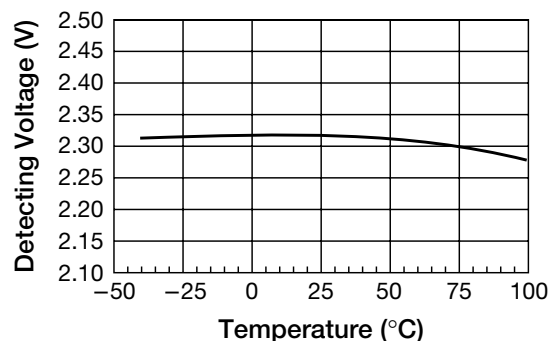
Ripple rejection



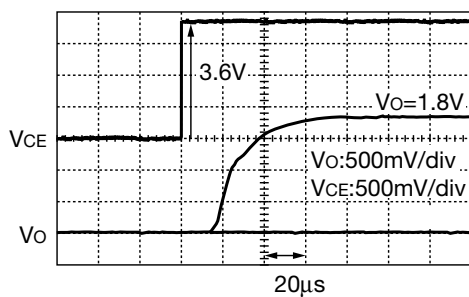
■ Temperature - Output Voltage



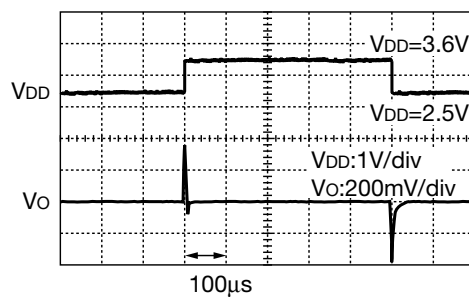
■ Temperature - Detecting Voltage



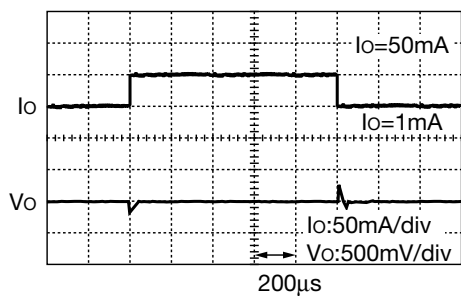
■ Regulator rise ($V_{DD}=3.6V$, $V_{CE}=0 \rightarrow 2.0V$, $I_o=50mA$)



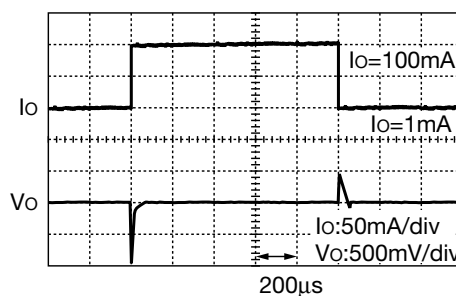
■ Input transient response ($V_{DD}=2.5V \rightarrow 3.6V$, $I_o=50mA$, $C_o=0.1\mu F$)



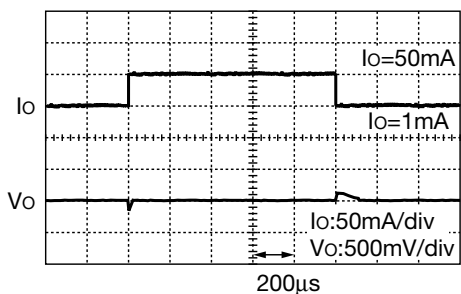
■ Load transient response ($I_o=1mA \rightarrow 50mA$, $C_o=0.1\mu F$)



■ Load transient response ($I_o=1mA \rightarrow 100mA$, $C_o=0.1\mu F$)



■ Load transient response ($I_o=1mA \rightarrow 50mA$, $C_o=1.0\mu F$)



■ Load transient response ($I_o=1mA \rightarrow 100mA$, $C_o=1.0\mu F$)

