

Connectivity Solutions

OE310G1I71 OCP Mezzanine Adapter

Single Port SFP+ 10 Gigabit Ethernet OCP Mezzanine Card Intel® X710 Based

Product Description

Silicom's SFP+ 10 Gigabit Ethernet Open Compute Project (OCP) mezzanine card is designed for use in any mother board that can utilize "OCP_Mezz 2.0_rev1.00" MEZZ cards.

The Silicom's 10 Gigabit Ethernet OCP mezzanine single 10 Gigabit Ethenet connectivity to the OCP Server node. The Silicom SFP+ 10 Gigabit Ethernet OCP mezzanine card is based on Intel X710 Ethernet controller with fully integrated Gigabit Ethernet Media Access Control (MAC) and SFI Interface. In addition to managing MAC and PHY Ethernet layer functions, the controller manages PCI Express packet traffic across its transaction, link, and physical/logical layers. Using hardware acceleration, the controller offloads tasks from the host, such as TCP/UDP/IP checksum calculations and TCP segmentation.

The Silicom SFP+ 10 Gigabit Ethernet OCP mezzanine card implement a type C NC-SI interface (single package, common bus buffers and shared RX queue).

Silicom's SFP+ 10 Gigabit Ethernet OCP Mezzanine cards is the ideal solution for implementing multiple network segments, mission-critical high-powered networking applications and environments within high performance OCP servers.

Key Features

Host Interface:

- Supports OCP Mezzanine card 2.0 Design Specification
- Supports FCI 61083-124402LT or equivalent mounted on the mezzanine card
- PCI Express Gen 3.0 X8 lanes in Connector A
- NC-SI for Manageability connection to BMC

SFP+ 10Gigabit Ethernet:

10Gigabit Ethernet Adapter with SFP cage support:

- -XR: Copper 10SFP+Cu (Passive Direct Attach Cable):
 - o Compliant with the SFP+ MSA SFF-8431 specification
 - Up to 7 meters.
- -SRD: Fiber 1/10 Gigabit Ethernet 1000Base-SX / 10GBASE-SR:
 - 1000BASE-SX with 1G 850nM Small form Factor Pluggable (SFP+)
 - 0 10GBASE-SR with 10Gigabit 850nM Small form Factor Pluggable (SFP+)
- -LRD: Fiber 1/10 Gigabit Ethernet 1000Base-LX / 10GBASE-LR:

- o 1000BASE-LX with 1G 1310nM Small form Factor Pluggable (SFP+)
- o 10GBASE-LR with 10Gigabit 1310nM Small form Factor Pluggable (SFP+)

-SRD: Fiber 1/10 Gigabit Ethernet 1000Base-SX / 10GBASE-SR:

- 10 Gigabit Fiber Ethernet port supports 10GBASE-SR (850nM LAN PHY)
- 1Gigabit Fiber Ethernet port supports 1000BASE-SX (850nM LAN PHY)
- 1/10Gigabit 850nM Small form Factor Pluggable (SFP+)

-LRD: Fiber 1/10 Gigabit Ethernet 1000Base-LX / 10GBASE-LR:

- 10 Gigabit Fiber Ethernet port supports 10GBASE-LR (1310nM LAN PHY)
- 1Gigabit Fiber Ethernet port supports 1000BASE-LX (1310nM LAN PHY)
- 1/10Gigabit 1310nM Small form Factor Pluggable (SFP+)

Performance Features:

- Support for jumbo frame up to 9.5KB
- Flow control support
- Priority Flow Control (draft IEEE 802.1Qbb)
- Enhanced Transmission Selection (draft IEEE802.1az)
- Statistics management and RMON
- 802.1q VLAN support
- DCB/DCB-X support
- Message Signal interrupts (MSI-X)
- Storage Enabling competitive performance with native OS intelligent offload solutions, including NAS, iSCSI and FCoE

LAN and Virtualization Features:

- Network Virtualization offloads for VXLAN and NVGRE
- Unified Networking Providing a single wire for LAN and storage: NAS (SMB, NFS) and SAN (iSCSI, FCoE)
- Virtual Bridging Support VEPA/802.1Qbg, BPE/802.1Qbh
- Physical Functions Up to 8 per port, up to 16 per device
- Support for 128 Virtual Device Queues (VMDq) per port
- Hardware Queue Pairs Up to 1.5K (non-RDMA); up to 256K (RDMA)
- Virtualization Alleviating hypervisor I/O bottlenecks by providing flow separation for Virtual Machines (VMs)
- TCP/IP/L2 features:
 - Receive Side Scaling (RSS)
 - Large Send Offload (LSO)

- TCP/UDP/IP/SCTP Checksum Offload
- o IPV4, IPV6

Technical Specifications

-SFP+ 10Gigabit Ethernet Technical Specifications Adapters:				
SFP+ (Small Form Factor Pluggable) supports:	SFI interfaces supports 10GBase-R PCS and 10 Gigabit PMA in order to connect with SFP+ to 10GBase-SR / / 1000Base-SX / 10GBase-LR and SFP+ Direct Attach			
10GBase-SR SFP+: IEEE Standard / Network topology:	Fiber 10Gigabit Ethernet, 10GBASE-SR (850nM LAN PHY).			
10GBase-SR SFP+: Data Transfer Rate :	10.3125GBd			
10GBase-SR SFP+: Cables and Operating distance Up to:	62.5um, 160MHz/Km 26m 62.5um, (OM1)200MHz/Km 33m 50um, 400MHz/Km 66m 50um, (OM2)500 MHz/Km 82m 50um, (OM3)2000MHz/Km 300m			
10GBase-LR SFP+: IEEE Standard / Network topology:	Fiber 10Gigabit Ethernet, 10GBASE-LR (1310nM LAN PHY)			
10GBase-LR SFP+: Data Transfer Rate:	10.3125GBd			
10GBase-LR SFP+: Cables and Operating distance Up to:	Single-Mode: 10000m at 9um			
10GSFP+Cu : IEEE Standard / Network topology:	Copper 10Gigabit Ethernet, 10GSFP+Cu (Direct Attach) up to 7 meters			
1000Base-SX / 10GBase-SR SFP+: IEEE Standard / Network topology:	Fiber Gigabit Ethernet, 1000Base-SX (850nM LAN PHY) Fiber 10Gigabit Ethernet, 10GBASE-SR (850nM LAN PHY)			

1000Base-SX / 10GBase-SR SFP+: Data Transfer Rate :	10.3125GBd / 1.25GBd		
10000Base-SX / 10GBase-SR SFP+: Cables and Operating distance Up to:	50um, (OM3)2000MHz/Km >550m		
1000Base-LX / 10GBase-LR SFP+: IEEE Standard / Network topology:	Fiber Gigabit Ethernet, 1000Base-LX (1310nM LAN PHY) Fiber 10Gigabit Ethernet, 10GBASE-LR (1310nM LAN PHY).		
1000Base-LX / 10GBase-LR SFP+: Data Transfer Rate :	10.3125GBd / 1.25GBd		
1000Base-LX / 10GBase-LR SFP+: Cables and Operating distance Up to:	10000Base-LX: Single-Mode: 5000m at 9um 10GBase-LR: Single-Mode: 10000m at 9um		
Operating Systems Support			
Operating system support:	Windows Linux FreeBSD VMware		
General Technical Specifications			
Interface Standard:	OCP Mezzanine card 2.0 Design Specification 1.00 PCI-Express Base Specification Revision 3.0 (8GT/s)		
Mezzanine Form Factor	Open Compute Project Mezzanine Card		

Vertical Stack	Туре 1	
PCI Express Card Type:	PCI Express Gen 3.0 X8 lanes in Connector A	
PCI Express Voltage	+12V +- 8%	
PCI Connector:	FCI 61083-124402LF or equivalent mounted on the mezzanine card	
Controller:	Intel FTX710-BM2	
Operating Humidity:	0%–90%, non-condensing	
Operating Temperature:	0°C – 50°C (32°F – 122°F)	
Storage:	-20°C–65°C (-4°F–149°F)	
EMC Certifications:	FCC Part 15, Subpart B Class B Conducted Emissions Radiated Emissions CE EN 55022: 1998 Class B Amendments A1: 2000; A2: 2003 Conducted Emissions Radiated Emissions CE EN 55024: 1998 Amendments A1: 2000; A2: 2003 Immunity for ITE Amendment A1: 2001 CE EN 61000-3-2 2000, Class A Harmonic Current Emissions CE EN 61000 3-3 1995, Amendment A1: 2001 Voltage Fluctuations and Flicker CE IEC 6100-4-2: 1995 ESD Air Discharge 8kV. Contact Discharge 4kV. CE IEC 6100-4-3: 1995 Radiated Immunity (80-1000Mhz), 3V/m 80% A.M. by 1kHz CE IEC 6100-4-4: 1995 EFT/8: Immunity to electrical fast transients 1kV Power Leads, 0.5Kv Signals Leads CE IEC 6100-4-5: 1995 Immunity to conductive surges COM Mode; 2kV, Dif. Mode 1kV CE IEC 6100-4-6: 1996 Conducted immunity (0.15-80 MHz) 3VRMS 80% A.M. By 1kHz CE IEC 6100-4-11: 1994 Voltage Dips and Short Interruptions V reduc >95%, 30% >95% Duration 0.5per, 25per, 250per	

LEDs			
LEDs:	Port has 2 LEDs to indicate link status and speed.		
	Link: Physical link Speed: Green stay on – physical link on with 10G Speed		
	Yellow stay on – physical link on with 1G Speed		
	Off – physical link off.		
	Link /ACT: Logic Link/Activity, Green		
	Green stay on – logic link up, no activity		
	Green blinking – logic link up, activity		
	Off – logic link off		
LEDs location:	LED is located on the PCB Link/Act and Speed LED located below its own SFP connector port.		
Connectors:	(1) SFP+ cage		

Order Information

P/N	Description	Notes
OE310G1I71-XR	Single Port SFP+ 10 Gigabit Ethernet OCP Mezzanine Card	X8 Gen3 , Based on Intel X710BM2, Support Direct Attached Copper cable, Support Silicom SFP+ approved transceiver. RoHS compliant
OE310G1I71-SRD	Single Port Fiber (SX/SR) 1/10 Gigabit Ethernet OCP Mezzanine Card	X8 Gen3, Based on Intel X710BM2, on board support for Fiber SX/SR, RoHS compliant
OE310G1I71-LRD	Single Port Fiber (SX/SR) 1/10 Gigabit Ethernet OCP Mezzanine Card	X8 Gen3, Based on Intel X710BM2, on board support for Fiber LX/LR, RoHS compliant

Model P/N -E

-E: PXE enable

*Advanced features may required driver development. Specifications details the x710 chips capabilities

1V2