



PLL Product Specification

Model: PLL350-144(Y) **Rev:** P1 **Date:** 9/1/2006
Customer: SIRENZA MICRODEVICES, INC. **AppNote:** Non-Programmable
Operating Temperature Range: (-40 ° to 85 ° C)

Pb RoHS Compliant
To order models as RoHS Compliant add "Y" suffix to base model number.

Parameter	Min	Typ	Max	Units	X	Remarks
Frequency Range -		144		MHz	X	
Step Size -		1000		kHz	X	Fixed Frequency
Settling Time -		30		mSec		Power up to lock
Output Power -	0	3	6	dBm	X	
Output Phase Noise:						
5.5 kHz		-104	-98	dBc/Hz		
10 kHz		-110	-104	dBc/Hz		
100 kHz		-139	-133	dBc/Hz	X	
1000 kHz		-157	-151	dBc/Hz		
>=3000 kHz		-160	-154	dBc/Hz		
Integrated Phase Noise - .1-5.5 kHz		-63	-60	deg RMS		dB DSBN
Power Supply:						
VCO/CP	4.9	5	5.1	Volts	X	
PLL IC	2.9	3	3.1	Volts	X	
Supply Current:						
VCO/CP		35	50	mA	X	
PLL IC		20	25	mA		
Spurious Product:						
100 +/-5.5 kHz		-95	-82	dBc		
1000 +/-500 kHz		-90	-81	dBc	X	
>=3000 +/-500 kHz		-90	-81	dBc		
Reference Feedthrough -		-90	-81	dBc		
Harmonic Suppression:						
2nd Harmonic		-13	-10	dBc	X	
3rd Harmonic		-20	-15	dBc	X	
Ref Osc Signal:						
Frequency		52		MHz	X	
Amplitude	1		3.3	Vp-p		
Phase Noise - 1 kHz			-130	dBc/Hz		

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Parameter	Min	Typ	Max	Units	X	Remarks
Input Impedance		100		k Ω		
Output Impedance -		50		Ω		Nominal

Package Information

Package Type:	PLL350 (0.8 x 0.582 x 0.154 inches)	Drawing Number:	61054
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Comments

X Indicates parameter to be tested 100% in production

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