

LMT043DFFFWD-4

LCD Module User Manual

Prepared by:	Checked by:	Approved by:
Chen zhiguo	ŕ	
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Rev.	Descriptions	Release Date
0.1	Preliminary	2017-08-15

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1. General Specification

Screen Size(Diagonal): 4.3 inch

Resolution: 480(RGB) x 272
Signal Interface: 24bit parallel interface
Color Depth: 16.7M color (24bit) *1
Dot Pitch: 0.198 x 0.198 (mm)
Pixel Configuration: RGB Vertical Stripe

Display Mode: Transmissive / Positive (normal white)

Surface Treatment : Anti-Glare Treatment

Viewing Direction: 12 o'clock (Gray Scale Inversion Direction) (*2)

6 o'clock (*3)

Outline Dimension : 105.5 x 67.2x 2.9 (mm)

(exclude FPC, see attached drawing for details)

Active Area: 95.04 x 53.86 (mm)

Weight: 49.4g ST7282T2 Operating Temperature: $-20 \sim +70^{\circ}\text{C}$ Storage Temperature: $-30 \sim +80^{\circ}\text{C}$

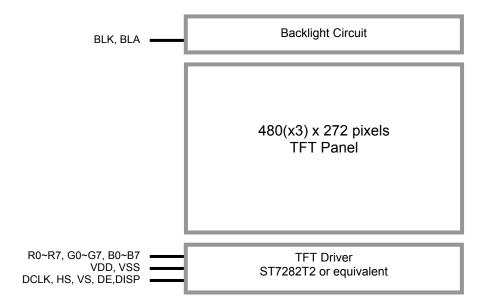
Note:

*1 Color tune may slightly changed by temperature and driving voltage.

*2 For saturated color display content (eg. pure-red, pure-green, pure-blue or pure-colors-combinations).

*3 For "color scales" display content.

2. Block Diagram



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3. Terminal Functions

3.1 Interface

1 BLK P Backlight LED Cathode supply 2 BLA P Backlight LED Anode supply 3 VSS P Power Ground (0V) 4 VDD P Positive Power Supply 5 R0 I Red color data input : : : 12 R7 13 G0 I Green color data input : : : 20 G7 21 B0 I Blue color data input : : : 28 B7 29 VSS P Power Ground (0V) 30 DCLK I Data clock signal input, falling edge trigger Display on/of control (internally pull low) DISP=0: standby mode DISP=1: normal operation 4 DISP=1: normal operation 4 Vertical Sync Signal Input, active low (If not using, pull high) 34 DE I Otata Clock Signal Input, active high (If not using, pull high) 35 NC - No connection, leave open 36 VSS P Power Ground (0V) No connection, leave open	Pin No.	Pin Name	1/0	Descriptions
3	1	BLK	Р	Backlight LED Cathode supply
VDD	2	BLA	Р	Backlight LED Anode supply
5 R0 I Red color data input : : : : 12 R7 I Green color data input : : : : 20 G7 I Blue color data input 21 B0 I Blue color data input : : : : 28 B7 29 VSS P Power Ground (0V) 30 DCLK I Data clock signal input, falling edge trigger 31 DISP I Display on/of control (internally pull low) 31 DISP I Informal clock signal input, falling edge trigger 31 DISP=0: standby mode DISP=0: standby mode DISP=1: normal operation Informal operation 32 HS I Horizontal Sync signal input, active low (If not using, pull high) 33 VS I Vertical Sync Signal Input, active high (If not using, pull low) 34 DE I Data Enable Signal Input, active	3	VSS	Р	Power Ground (0V)
Section Sect		VDD	Р	Positive Power Supply
12 R7 13 G0 I Green color data input : : : 20 G7 I Blue color data input : : : 28 B7 I Blue color data input 29 VSS P Power Ground (0V) 30 DCLK I Data clock signal input, falling edge trigger 31 DISP I Display on/of control (internally pull low) 31 DISP I DISP=0: standby mode DISP=1: normal operation I Horizontal Sync signal input, active low (If not using, pull high) 33 VS I Vertical Sync Signal Input, active low (If not using, pull high) 34 DE I Data Enable Signal Input, active high (If not using, pull low) 35 NC - No connection, leave open 36 VSS P Power Ground (0V) 37 NC - No connection, leave open 38 NC - No connection, leave open <td></td> <td>R0</td> <td>I</td> <td>Red color data input</td>		R0	I	Red color data input
Green color data input Green color data input		:		
E E E E E E E E E E E E E E E E E E E	12	R7		
21 B0 I Blue color data input : : : 28 B7 29 VSS P Power Ground (0V) 30 DCLK I Data clock signal input, falling edge trigger Display on/of control (internally pull low) DISP=0: standby mode DISP=1: normal operation DISP=1: normal operation 32 HS I Horizontal Sync signal input, active low (If not using, pull high) 33 VS I Vertical Sync Signal Input, active low (If not using, pull high) 34 DE I Data Enable Signal Input, active high (If not using, pull low) 35 NC - No connection, leave open 36 VSS P Power Ground (0V) 37 NC - No connection, leave open 38 NC 39 NC	13	G0	I	Green color data input
21 B0 I Blue color data input : : : 28 B7 29 VSS P Power Ground (0V) 30 DCLK I Data clock signal input, falling edge trigger Display on/of control (internally pull low) DISP=0: standby mode DISP=1: normal operation DISP=1: normal operation 32 HS I Horizontal Sync signal input, active low (If not using, pull high) 33 VS I Vertical Sync Signal Input, active low (If not using, pull high) 34 DE I Data Enable Signal Input, active high (If not using, pull low) 35 NC - No connection, leave open 36 VSS P Power Ground (0V) 37 NC - No connection, leave open 38 NC 39 NC	:	:		·
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29 VSS P Power Ground (0V) 30 DCLK I Data clock signal input, falling edge trigger 31 DISP Display on/of control (internally pull low) 31 DISP=0: standby mode DISP=1: normal operation 32 HS I Horizontal Sync signal input, active low (If not using, pull high) 33 VS I Vertical Sync Signal Input, active low (If not using, pull high) 34 DE I Jata Enable Signal Input, active high (If not using, pull low) 35 NC - 36 VSS P Power Ground (0V) 37 NC - 38 NC 39 NC	:	:		
Double Data clock signal input, falling edge trigger	28	B7		
Display on/of control (internally pull low) DISP=0: standby mode DISP=1: normal operation Horizontal Sync signal input, active low (If not using, pull high) Vertical Sync Signal Input, active low (If not using, pull high) Data Enable Signal Input, active high (If not using, pull low) No connection, leave open		VSS	Р	Power Ground (0V)
DISP	30	DCLK	I	Data clock signal input, falling edge trigger
S	31	DISP	I	DISP=0: standby mode
34 DE I (If not using, pull high) 34 DE I Data Enable Signal Input, active high (If not using, pull low) 35 NC - No connection, leave open 36 VSS P Power Ground (0V) 37 NC - No connection, leave open 38 NC - No connection, leave open	32	HS	I	(If not using, pull high)
34 DE I (If not using, pull low) 35 NC - No connection, leave open 36 VSS P Power Ground (0V) 37 NC - No connection, leave open 38 NC - No connection, leave open	33	VS	I	(If not using, pull high)
36 VSS P Power Ground (0V) 37 NC - No connection, leave open 38 NC 39 NC		DE	I	
37 NC 38 NC 39 NC No connection, leave open	35	_	-	No connection, leave open
38 NC 39 NC			Р	Power Ground (0V)
39 NC			-	No connection, leave open
	38	NC		
40 NC	39			
1 0 NO	40	NC		

Note:

The LMT043DFFFWD-4 both supports DE mode and Sync mode timing. SYNC mode, DE pull-lo, HSYNC and VSYNC for timing control DE mode, HSYNC and VSYNC pull-hi, DE for timing control

4. Absolute Maximum Ratings

Items	Symbol	Min.	Max.	Unit	Condition
Supply Voltage	V_{DD}	-0.3	+4.6	V	GND = 0V
Operating Temperature	T _{OP}	-20	+70	°C	No Condensation
Storage Temperature	T _{ST}	-30	+80	°C	No Condensation

Cautions:

Any Stresses exceeding the Absolute Maximum Ratings may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

5. Electrical Characteristics

5.1 DC Characteristics (MCU terminal)

VSS=0V, V_{DD} =3.3V, T_{OP} =25°C

Items	Symbol	MIN.	TYP.	MAX.	Unit	Applicable Pin
Operating Voltage	V_{DD}	3.0	3.3	3.6	V	VDD
Input High Voltage	V _{IH}	0.7VDD	-	VDD	V	Input pins
Input Low Voltage	V_{IL}	VSS	-	0.3VDD	V	Input pins
Frame Freq	F _{FRAME}	ı	60	ı	Hz	
Dot Data Clock	f _{DOTCLK}	ı	9.0	15	MHz	
Operating Current (*1)	I _{DD}	ı	19.3	1	mA	VDD
Standby Current (*2)	I _{DD}	ı	50	1	uA	VDD

Note.

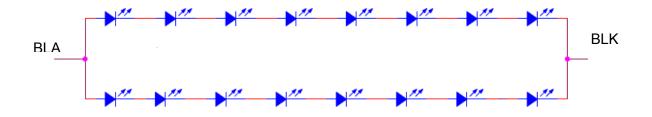
5.2 LED Backlight Circuit Characteristics

If_{BLA}=40mA, T_{OP}=25°C

Items	Symbol	MIN.	TYP.	MAX.	Unit	Note
Forward Voltage	Vf	-	25.6	-	V	
Forward Current	If _{BLA}	-	40		mA	
Life Time	-	10,000	(20,000)	-	hr	

Cautions:

Exceeding the recommended driving current could cause substantial damage to the backlight and shorten its lifetime.



No. of LEDs = 2x8 pcs

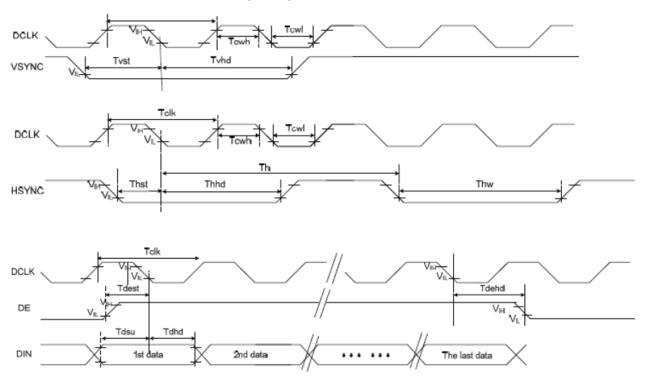
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^{*1.} test image is Black Mode, Frame Freq=60Hz

^{*2.} DISP=0

5.3 AC Characteristics

5.3.1 Clock and Date Input Timing Diagram



Timing Diagram

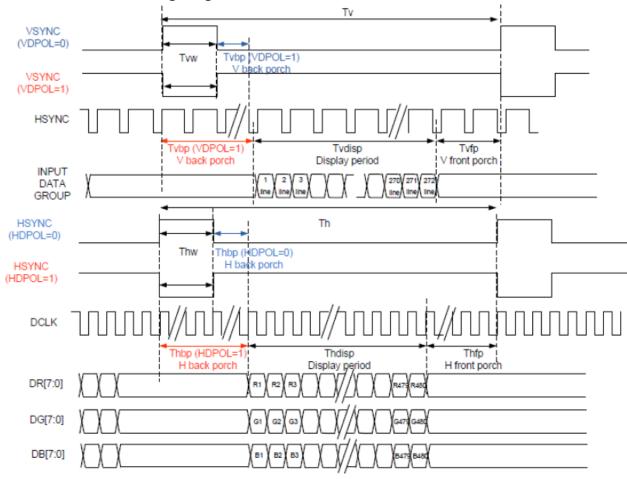
VDDI =3.3V, VDD=3.3V, AGND=0V, T_{OP} =25°C

ltem	Symbol	Min.	Тур.	Max.	Unit	Conditions
System operation timing						
VDD power source slew time	TPOR	-	-	20	ms	From 0V to 99% VDD
GRB pulse width	tRSTW	10	50		us	R=10Kohm, C=1uF
Input/ Output timing						
CLK pulse duty	Tew	40	50	60	%	
Hsync width	Thw	1			DCLK	
Hsync period	Th	55	60	65	us	
Vsync setup time	Tvst	12	-	-	ns	
Vsync hold time	Tvhd	12	-	-	ns	
Hsync setup time	Thst	12			ns	
Hsync hold time	Thhd	12			ns	
Data setup time	Tdsu	12	-	-	ns	
Data hold time	Tdhd	12	-	-	ns	
DE setup time	Tdest	12			ns	
DE hold time	Tdehd	12	٠	٠	ns	

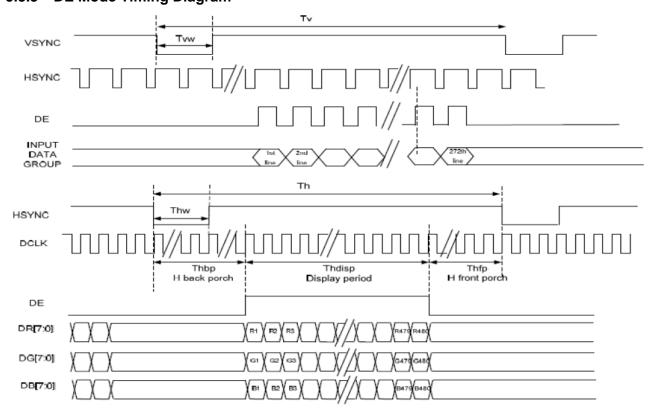
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5.3.2 SYNC Mode Timing Diagram



5.3.3 DE Mode Timing Diagram



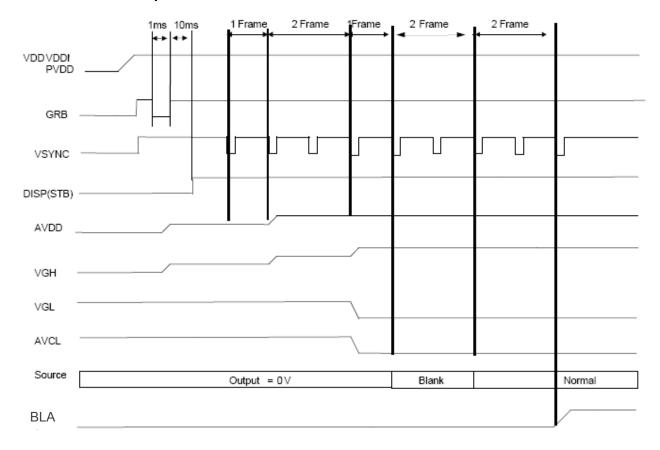
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	Item		Min.	Тур.	Max.	Unit	Remark
DCLK Free	quency	Fclk	8	9	12	MHz	
DCLK Peri	od	Tclk	83	111	125	ns	
HSYNC	Period Time	Th	485	525	532	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	50	DCLK	By H_Blanking setting
	Front Porch	Thfp	2	2	2	DCLK	
	Pulse Width	Thw	1	1	1	DCLK	
VSYNC	Period Time	Tv	275	285	303	Н	
	Display Period	Tvdisp		272		Н	
	Back Porch	Tvbp	2	12	30	Н	By V_Blanking setting
	Front Porch	Tvfp	1	1	1	Н	
	Pulse Width	Tvw	1	1	1	Н	

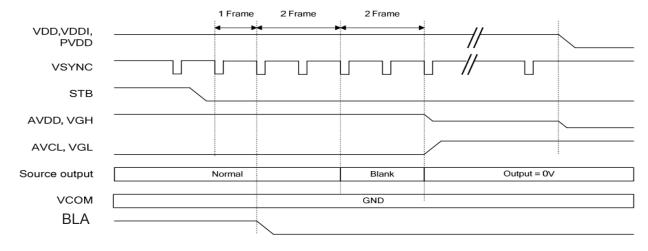
Note:it is necessary to keep Tvbp=12 and Thbp=43 in sync mode. DE mode is unnecessary to keep it

5.3.4 Power ON Sequence



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5.3.5 Power Off Sequence



6. Optical Characteristics

Item		Symbol	Condition	Min	Тур	Max	Unit	Remark
View Angles		θТ		60	70	-		
		θВ	CR ≥ 10	50	60	-	Degree	Note2,3
		θL		60	70	-		
				60	70	-		
Contrast Ratio		CR	θ =0°	600	800	-		Note 3
Response Time		Ton	25℃	-	25	35	ms	Note 4
neopeneo riine		Toff						
	White	х		0.257	0.307	0.357		Note 1,5
		у		0.280	0.330	0.380		,,,,
	Red	х	Backlight is on	0.532	0.582	0.632		Note 1,5
Chromaticity		у		0.299	0.349	0.399		
	Green	х		0.294	0.344	0.394		Note 1,5
	0.00	у		0.538	0.588	0.638		1,0
	Blue	х		0.101	0.151	0.201		Note 1,5
Bide		у		0.049	0.099	0.149		1,0
Uniformity	Uniformity			75	80	-	%	Note 6
NTSC	NTSC			45	50	-	%	Note 5
Luminance		L		715	885	-	cd/m²	Note 7

^{1.}IF= 40 mA, and the ambient temperature is 25° C.

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^{2.} The test systems refer to Note 1 and Note 2.

Note 1:

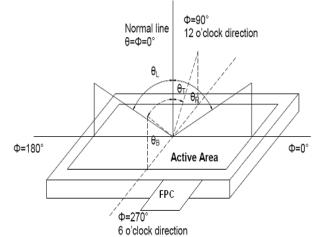
The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment SR-3A (1°) Measuring condition:

- Measuring surroundings: Dark room
- Measuring temperature: Ta=25℃.
- Adjust operating voltage to get optimum contrast at the center of the display.

Note 2:

The definition of viewing angle:

Refer to the graph below marked by θ and Φ



Note 3:

The definition of contrast ratio (Test LCM using SR-3A (1°)):

Contrast Ratio(CR) = Luminance When LCD is at "White" state
Luminance When LCD is at "Black" state
(Contrast Ratio is measured in optimum common electrode voltage)

Note 4:

Definition of Response time. (Test LCD using BM-7A(2°)):

The output signals of photo detector are measured

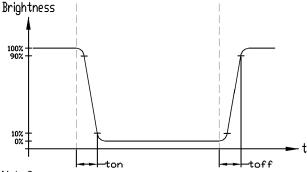
when the input signals are changed from

"black" to "white"(falling time)

and from "white" to "black" (rising time), respectively.

The response time is defined as

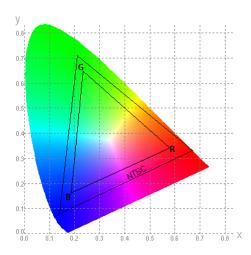
the time interval between the 10% and 90% of amplitudes.Refer to figure as below.



Note 5:

Definition of Color of CIE1931 Coordinate and NTSC Ratio.

Color gamut:

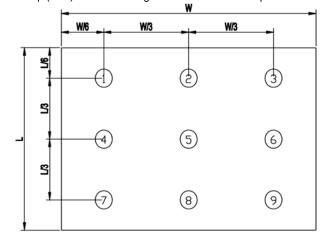


Note 6:

The luminance uniformity is calculated by using following formula. \triangle Bp = Bp (Min.) / Bp (Max.)×100 (%)

Bp (Max.) = Maximum brightness in 9 measured spots

Bp (Min.) = Minimum brightness in 9 measured spots.



Note 7: Measured the luminance of white state at center point

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7. Precautions of using LCD Modules

Mounting

- Mounting must use holes arranged in four corners or four sides.
- The mounting structure so provide even force on to LCD module. Uneven force (ex. Twisted stress) should not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- It is suggested to attach a transparent protective plate to the surface in order to protect the polarizer. It should have sufficient strength in order to the resist external force.
- The housing should adopt radiation structure to satisfy the temperature specification.
- Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. Never rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics deteriorate the polarizer.)
- When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer

Operating

- The spike noise causes the mis-operation of circuits. It should be within the ± 200 mV level (Over and under shoot voltage)
- Response time depends on the temperature.(In lower temperature, it becomes longer.)
- Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- When fixed patterns are displayed for a long time, remnant image is likely to occur.
- Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference

Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

Storage

When storing modules as spares for a long time, the following precautions are necessary.

- Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

Protection Film

- When the protection film is peeled off, static electricity is generated between the film and polarizer.
 This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt tore main on the polarizer. Please carefully peel off the protection film without rubbing it against the polarizer.
- When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Transportation

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The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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Appendix A < Inspection items and criteria for appearance defect> 8.

Items	Criteria								
Open Segment or Common	Not permitted	Not permitted							
Short	Not permitted	Not permitted							
Wrong Viewing Angle	Not permitted								
Decliners	Not permitted								
Contrast Ration Uneven	According to the limit	t specimen							
Crosstalk	According to the limit	t specimen							
White spots	X>1 pixel	A-area	Not permitted	Max 6 spots					
		B-area	Max. 1 allowed						
	1/2 pixel <x≤1 pixel<="" td=""><td>A-area</td><td>Not permitted</td><td>allowed</td></x≤1>	A-area	Not permitted	allowed					
		B-area	Max. 2 allowed						
	X≤1/2 pixel	A-area	Max. 1 allowed						
		B-area	Max. 4 allowed						
Black Sport	X>1 pixel	A-area	Not permitted						
		B-area	Max. 2 allowed						
	X≤1/2 pixel	A-area	Max. 1 allowed						
		B-area	Max. 4 allowed						
Line Defect	Apparent vertical hor	rizontal line d	efects are not pern	nitted					

Note:

- On Pixel include 3 dots (RedDot + GreenDot + BlueDot) Definition of Panel "A-area" and "B-area"
- 2.

