LINE MATCHING TRANSFORMER P3189

Features

* Lead-free (Pb-free)
* RoHS compliant
* 12.6mm (0.5") seated height
* Environmentally tested to IEC 68
* CERT reliability tested
* Industry Standard Pinout
* IEC 60950 and UL 60950 Certified
* UL Recognized Component
* High thermal stability

Applications

* Telecommunications
* Voice
* Least-cost routers
* V.22bis

DESCRIPTION

P3189 is designed as a lower cost replacement for industry standard P1200 and P3800 in applications where some relaxation of transformer signal distortion performance can be tolerated.

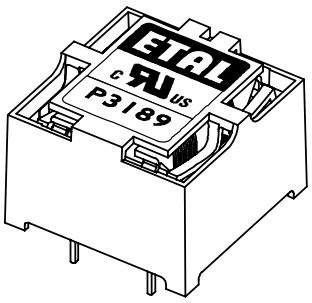
P3189 uses patented design and construction methods to achieve excellent signal performance and safety isolation to international standards. The part is completely lead-free, compliant with RoHS Directive 2002/95/EC, and suitable for lead-free and conventional processing.

P3189 is certified to IEC 60950 and UL 1950. P3189 is a UL Recognized Component and is supported an IEC CB Test Certificate.

P3189 is a rugged lightweight design that has been subjected to relevant environmental testing according to IEC 68 and Combined Environmental Reliability Testing (CERT) beyond normal operational levels and passed all tests, remaining fully functional.

Pb RoHS COMPLIANT

Patented



SPECIFICATIONS

Electrical

At T = 25ºC and as circuit fig. 2 unless otherwise stated.

Parameter	Conditions	Min	Typ	Max	Units
Insertion Loss	f = 2kHz, R_L = 560Ω	-	-	1.5	dB
Frequency response	200Hz - 4kHz	-	-	±0.2	dB
Return Loss	200Hz - 4kHz	18	-	-	dB
Third Harmonic Distortion (1)	300Hz - 6dBm 697Hz - 6dBm	- -	- -	-62 -70	dBm dBm
Voltage isolation (2)	50Hz DC	2.12 3.0	- -	- -	kVrms kV
Operating range: Functional Storage	Ambient temperature	 0 -40	 - -	 +70 +85	ºC ºC

Lumped equivalent circuit parameters as Fig. 1

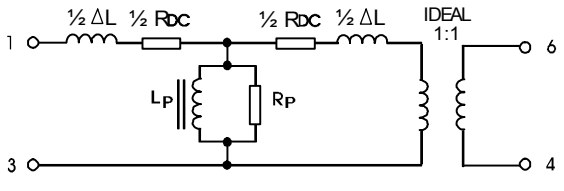
DC resistance, R_{DC} (3)	Sum of windings	124	-	152	Ω
Leakage inductance ΔL		14.5	-	18	mH
Shunt inductance Lp (4)	10mV 200Hz	1.4	-	-	H
Shunt loss Rp (4)	10mV 200Hz	4	-	-	kΩ

Notes

1. Third harmonic typically exceeds other harmonics by 20dB.
2. Components are 100% tested at 3.25kV DC.
3. Caution: do not pass DC through windings. Telephone line current, etc. must be diverted using choke or semiconductor line hold circuit.
4. At signal levels greater than 100mV, Lp will increase and Rp will decrease slightly but the effect is usually favourable to the return loss characteristic.

Equivalent Circuit

Fig. 1



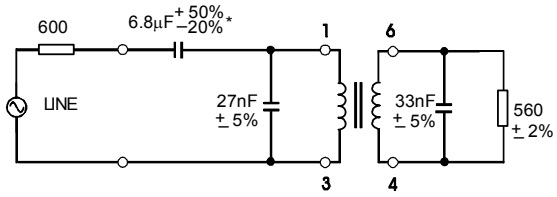
Patented

MATCHING RECOMMENDATIONS

600Ω MATCH

Recommended Circuit

Fig. 2



* If return loss > 16dB, 300Hz - 4kHz is acceptable, blocking capacitor may be relaxed to 4.7µF +50% −20%

EUROPEAN CTR21 COMPLEX MATCH

Recommended Circuit

Fig. 3

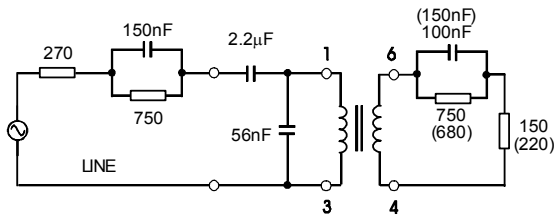
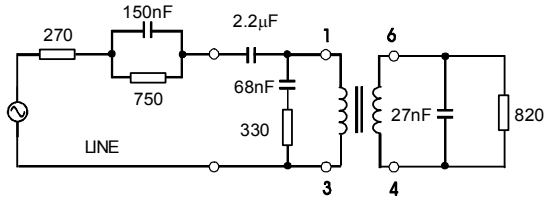


Figure 3 gives flat RX and TX responses against the CTR21 complex reference impedance (typically around ±0.5dB 300Hz - 3.4kHz). Return loss is typically better than 20dB. The circuit values in parentheses give optimum flatness at a small sacrifice in return loss.

For circuits with existing board drillings, figure 4 gives good return loss (>20dB) and RX flatness against the CTR21 complex reference impedance, but TX flatness is degraded by the use of this topology.

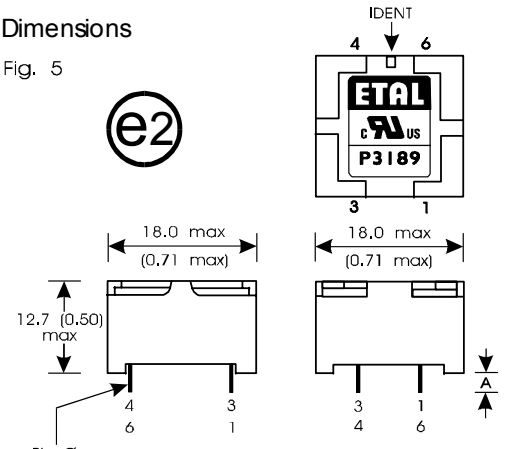
Fig. 4



CONSTRUCTION

Dimensions

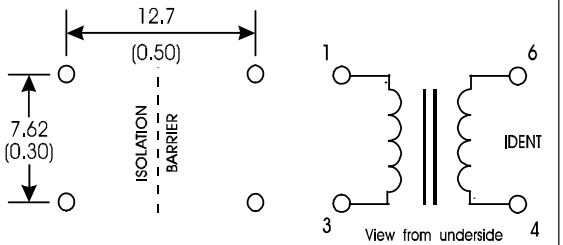
Fig. 5



NOTES:
Dimension "A" : 3.0 to 3.5 mm (0.118 - 0.138)

Connections

Fig. 6



Tolerance ±0.3mm (±0.012inch)
Recommended PCB hole size 1mmØ(0.04inch)

Dimensions shown are in millimetres (inches).
Geometric centres of outline and pin grid coincide within a tolerance circle of 0.6mm
Windings may be used interchangeably as primary or secondary.

ENVIRONMENTAL TESTING

Reliability testing to IEC 68

Tested to clauses of IEC 68 and compliant with all functional and safety requirements following exposure as follows:

Test description	IEC 68 reference	Test details	Result
Robustness of terminations	68-2-21 Test Ua_1 68-2-21 Test Ua_2	Tensile 5N pull Thrust 1N push	No impairment No pin detachment or distortion
Solderability	68-2-20 Test Ta Method 1	Solder bath 235ºC 2s	No impairment Finish smooth, bright and even
Resistance to soldering heat	68-2-20 Test Tb Method 1A 68-2-20 Test Tb Method 2	Solder bath 260ºC 10s Soldering iron 350ºC 5s	
Vibration	68-2-6 Test Fc	Sweep 10-55-10Hz in 1 minute Amplitude 1.5mm pk-pk Duration 2h per axis, 3 axes	No impairment
Shock	68-2-27 Test Ea	Peak acceleration 1000m/s^2 Duration of pulse 6ms 3 shocks each direction on 3 axes	No impairment
Cold	68-2-1 Test Ab	-25ºC 16h Recovery to ambient 1-2h	No impairment
Dry heat	68-2-2 Test Bb	125ºC 16h Recovery to ambient 1-2h	No impairment
Damp heat	68-2-3 Test Ca	40ºC 4 days, RH 93% Recovery to ambient 1-2h	No impairment
Change of temperature	68-2-14 Test Na	T_A –25ºC T_B +85ºC t_1 30 min 2 min ≤t_2≤3 min Recovery to ambient 1-2h 5 cycles	No impairment

Combined Environmental Reliability Testing (CERT)

Components step stressed at increasing levels of severity using combined stresses to detect potential weaknesses. Results are shown for highest levels of stress tested. Compliant with all functional and safety tests following exposure as follows:

Test description	Test details	Duration	Result
Storage Test	Thermal cycling -30ºC to +100ºC at 11ºC/min 6mm pk 2-9Hz at 1 octave/min 20m/s^2 9-200Hz	20 mins per plane	No impairment
Transportation Test	Thermal cycling -65ºC to +80ºC Random vibration 10-200Hz and 200-2000Hz at 57m/s^2 RMS	2 hours per plane	No impairment

SAFETY

Constructed in accordance with IEC 60950-1, EN 60950-1 and UL 60950, supplementary insulation, 250Vrms maximum working voltage, flammability class V-0.

There are no special installation requirements (beyond attending to usual PCB track separations) since the integral cover provides supplementary insulation from its external faces to internal core and windings.

CERTIFICATION

Certified under the IEC CB scheme (Certificate DK-8570) to IEC 60950-1-2001, sub-clauses 1.5, 1.5.1, 1.5.2, 1.7.1, 2.9, 2.9.1, 2.9.2, 2.9.3, 2.10, 2.10.1, 2.10.2, 2.10.3, 2.10.3.1, 2.10.3.3, 2.10.4, 2.10.5, 2.10.5.1, 2.10.5.2, 2.10.5.4, 4.7, 4.7.1 (classV-1), 4.7.3, 4.7.3.1, 4.7.3.4, 5.2, 5.2.1, 5.2.2, 6.1.2.1 (Finland, Norway, Sweden national deviations) for a maximum working voltage of 250Vrms, nominal mains supply voltage not exceeding 300Vrms and a maximum operating temperature of 70ºC in Pollution Degree 2 environments.

Recognized under the Component Recognition Program of Underwriters Laboratories Inc. to US and Canadian requirements CAN/CSA C22.2 No. 60950-1-03/UL60950-1, First Edition, based on IEC 60950-1, First Edition, maximum working voltage 250Vrms, Pollution Degree 2, supplementary insulation.

UL File number E203175.

Additionally, Profec Technologies certifies all transformers as providing voltage isolation of 2.12kVrms, 3kV DC minimum. All shipments are supported by a certificate of conformity to current applicable safety standards.

ABSOLUTE MAXIMUM RATINGS

(Ratings of components independent of circuit).

Short term isolation voltage (1s)	2.12kVrms 3.0kVDC
DC current	100μA
Storage temperature	-40ºC to +85ºC
Lead temperature, 10s	260ºC

INTELLECTUAL PROPERTY RIGHTS

ETAL, P1200, P3189, and P3800 are Trade Marks of Profec Technologies Ltd.
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P3189 design and construction are protected by patents and registered design.
UK Registered Design No. 2077360.
US Patent No. 6,690,254
French Registered Design No. 991512.
United States Registered Design 426, 815.
Mexico Registered Design 12143.
Other patents and registered designs pending.

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ISO 9001
FM 25326

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