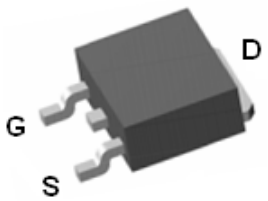
UNIKC Semiconductor

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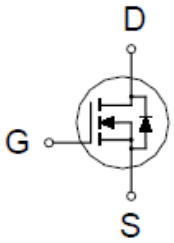
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
150V	90mΩ @V_{GS} = 10V	18A



TO-252



ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current	T_C = 25 °C	I_D	18	A
	T_C = 100 °C		11	
Pulsed Drain Current[1]		I_{DM}	37	
Avalanche Current		I_{AS}	8	
Avalanche Energy	L = 1mH	E_{AS}	32	mJ
Power Dissipation	T_C = 25 °C	P_D	56	W
	T_C = 100 °C		22.7	
Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.8	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

[1]Pulse width limited by maximum junction temperature.

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RoHS Compliant

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N-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	150			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 120V, V_{GS} = 0V			1	μA
		V_{DS} = 100V, V_{GS} = 0V , T_J = 125 °C			10	
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 4.5V, I_D = 10A		80	110	mΩ
		V_{GS} = 10V, I_D = 10A		76	90	
Forward Transconductance[1]	g_{fs}	V_{DS} = 5V, I_D = 10A		35		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		1119		pF
Output Capacitance	C_{oss}			106		
Reverse Transfer Capacitance	C_{rss}			50		
Gate Resistance	R_g	V_{GS} = 0V, V_{DS} = 0V, f = 1MHz		0.9		Ω
Total Gate Charge[2]	Q_g(V_{GS} = 10V)	V_{DS} = 75V, V_{GS} = 10V, I_D = 10A		24		nC
	Q_g(V_{GS} = 4.5V)			13		
Gate-Source Charge[2]	Q_{gs}			3.3		
Gate-Drain Charge[2]	Q_{gd}			7		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DS} = 75V, I_D ≅ 10A, V_{GS} = 10V, R_{GEN} = 6Ω		21		nS
Rise Time[2]	t_r			34		
Turn-Off Delay Time[2]	$t_{d(off)}$			30		
Fall Time[2]	t_f			22		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I_S				15	A
Forward Voltage[1]	V_{SD}	I_F = 10A, V_{GS} = 0V			1.2	V
Reverse Recovery Time	t_{rr}	I_F = 10A, dI_F/dt = 100A / μS		55		nS
Reverse Recovery Charge	Q_{rr}			96		nC

[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

[2]Independent of operating temperature.

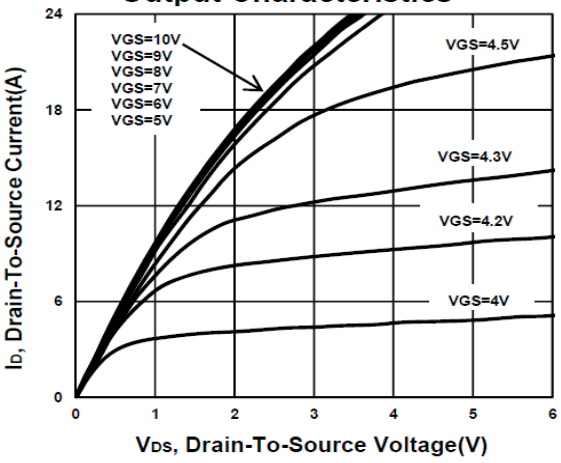
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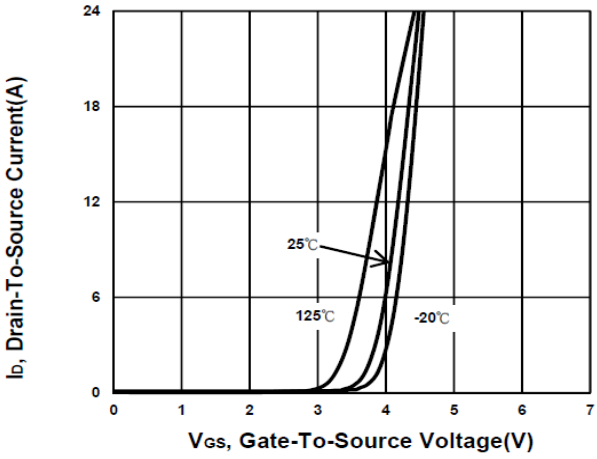
P9515BD

N-Channel Enhancement Mode MOSFET

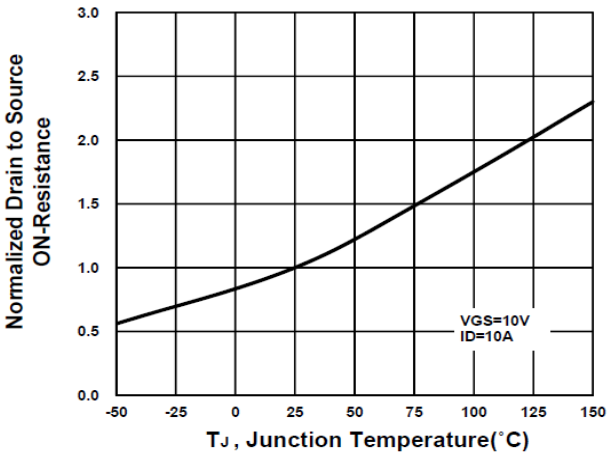
Output Characteristics



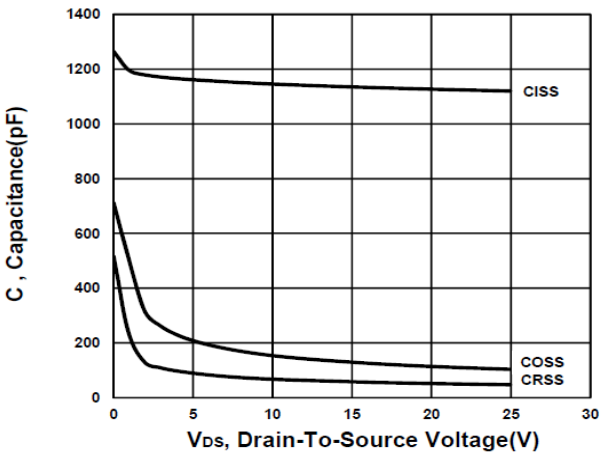
Transfer Characteristics



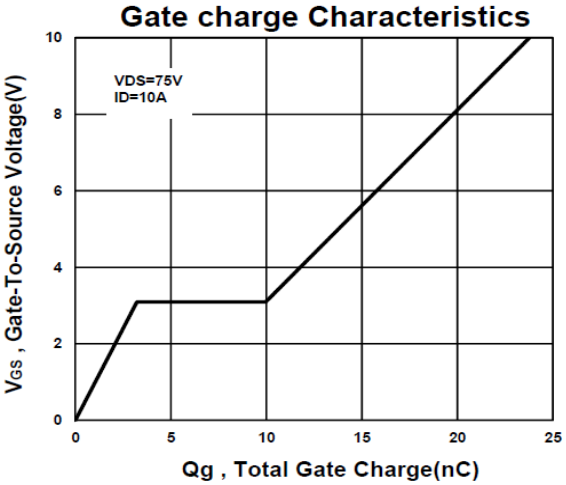
On-Resistance VS Temperature



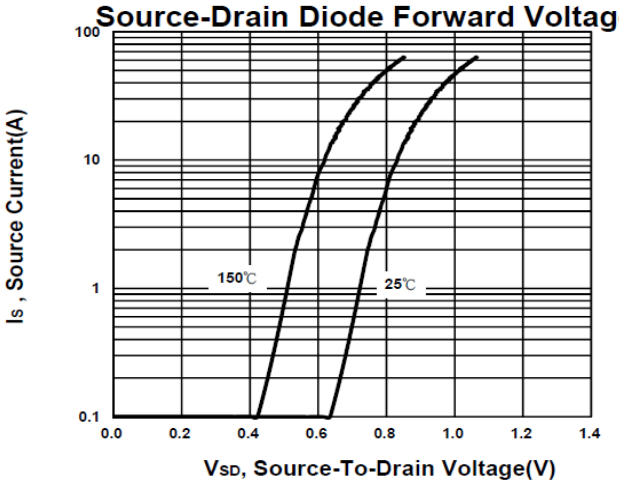
Capacitance Characteristic



Gate charge Characteristics



Source-Drain Diode Forward Voltage



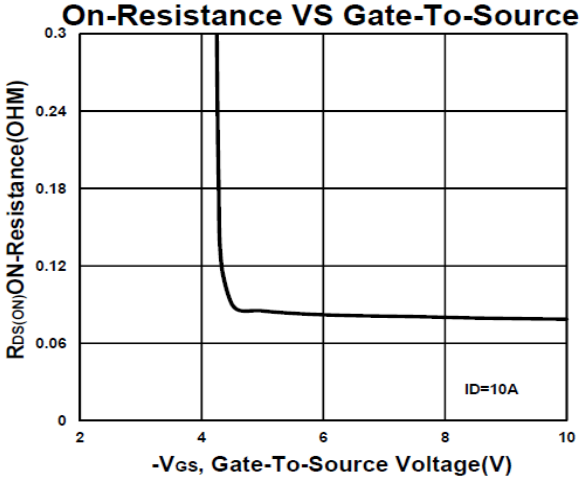
UNIKC Semiconductor

RoHS Compliant

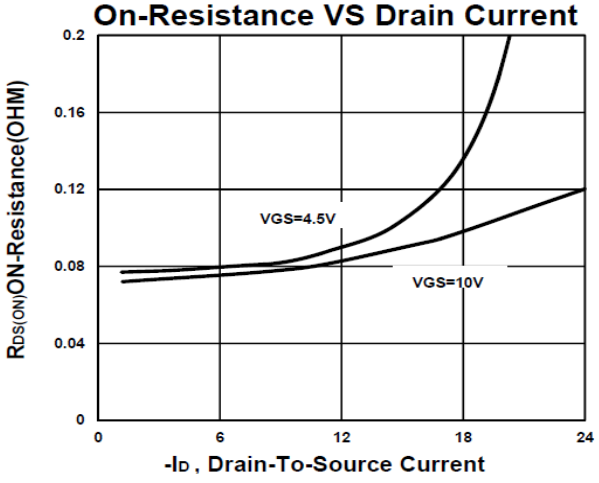
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N-Channel Enhancement Mode MOSFET

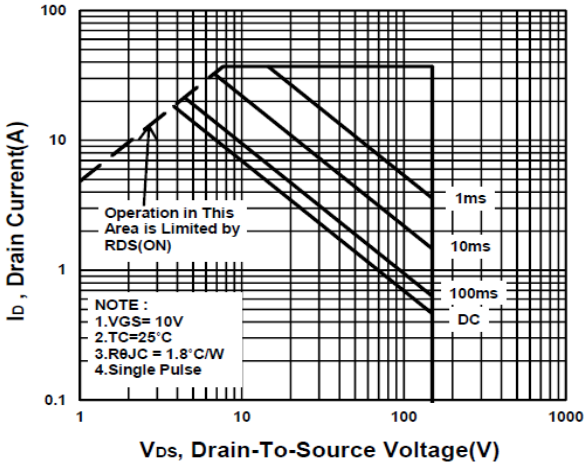
On-Resistance VS Gate-To-Source



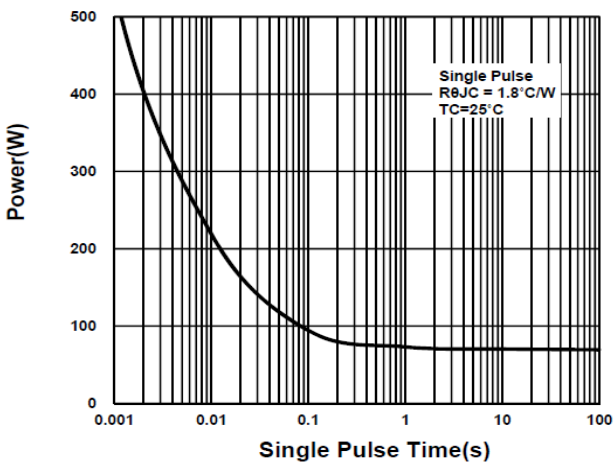
On-Resistance VS Drain Current



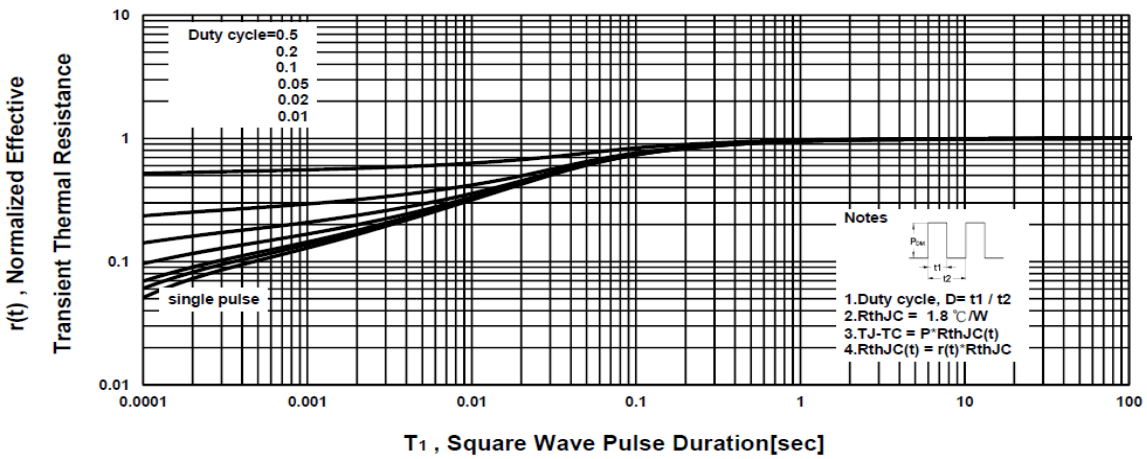
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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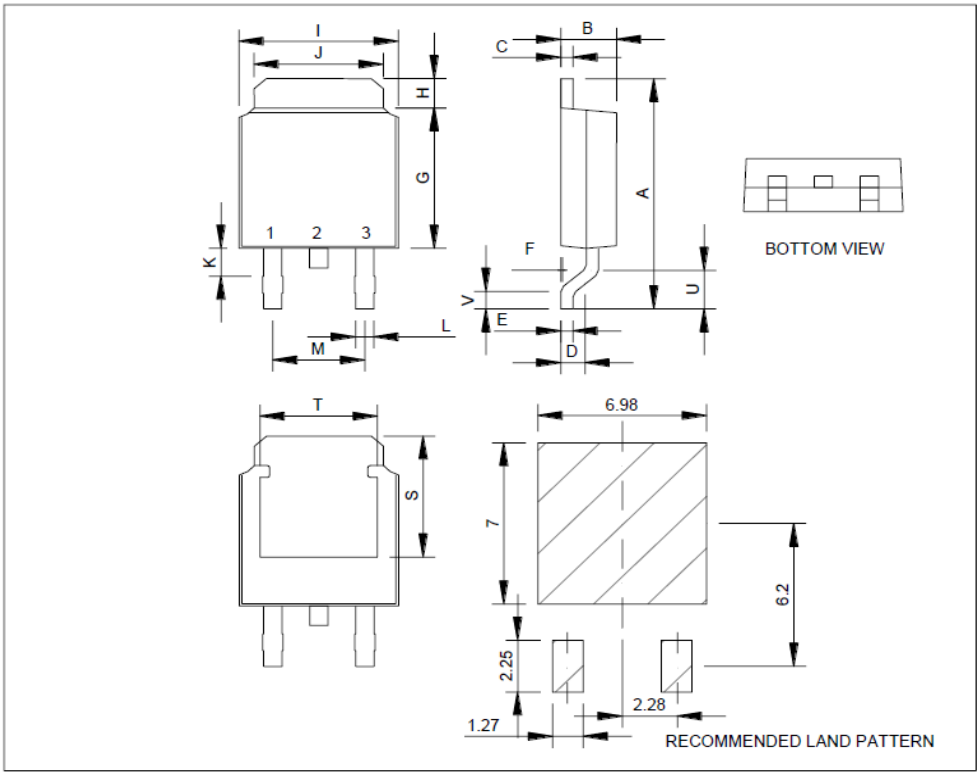
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N-Channel Enhancement Mode MOSFET

Package Dimension

TO-252 (DPAK) MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	8.9	10	10.41	J	4.8		5.64
B	2.1	2.2	2.4	K	0.15		1.1
C	0.4	0.5	0.61	L	0.4	0.76	0.89
D	0.82	1.2	1.5	M	4.2	4.58	5
E	0.4	0.5	0.61	S	4.9	5.1	5.3
F	0		0.2	T	4.6	4.75	5.44
G	5.3	6.1	6.3	U	1.4		1.78
H	0.9		1.7	V	0.55	1.25	1.7
I	6.3	6.5	6.8				



*因为各家封装模具不同而外观略有所差异，不影响电性及Layout。

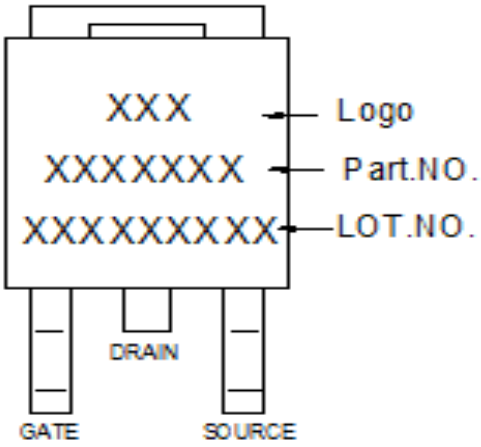
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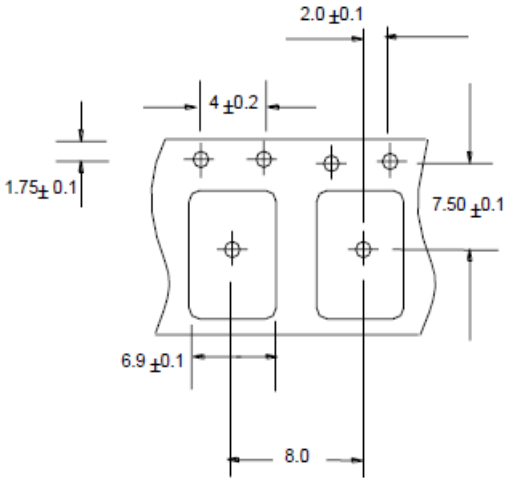
N-Channel Enhancement Mode MOSFET

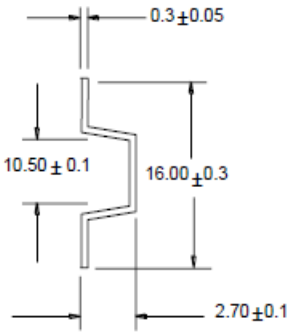
A. Marking Information

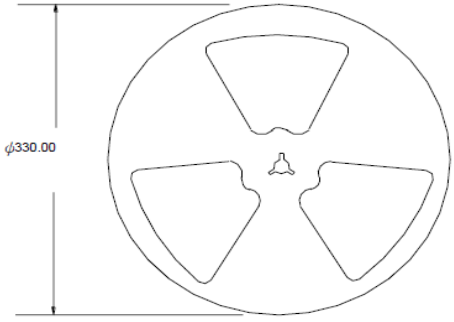


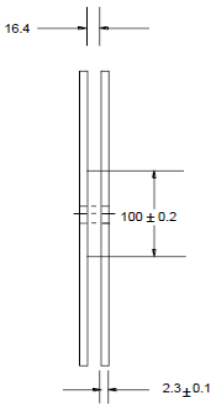
B. Tape&Reel Information:2500pcs/Reel





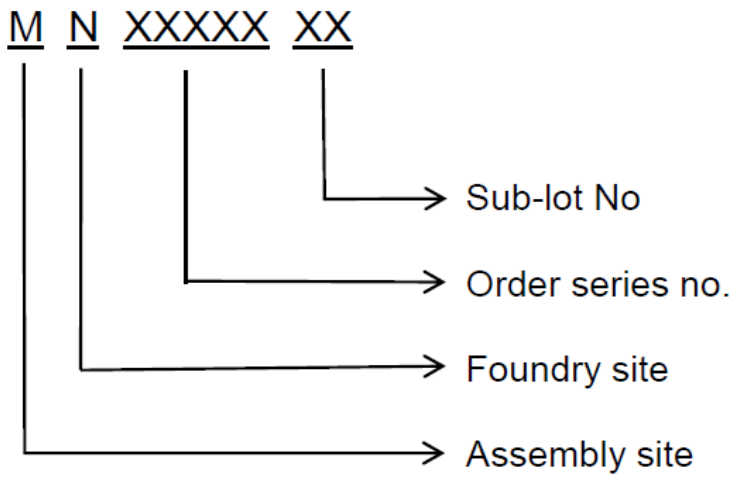




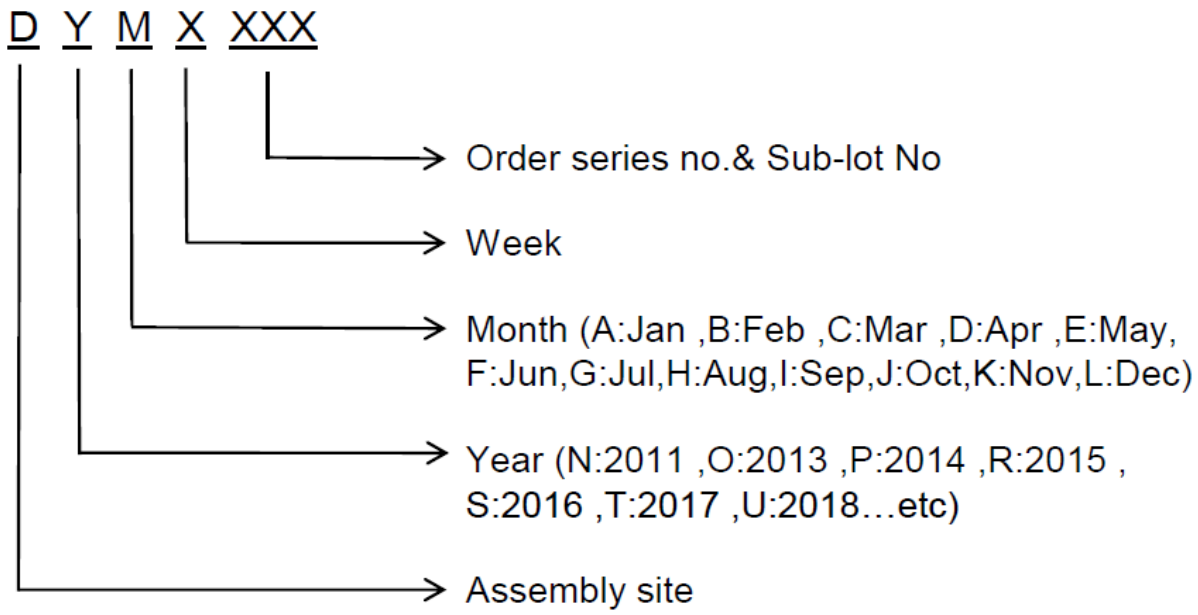


C. Lot No.&Date Code rule

1.Lot No.



2.Date Code



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N-Channel Enhancement Mode MOSFET

D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文”O”和数字”0”，”G和”Q”的字型即可)
3	U-NIKC	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	RoHS label	RoHS long axis: 12 mm minor axis:6 mm bottom color: White Font color: Black Font style: Arial
11	Halogen Free label	G Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial
12	Scan information	Device / Lot / D/C / QTY , Insert “ / “ between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least